# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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# Contact us

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# **UNR51A4G**

### Silicon PNP epitaxial planar type

For digital circuits

### Features

- Costs can be reduced through downsizing of the equipment and reduction of the number of parts.
- SMini type package allowing easy automatic insertion through tape packing

Absolute Maximum Ratings $T_a = 25^{\circ}C$							
Parameter	Symbol	Rating	Unit				
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	-50	V				
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	-50	V				
Collector current	I <sub>C</sub>	-80	mA				
Total power dissipation	P <sub>T</sub>	150	mW				
Junction temperature	Tj	150	°C				
Storage temperature	T <sub>stg</sub>	-55 to +150	°C				



(47 kΩ)

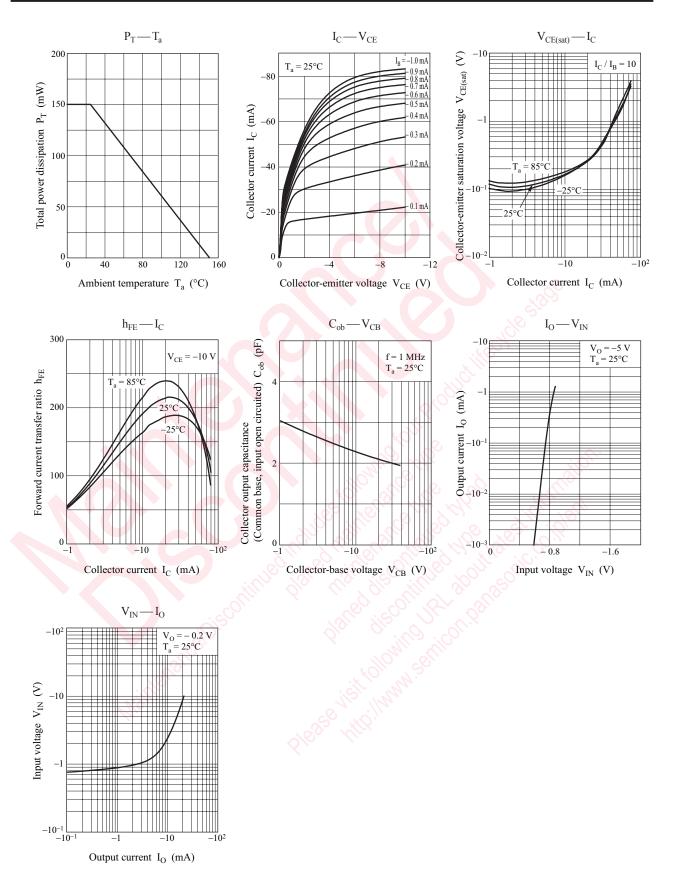
### Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_{\rm C} = -10 \mu {\rm A}, I_{\rm E} = 0$	-50	Su.		V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_{\rm C} = -2 \text{ mA}, I_{\rm B} = 0$	-50			V
Collector-base cutoff current (Emitter open)	ICBO	$V_{CB} = -50 \text{ V}, I_E = 0$	$\dot{\mathcal{S}}_{0}$		- 0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = -50 \text{ V}, I_B = 0$			- 0.5	μΑ
Emitter-base cutoff current (Collector open)	I <sub>EBO</sub>	$V_{\rm EB} = -6 \text{ V}, I_{\rm C} = 0$			- 0.2	mA
Forward current transfer ratio	h <sub>FE</sub>	$V_{\rm CE} = -10 \text{ V}, I_{\rm C} = -5 \text{ mA}$	80			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{\rm C} = -10 \text{ mA}, I_{\rm B} = -0.3 \text{ mA}$			- 0.25	V
Output voltage high-level	V <sub>OH</sub>	$V_{CC} = -5 \text{ V}, V_B = -0.5 \text{ V}, R_L = 1 \text{ k}\Omega$	-4.9			V
Output voltage low-level	V <sub>OL</sub>	$V_{\rm CC} = -5 \text{ V}, V_{\rm B} = -2.5 \text{ V}, R_{\rm L} = 1 \text{ k}\Omega$			- 0.2	V
Input resistance	R <sub>1</sub>		-30%	10	+30%	kΩ
Resistance ratio	$R_1 / R_2$		0.17	0.21	0.25	
Transition frequency	$f_T$	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$		80		MHz

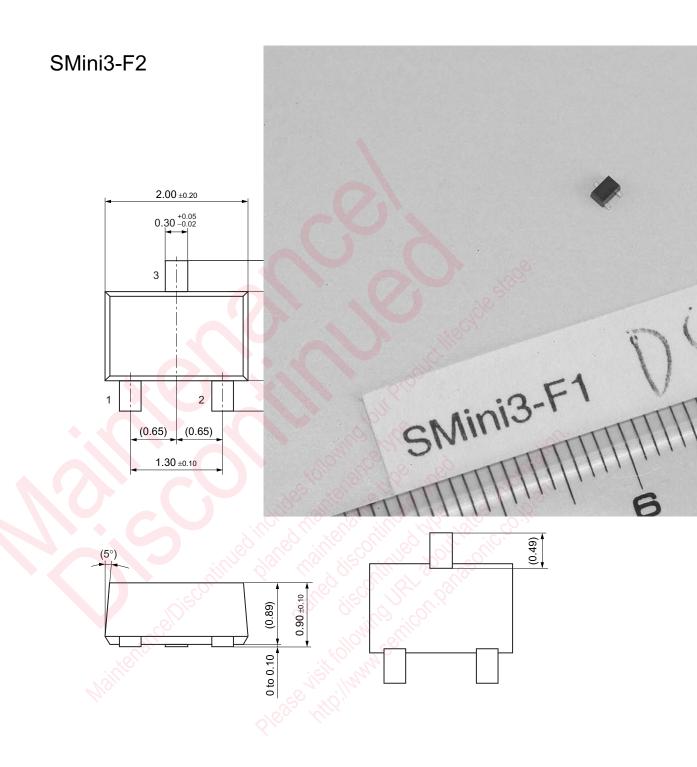
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

### UNR51A4G

### **Panasonic**



## **Panasonic**



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