

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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UNR51ALG

Silicon PNP epitaxial planar type

For digital circuits

■ Features

- Costs can be reduced through downsizing of the equipment and reduction of the number of parts.
- SMini type package allowing easy automatic insertion through tape packing

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V_{CBO}	-50	V	
Collector-emitter voltage (Base open)	V _{CEO}	-50	V	
Collector current	I_{C}	-80	mA	
Total power dissipation	P_{T}	150	mW	
Junction temperature	T _j	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

■ Package

- Code
 - SMini3-F2
- Pin Name 1: Base
 - 2: Emitter
 - 3: Collector
- Marking Symbol: ED
- Internal Connection

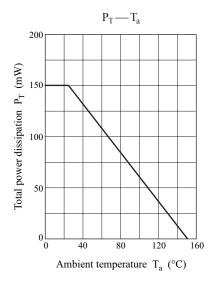
$$\begin{array}{c|c} R_1 \ (4.7 \ k\Omega) \\ B \circ \longrightarrow W \\ \hline R_2 \\ (4.7 \ k\Omega) \end{array} \sim C$$

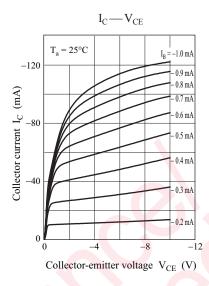
■ Electrical Characteristics $T_a = 25$ °C±3°C

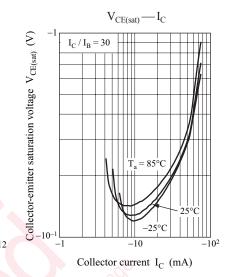
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = -10 \mu{\rm A}, I_{\rm E} = 0$	-50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = -2 \text{ mA}, I_B = 0$	-50			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$	60.		- 0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = -50 \text{ V}, I_{B} = 0$			- 0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{EB} = -6 \text{ V}, I_C = 0$			-2.0	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	20			_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = -10 \text{ mA}, I_B = -0.3 \text{ mA}$			- 0.25	V
Output voltage high-level	V_{OH}	$V_{CC} = -5 \text{ V}, V_{B} = -0.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$	-4.9			V
Output voltage low-level	V _{OL}	$V_{CC} = -5 \text{ V}, V_{B} = -2.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$			- 0.2	V
Input resistance	R ₁		-30%	4.7	+30%	kΩ
Resistance ratio	R_1/R_2		0.8	1	1.2	_
Transition frequency	f_T	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$		80		MHz

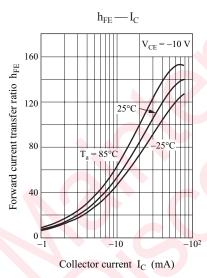
 $Note) \quad \text{Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS~C~7030~measuring~methods~for~transistors.}$

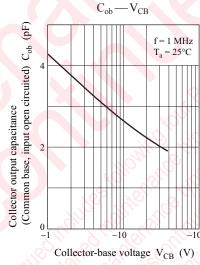
UNR51ALG Panasonic

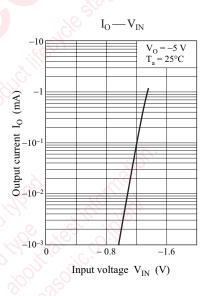


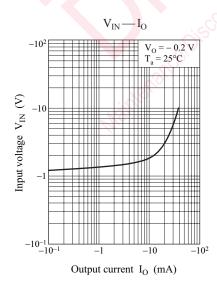




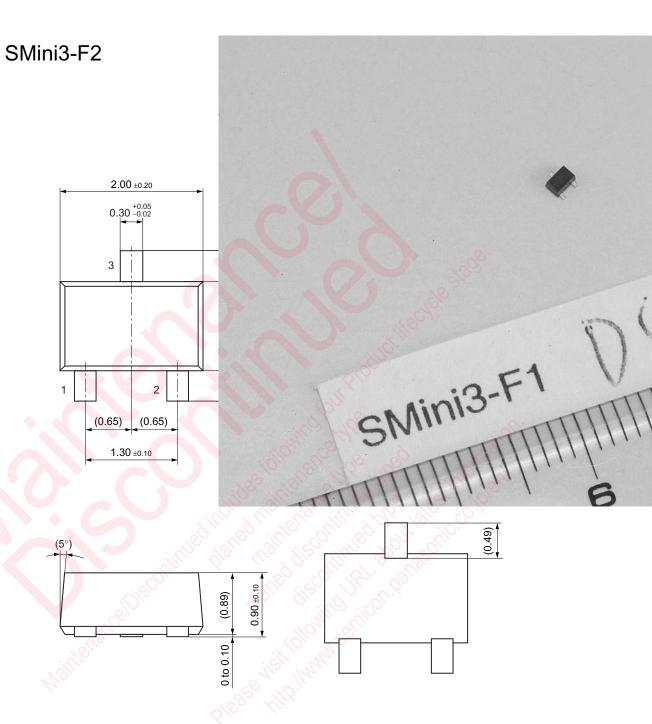








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