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# **UNR92A0G**

# Silicon NPN epitaxial planar type

## For digital circuits

#### ■ Features

- Optimum for high-density mounting and downsizing of the equipment
- Contribute to low power consumption

## ■ Absolute Maximum Ratings $T_a = 25$ °C

| Parameter                             | Symbol           | Rating      | Unit |  |
|---------------------------------------|------------------|-------------|------|--|
| Collector-base voltage (Emitter open) | V <sub>CBO</sub> | 50          | V    |  |
| Collector-emitter voltage (Base open) | V <sub>CEO</sub> | 50          | V    |  |
| Collector current                     | $I_{C}$          | 80          | mA   |  |
| Total power dissipation               | P <sub>T</sub>   | 125         | mW   |  |
| Junction temperature                  | $T_j$            | 125         | °C   |  |
| Storage temperature                   | T <sub>stg</sub> | -55 to +125 | °C   |  |

#### Package

- Code
  - SSMini3-F3
- Pin Name
  - 1: Base
  - 2: Emitter
- 3: Collector

## ■ Marking Symbol: KT

#### ■ Internal Connection

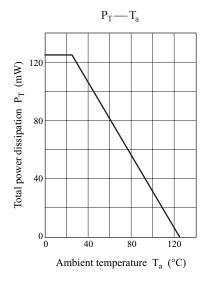
$$B \circ \longrightarrow K_1 \circ C$$

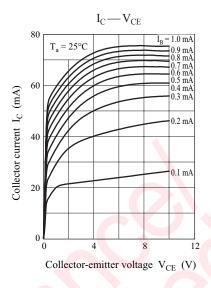
## ■ Electrical Characteristics T<sub>a</sub> = 25°C±3°C

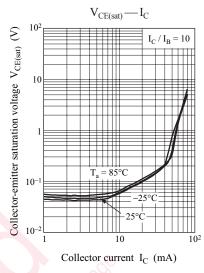
| Parameter                                    | Symbol               | Conditions   | Min  | Тур | Max  | Unit |
|--|----------------------|--|------|-----|------|------|
| Collector-base voltage (Emitter open)        | V <sub>CBO</sub>     | $I_C = 10 \mu\text{A}, I_E = 0$  | 50   |     |      | V    |
| Collector-emitter voltage (Base open)        | V <sub>CEO</sub>     | $I_{\rm C} = 2  \text{mA}, I_{\rm B} = 0$                                | 50   |     |      | V    |
| Collector-base cutoff current (Emitter open) | I <sub>CBO</sub>     | $V_{CB} = 50 \text{ V}, I_{E} = 0$                                       |      |     | 0.1  | μA   |
| Collector-emitter cutoff current (Base open) | I <sub>CEO</sub>     | $V_{CE} = 50 \text{ V}, I_{B} = 0$                                       |      |     | 0.5  | μA   |
| Emitter-base cutoff current (Collector open) | I <sub>EBO</sub>     | $V_{EB} = 6 \text{ V, } I_C = 0$   |      |     | 0.01 | mA   |
| Forward current transfer ratio               | $h_{FE}$             | $V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$                            | 160  |     | 460  |      |
| Collector-emitter saturation voltage         | V <sub>CE(sat)</sub> | $I_C = 10 \text{ mA}, I_B = 0.3 \text{ mA}$                              |      |     | 0.25 | V    |
| Output voltage high-level                    | V <sub>OH</sub>      | $V_{CC} = 5 \text{ V}, V_{B} = 0.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$ | 4.9  |     |      | V    |
| Output voltage low-level                     | V <sub>OL</sub>      | $V_{CC} = 5 \text{ V}, V_{B} = 2.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$ |      |     | 0.2  | V    |
| Input resistance                             | R <sub>1</sub>       |  | -30% | 47  | +30% | kΩ   |
| Transition frequency                         | $f_T$                | $V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$        |      | 150 |      | MHz  |

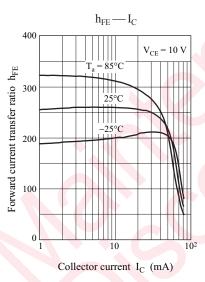
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

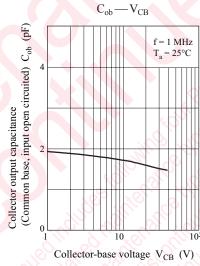
UNR92A0G Panasonic

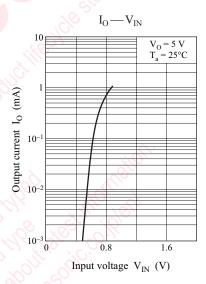


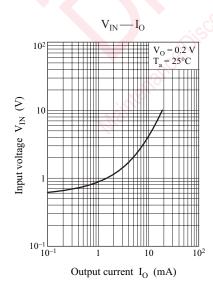








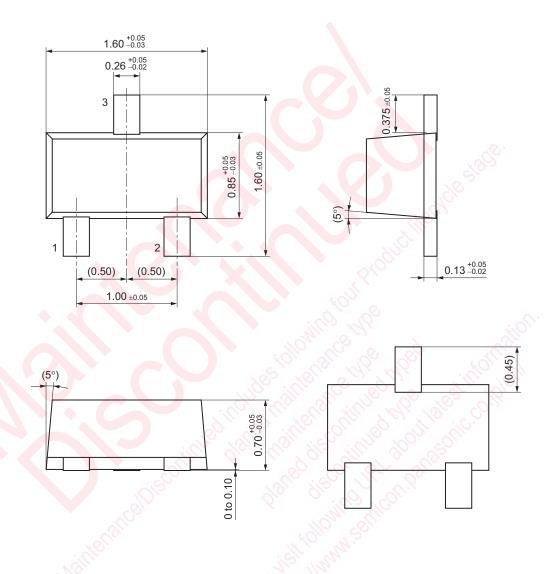




2 SJH00242AED

Panasonic UNR92A0G

SSMini3-F3 Unit: mm



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