## imall

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## UP04213G

Silicon NPN epitaxial planar type

#### For switching/digital circuits

#### Features

- Two elements incorporated into one package (Transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half

#### Basic Part Number

• UNR2210  $\times$  2

#### Absolute Maximum Ratings $T_a = 25^{\circ}C$

| Parameter                             | Symbol           | Rating      | Unit | 7 |
|---------------------------------------|------------------|-------------|------|---|
| Collector-base voltage (Emitter open) | V <sub>CBO</sub> | 50          | V    |   |
| Collector-emitter voltage (Base open) | V <sub>CEO</sub> | 50          | V    | - |
| Collector current                     | I <sub>C</sub>   | 100         | mA   |   |
| Total power dissipation               | P <sub>T</sub>   | 125         | mW   |   |
| Junction temperature                  | Tj               | 125         | °C   |   |
| Storage temperature                   | T <sub>stg</sub> | -55 to +125 | °C   |   |

# Package Code SSMini6-F2

#### Pin Name

- 1: Emitter (Tr1)4: Emitter (Tr2)2: Base (Tr1)5: Base (Tr2)3: Collector (Tr2)6: Collector (Tr1)
- Marking Symbol: 8S

# 

(B1)

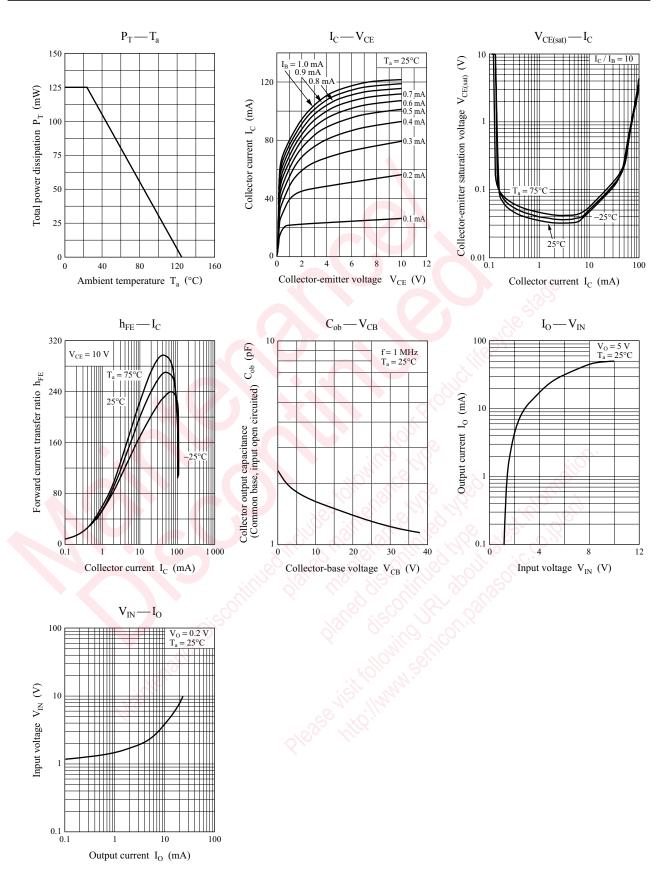
#### Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

| Parameter                                    | Symbol                     | Conditions   | <ul> <li>Min</li> </ul> | Тур | Max  | Unit |
|--|----------------------------|--|-------------------------|-----|------|------|
| Collector-base voltage (Emitter open)        | V <sub>CBO</sub>           | $I_{\rm C} = 10 \mu {\rm A}, I_{\rm E} = 0$  | \$ 50                   | 5   | 8,   | V    |
| Collector-emitter voltage (Base open)        | V <sub>CEO</sub>           | $I_{\rm C} = 2  {\rm mA}, I_{\rm B} = 0$   | 50                      |     |      | V    |
| Collector-base cutoff current (Emitter open) | I <sub>CBO</sub>           | $V_{CB} = 50 \text{ V}, I_E = 0$   | Son as                  | 2/1 | 0.1  | μΑ   |
| Collector-emitter cutoff current (Base open) | I <sub>CEO</sub>           | $V_{CE} = 50 \text{ V}, I_B = 0$   | S.C.                    |     | 0.5  | μΑ   |
| Emitter-base cutoff current (Collector open) | I <sub>EBO</sub>           | $V_{\rm EB} = 6 \text{ V}, I_{\rm C} = 0$  | 2.                      |     | 0.1  | mA   |
| Forward current transfer ratio               | $\mathbf{h}_{\mathrm{FE}}$ | $V_{CE} = 10 \text{ V}, I_C = 5 \text{ mA}$  | 80                      |     |      |      |
| Collector-emitter saturation voltage         | V <sub>CE(sat)</sub>       | $I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0.3 \text{ mA}$  |                         |     | 0.25 | V    |
| Output voltage high-level                    | V <sub>OH</sub>            | $V_{\rm CC} = 5 \text{ V}, V_{\rm B} = 0.5 \text{ V}, R_{\rm L} = 1 \text{ k}\Omega$                 | 4.9                     |     |      | V    |
| Output voltage low-level                     | V <sub>OL</sub>            | $V_{\rm CC} = 5 \text{ V}, \text{ V}_{\rm B} = 3.5 \text{ V}, \text{ R}_{\rm L} = 1 \text{ k}\Omega$ |                         |     | 0.2  | V    |
| Input resistance                             | R <sub>1</sub>             | 018,02 Nor   | -30%                    | 47  | +30% | kΩ   |
| Resistance ratio                             | $R_1/R_2$                  |  | 0.8                     | 1.0 | 1.2  |      |
| Transition frequency                         | f <sub>T</sub>             | $V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$                                    |                         | 150 |      | MHz  |

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

#### UP04213G

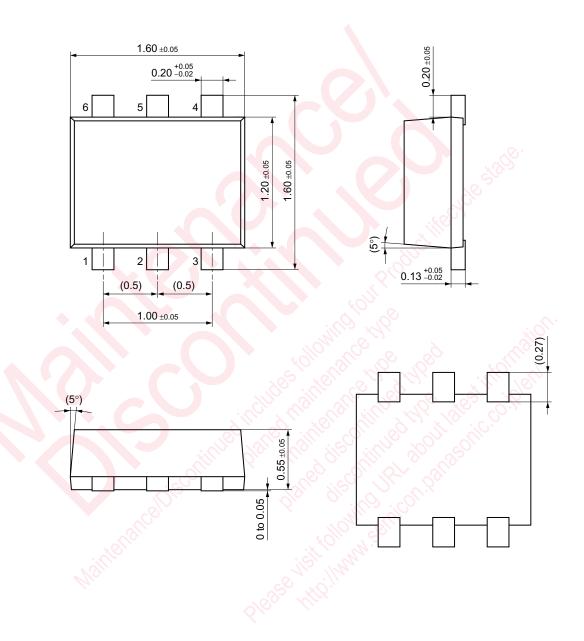
### **Panasonic**



### **Panasonic**

### SSMini6-F2

Unit: mm



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