imall

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UP05C8BG

Silicon NPN epitaxial planar type (Tr) Silicon epitaxial planar type (CCD load device)

For CCD output circuits

Features

- Two elements incorporated into one package (Tr + CCD load device)
- Costs can be reduced through downsizing of the equipment and reduction of the number of parts.

Basic Part Number

• 2SC3931G + CCD load device

Absolute Maximum Ratings $T_a = 25^{\circ}C$

	Parameter	Symbol	Rating	Unit	
Tr	Collector-base voltage (Emitter open)	V _{CBO}	30	V	
	Collector-emitter voltage (Base open)	V _{CEO}	20	V	
	Emitter-base voltage (Collector open)	V _{EBO}	3	V	
	Collector current	I _C	15	mA	
CCD	Limiting element voltage	V _{max}	40	S V	
load device	Limiting element current	I _{max}	.10	mA	
Overall	Total power dissipation *	P _T	125	mW	
	Junction temperature	Tj	125	€	
	Storage temperature	T _{stg}	-55 to +125	°C	

Note) * : Measuring on substrate at 17 mm × 10 mm × 1 mm

- 3: Gate 6: Collector
- Marking Symbol: 4F

Package

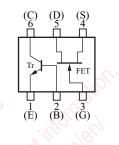
• Pin Name 1: Emitter

2: Base

SSMini6-F2

• Code

Internal Connection



4: Source

5: Drain

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

• Tr

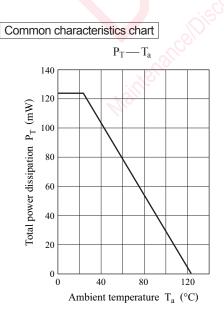
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu {\rm A}, \ I_{\rm E} = 0$	30			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = 10 \ \mu {\rm A}, \ I_{\rm C} = 0$	3			V
Base-emitter voltage	V _{BE}	$V_{CE} = 6 V, I_C = 1 mA$		720		mV
Forward current transfer ratio	h _{FE}	$V_{CE} = 6 V, I_C = 1 mA$	65		160	
Reverse transfer capacitance (Common emitter)	C _{re}	$V_{CB} = 6 \text{ V}, I_E = -1 \text{ mA}, f = 10.7 \text{ MHz}$		0.8		pF
Transition frequency	f _T	$V_{CB} = 6 \text{ V}, I_E = -1 \text{ mA}, f = 200 \text{ MHz}$		640		MHz
Noise figure	NF	$V_{CB} = 6 \text{ V}, I_E = -1 \text{ mA}, f = 100 \text{ MHz}$		3.3		dB
Power gain	PG	$V_{CB} = 6 \text{ V}, I_E = -1 \text{ mA}, f = 100 \text{ MHz}$		24		dB

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

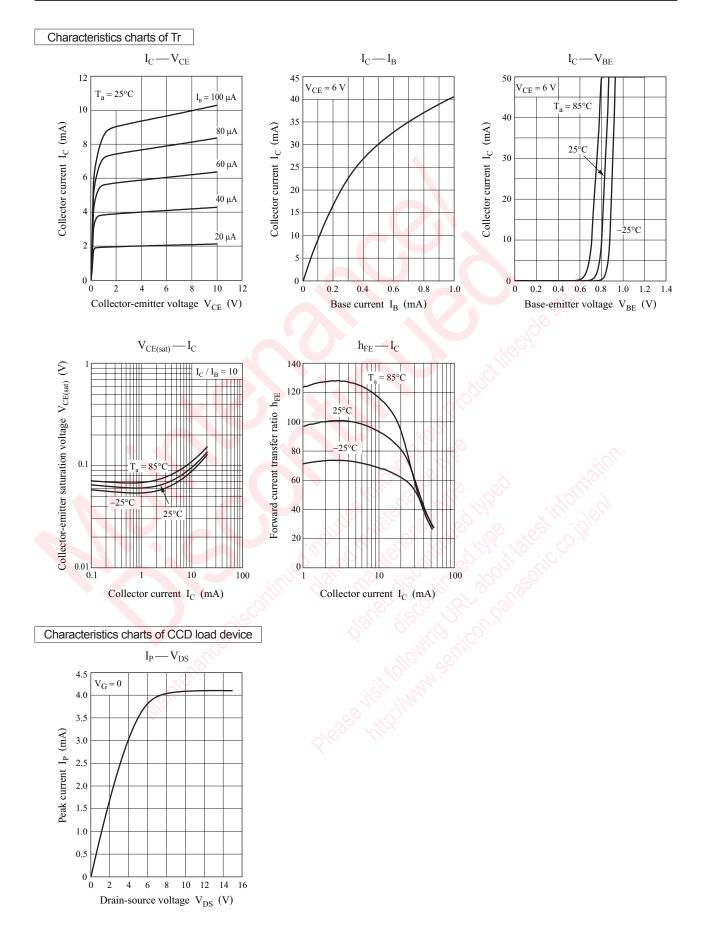
CCD Load Device

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Pinchi off current	I _P	$V_{\rm DS} = 10 {\rm V}, {\rm V}_{\rm G} = 0$	3.5		5.5	mA
Output impedance	Zo	$V_{\rm DS} = 10 {\rm V}, {\rm V}_{\rm G} = 0$	5	0.05		MΩ

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

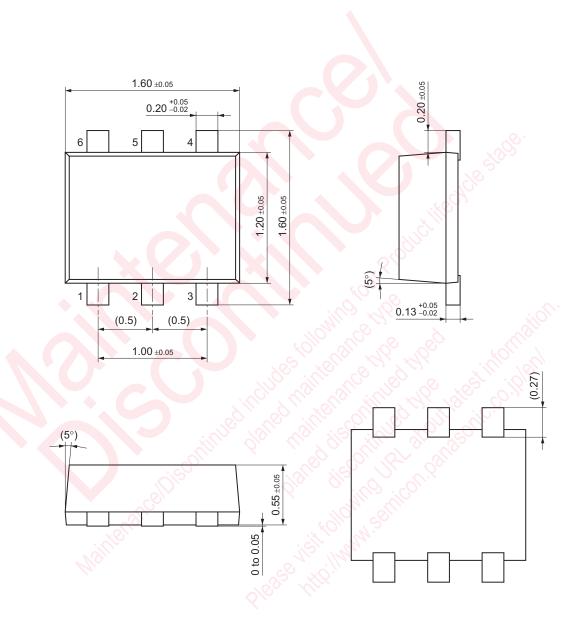


Panasonic



SSMini6-F2

Unit: mm



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