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# **UPD1001**

# **Programmable USB Power Delivery Controller**

# **Highlights**

- Integrated USB Power Delivery (PD) PHY
- · Support for Power Delivery Message Protocol
- · Integrated Voltage and Current ADC Inputs
- · Configuration Profile Selection
- · On-chip Microcontroller
- · SPI Interface
- Commercial, Industrial, and Automotive Grade Temperature Support
- Available in 28-TSSOP and 32-SQFN Packages

# **Target Applications**

- · AC Adapters & Chargers
  - Type-A
  - Type-B
  - Micro-A
  - Micro-B
  - Captive cable

# **Key Benefits**

- · Integrated USB Power Delivery (PD) PHY
  - Integrated receive termination
  - Requires minimal external components
- Support for Power Delivery Message Protocol
  - Message Generation/Consumption
  - Retry Generation
  - Error Handling
  - State Behavior
- Cable Detect Logic
  - Cable attachment type
- CFG\_SEL pins allow selection of multiple profiles
  - Provider
  - Consumer/Provider
- Integrated Voltage (VMON) and Current (IMON) ADC Inputs
- · Dead Battery Support
- · On-chip Microcontroller
  - Manages I/Os and other signals
  - Implements power delivery policy engine and device policy manager
- Configuration Programming via OTP, or Vendor Defined Messaging
- · Supports Low Power Modes
- · Serial Peripheral Interface (SPI) Bus
- Internal 3.3 V and 1.8 V Voltage Regulators
- Integrated Oscillator Reduces BOM Costs
- Package
  - 28-pin TSSOP (9.7 x 6.1 mm)
  - 32-pin SQFN (5 x 5 mm)
- Environmental
  - Commercial temperature range (0°C to +70°C)
  - Industrial temperature range (-40°C to +85°C)
  - Automotive temperature range (-40°C to +105°C)

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# 1.0 INTRODUCTION

# 1.1 General Description

The UPD1001 is a programmable USB Power Delivery (PD) controller designed to adhere to the *USB Power Delivery Specification*. USB Power Delivery allows a host (or device) to provide or consume up to 5 Amps and/or up to 20 Volts of power from a USB PD capable partner device on the other end of the USB cable. USB PD capable standard and custom cables/connectors are supported, which in most cases are backward compatible with standard USB connections.

The UPD1001 provides a complete USB Power Delivery solution for all charger and adapter solutions. The functionality of the UPD1001 is selected via two configuration selection pins, CFG\_SEL0 and CFG\_SEL1, which can be used to select unique PD and system configurations. Designing the UPD1001 into a system can be as simple as selecting a configuration, with no external EEPROM required. Advanced programmability options exist with an external EEPROM installed.

The integrated USB Power Delivery MAC and PHY support provider and consumer operation via the PD communication protocol, as specified in Revision 1.0 (Version 1.2) of the *USB Power Delivery Specification*. Monitoring of VBUS and battery charging is accomplished via the integrated voltage and current ADC inputs. The PHY supports cable ID detection/identification and loopback modes. The PHY includes a 24MHz FSK modulator/demodulator and provides integrated terminations. The USB PD MAC supports both USB PD insertion detection (cold socket) and dead battery cases.

The on-chip microcontroller manages the IOs and implements the power delivery local policy engine and device manager. The SPI ROM controller is used by the microcontroller for optional external code execution from ROM. A One Time Programmable (OTP) ROM is integrated in the UPD1001. Integrated 3.3 V and 1.8 V regulators allow device operation from a single power supply. The UPD1001 is available in commercial (0°C to +70°C), industrial (-40°C to +85°C), and automotive (-40°C to +105°C) temperature ranges. An internal block diagram of the UPD1001 is shown in Figure 1-1.

Power PD\_ID Micro-Delivery PD\_DATA controller AFE SPI ROM **SPI ROM** Power Controller **Delivery** MAC  $VBUS^{\dagger}$ Voltage Switch<sup>†</sup> VTR 1/0 1/0 Controller<sup>†</sup> Voltage VMON **Monitor** Current Reset IMON RESET\_N **Monitor** Controller CFG\_SEL0 Configuration Clocks  $CFG\_SEL1$ Select

†Specific functions are determined by package and CFG\_SEL0/CFG\_SEL1 profile selection.

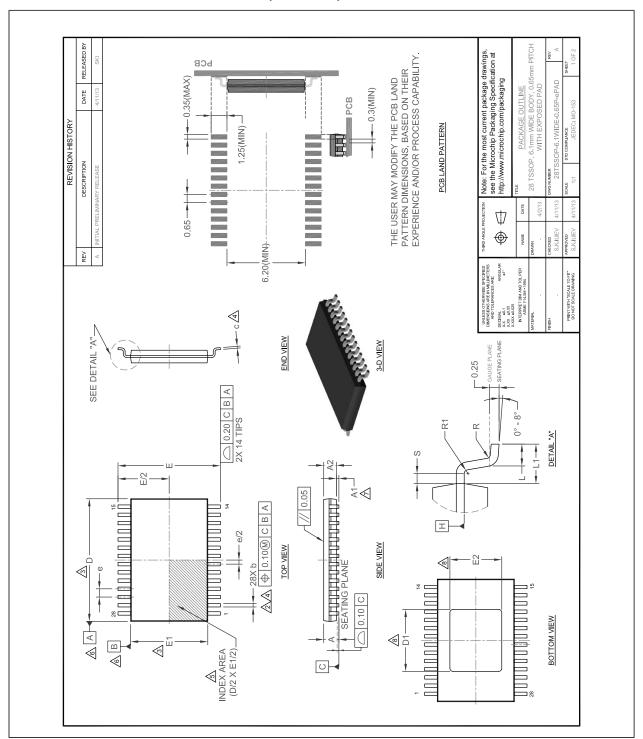
FIGURE 1-1: INTERNAL BLOCK DIAGRAM

**UPD1001** 

# 2.0 PACKAGE OUTLINES

# 2.1 28-TSSOP

FIGURE 2-1: 28-TSSOP PACKAGE (DRAWING)



#### FIGURE 2-2: 28-TSSOP PACKAGE (DIMENSIONS)

	REVISION HISTORY		
REV	DESCRIPTION	DATE	RELEASED BY
<	INITIAL PRELIMINARY RELEASE	4/11/13	SKI

REMARK

COMMON DIMENSIONS

NOM MAX NOTE

Z

SYMBOL

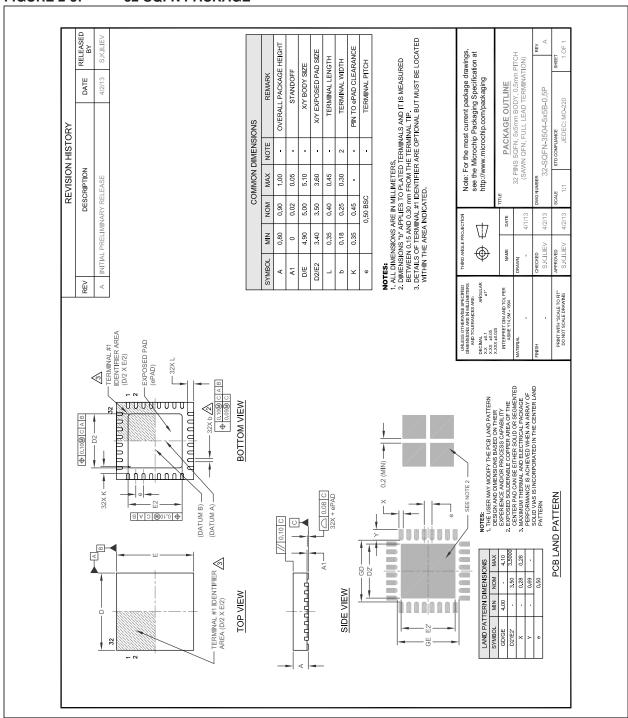
OVERALL PACKAGE HEIGHT	STANDOFF	BODY THICKNESS	X BODY SIZE	X EXPOSED PAD	LEAD SPAN	Y BODY SIZE	Y EXPOSED PAD	LEAD FOOT LENGTH	LEAD LENGTH	LEAD WIDTH	LEAD FOOT THICKNESS	LEAD PITCH	LEAD SHOULDER	LEAD FOOT RADIUS	LEAD SHOULDER RADIUS	
1	7	ı	က		ı	3		ı		2,4	4	ı	ı	ı		
1.20	0.15	1.05	9.80	5.00		6.20	4.20	0.75		0:30	0.20		-		1	
1		1.00	9.70	4.90	8.10 BSC	6.10	4.10	09.0	1.00 REF	1		0.65 BSC		-		
,	0.00	08.0	9.60	4.80		00.9	4.00	0.45		0.19	60.0		0.20	60.0	0.09	
Α	H4	A2	٥	10	ш	E1	E2	Г	L1	q	0	ө	S	Я	R1	

- 1. ALL DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES). 2. DIMENSION "b" DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 MM TOTAL IN EXCESS OF THE "b" DIMENSION AT MMC. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT MINIMUM SPACE BETWEEN PROTRUSION AND ADJACENT LEAD IS 0.07 MM.
  - BURRS. MAXIMUM MOLD FLASH, PROTRUSIONS OR GATE BURRS IS 0.15 MM PER SIDE. DIMENSION "E1" DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. MAXIMUM INTERLEAD FLASH OR PROTRUSION IS 0.25 MM PER SIDE. "D" & "E1" DIMENSION "D" DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE
    - DIMENSIONS ARE DETERMINED AT DATUM PLANE "H". "b" & "c" APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.10 TO 0.25 MM FROM THE LEAD TIP.
      - DETAILS OF THE PIN 1 IDENTIFIER ARE OPTIONAL, BUT MUST BE LOCATED WITHIN THE ZONE INDICATED. 5
- DATUMS "A" AND "B" TO BE DETERMINED AT DATUM PLANE "H". "A1" IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY, EXCLUDING ANY THERMAL 9
  - ENHANCEMENT ON CAVITY DOWN PACKAGE CONFIGURATIONS. "D1" AND "E2" DIMENSIONS DO NOT INCLUDE MOLD FLASH

UNLESS O'THERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS AND TOLERANGES ARE: DECIMAL X.X. 40.1 £1* X.X. 40.05	THIRD ANGLE PROJECT	oneccion	Note: For see the M http://wwv	Note: For the most current package drawings, see the Microchip Packaging Specification at http://www.microchip.com/packaging	wings, ion at
A.XXX.±0.025 INTERPRET DIM AND TOL PER ASME Y14.5M - 1994	NAME	DATE	ште	PACKAGE OUTLINE	
MATERIAL -	DRAWN -	4/2/13	28 TSSO	28 TSSOP, 6.1mm WIDE BODY, 0.65mm PITCH WITH EXPOSED PAD	n PITCH
FINISH	CHECKED		DWG NUMBER		REV
	S.K.ILIEV	4/11/13	28TS	28TSSOP-6.1WIDE-0.65P-ePAD	⋖
PRINTWITH SCALE TO FIT	APPROVED		SCALE	STD COMPLIANCE SH	SHEET
DO NOT SCALE DRAWING	S.K.ILIEV	4/11/13 1:1	1::	JEDEC: MO-153	2 OF 2

# 2.2 32-SQFN

FIGURE 2-3: 32-SQFN PACKAGE



# **UPD1001**

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PART NO Device	[X] - XX / XX  Tape and Reel Temperature Package Option Range	Examples:  a) UPD1001-A/ST Tray, Commercial temp., 28-pin TSSOP
Device:	UPD1001	b) UPD1001T-AI/MQ Tape & reel, Industrial temp., 32-pin SQFN
Tape and Reel Option:	Blank = Standard packaging (tray) T = Tape and Reel(Note 1)	
Temperature Range:	A = $0^{\circ}$ C to +70°C (Commercial) AI = -40°C to +85°C (Industrial) AV = -40°C to +105°C (Automotive)	
Package:	ST = 28-pin TSSOP MQ = 32-pin SQFN	Note 1: Tape and Reel identifier only appears in the catalog part number description. Thi identifier is used for ordering purposes at is not printed on the device package.  Check with your Microchip Sales Office to package availability with the Tape and Responding.

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