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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







Powermite 3 Surface Mount 8 Amp 40 Volt Schottky Rectifier

DESCRIPTION

The UPS840e3 offers an exceptionally small and powerful RoHS compliant package for a 40 V, 8 Amp Schottky rectifier. Competing solutions typically come only in much larger packages. It is an ideal, small footprint, surface mount solution with a full metallic bottom side and is compatible with automatic insertion equipment. In combination with Microsemi's unique internal locking tab on the die, it provides a very low thermal resistance path to move heat out of the package. The resulting lowering of junction temperature in the die provides for lower leakage current and power loss.

Important: For the latest information, visit our website http://www.microsemi.com.

FEATURES

- Low profile package (<1.1 mm)
- Small footprint: 32 mm² (4.826 x 6.604 mm) or 0.05 in² (0.190 x 0.260 inches). See mounting pad details on the <u>last page</u>.
- Plastic package has Underwriters Laboratory Flammability classification 94V-0
- RoHS compliant

APPLICATIONS / BENEFITS

- High current capability with low forward voltage drop
- Guard ring construction for transient protection
- Silicon Schottky (hot carrier) rectifier for minimal t_{rr} and minimal reverse recovery voltage
- Elimination of reverse-recovery oscillations to reduce need for EMI filtering.
- For use in high-frequency switching power supplies, inverters, free-wheeling diode applications, charge pump circuits, and polarity protection applications.
- Low forward power loss and high efficiency
- Low inductive parasitics (<2nH) for minimal L (di/dt) effects
- Robust package configuration for pick-and-place handling
- Full-metallic bottom eliminates flux entrapment

MAXIMUM RATINGS

Parameters/Test Conditions	Symbol	Value	Unit
Storage Temperature	T _{STG}	-55 to +150	°C
Junction Temperature	TJ	-55 to +125	°C
Thermal Resistance Junction-to-Ambient ⁽¹⁾	R _{ÐJA}	65	ºC/W
Thermal Resistance Junction-to-Case	R _{eJC}	2.5	ºC/W
Forward Surge Current ⁽²⁾	I _{FSM}	150	Α
Total Capacitance @ 1 MHz and V_R of 4.0 V	Ст	700	pF
Average Rectified Output Current @ $T_{C} = 75$ ^{0}C	Ι _Ο	8.0	A
Solder Temperature @ 10 s		260	°C

Notes: 1. On PCB with FR4 using 2 oz copper and recommended mounting pad size (see <u>pad layout</u>).

2. At 8.3 ms single half-sine waveform superimposed on rated load (JEDEC method).





Powermite 3 Package

MSC – Lawrence

6 Lake Street, Lawrence, MA 01841 Tel: 1-800-446-1158 or (978) 620-2600 Fax: (978) 689-0803

MSC - Ireland

Gort Road Business Park, Ennis, Co. Clare, Ireland Tel: +353 (0) 65 6840044 Fax: +353 (0) 65 6822298

Website:

www.microsemi.com



MECHANICAL and PACKAGING

- CASE: Molded epoxy package meets UL94V-0
- TERMINALS: Copper with annealed matte-tin plating for RoHS compliance. Solderable per MIL-STD-750 method 2026. (Consult factory for tin-lead plating).
- MARKING: Body marked with "S840•" (dot indicates RoHS compliance)
- POLARITY: Two-leads on side are internally connected together for anode and bottom side is the cathode.
- TAPE & REEL option: Packaging per EIA-481-2 with 16 mm tape. Consult factory for quantities.
- WEIGHT: Approximately 0.072 grams
- See <u>Package Dimensions</u> on last page.

PART NOMENCLATURE



SYMBOLS & DEFINITIONS			
Symbol	Definition		
Ст	Total Capacitance: The total small signal capacitance between the diode terminals of a complete device.		
IF	Forward Current: The forward current dc value, no alternating component.		
I _{FSM}	Maximum Forward Surge Current: The forward current, surge peak or rated forward surge current.		
Ι _Ο	Average Rectified Output Current: The output current averaged over a full cycle with a 50 Hz or 60 Hz sine-wave input and a 180 degree conduction angle.		
I _R	Reverse Current: The maximum reverse (leakage) current that will flow at the specified voltage and temperature.		
VF	Maximum Forward Voltage: The maximum forward voltage the device will exhibit at a specified current.		
VR	Reverse Voltage: The reverse voltage dc value, no alternating component.		
V _{RRM}	Repetitive Peak Reverse Voltage: The peak reverse voltage including all repetitive transient voltages but excluding all non-repetitive transient voltages.		
V _{RWM}	Working Peak Reverse Voltage: The maximum peak voltage that can be applied over the operating temperature range excluding all transient voltages (ref JESD282-B). Also sometimes known as PIV.		

ELECTRICAL CHARACTERISTICS

Part	Working Peak Reverse	Maximum RMS Voltage	Maximum Peak Repetitive	Maximum Forward Voltage (Note 1)		Maximum Reverse Current I _R @ V _{RWM}	Maximum Voltage Rate of Change	
Number	mber Voltage V _{RMS} Voltage V V _{RWM} V _{RRM} V		V _F @ 3 A	V _F @ 8 A	V _F @ 10 A		dV/dt	
	Volts	Volts	Volts	Volts	Volts	Volts	mA	V/µs
UPS840e3	40	28	40	0.39	0.45	0.49	0.5	1000

NOTES: (1) Pulse test, 1% duty cycle.

GRAPHS

PACKAGE DIMENSIONS

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	INCH		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.068	0.072	1.73	1.83	
В	0.172	0.174	4.37	4.43	
С	0.197	0.204	5.01	5.17	
D	0.035	NOM	0.889	NOM	
Е	0.159	0.161	4.03	4.09	
F	0.072	NOM	1.83 NOM		
G	0.056 NOM		1.422 NOM		
н	0.043	0.045	1.10	1.14	
J	0.252	0.260	6.40	6.61	
Κ	0.007 NOM		0.178 NOM		
L	0.028	0.030	0.71	0.77	
Μ	0.014	0.018	0.36	0.46	

PAD LAYOUT

	INCH	MILLIMETERS
DIM	NOMINAL	NOMINAL
Α	0.190	4.826
В	0.210	5.344
С	0.038	0.965
D	0.034	0.864
E	0.030	0.762
F	0.030	0.762

SCHEMATIC

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