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# 1A, 50V - 1000V High Efficient Surface Mount Rectifiers

# **FEATURES**

- Glass passivated chip junction
- Ideal for automated placement
- Low forward voltage drop
- Ultrafast recovery time for high efficiency
- Built-in strain relief
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition







DO-214AC (SMA)

#### **MECHANICAL DATA**

Case: DO-214AC (SMA)

Molding compound, UL flammability classification rating 94V-0

Moisture sensitivity level: level 1, per J-STD-020 Part No. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test **Polarity:** Indicated by cathode band **Weight:** 0.06 g (approximately)

MAXIMUM RATINGS AND ELECTRICAL CHARA	CIENISTICS	(1 <sub>A</sub> -25	Curii	ียรร บน	iei wise	notea	)		
PARAMETER	SYMBOL	US	US	US	US	US	US	US	UNIT
I ANAMETEN	O I MIBOL	1 A	1 B	1 D	1 G	1 J	1K	1 M	
Maximum repetitive peak reverse voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Maximum RMS voltage	$V_{RMS}$	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	$V_{DC}$	50	100	200	400	600	800	1000	V
Maximum average forward rectified current	I <sub>F(AV)</sub>	1					Α		
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>				30				Α
Maximum instantaneous forward voltage (Note 1) @ 1 A	V <sub>F</sub>	1.0 1.7			V				
Maximum reverse current @ rated $V_R$ $T_J$ =125°C $T_J$ =125°C	I <sub>R</sub>	5 150		μΑ					
Maximum reverse recovery time (Note 2)	t <sub>rr</sub>	50 75			ns				
Typical junction capacitance (Note 3)	CJ	15 10			pF				
Typical thermal resistance	R <sub>eJL</sub> R <sub>eJA</sub>	27 75			°C/W				
Operating junction temperature range	TJ	- 55 to +150				°C			
Storage temperature range	T <sub>STG</sub>	- 55 to +150					°C		

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Reverse Recovery Test Conditions:  $I_F$ =0.5A,  $I_R$ =1.0A,  $I_{RR}$ =0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.



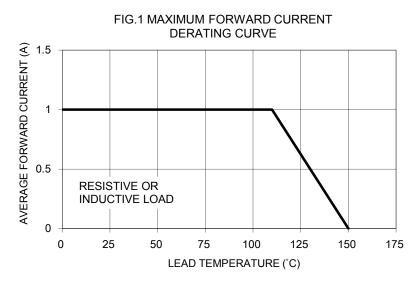
PART NO.	PART NO.	PACKING CODE	PACKING CODE	PACKAGE	PACKING
	SUFFIX		SUFFIX		
US1x (Note 1) H		R3	O	SMA	1,800 / 7" Plastic reel
		R2		SMA	7,500 / 13" Paper reel
	ш	M2		SMA	7,500 / 13" Plastic reel
	П	F3		Folded SMA	1,800 / 7" Plastic reel
		F2		Folded SMA	7,500 / 13" Paper reel
		F4		Folded SMA	7,500 / 13" Plastic reel
	N/A	E3		Clip SMA	1,800 / 7" Plastic reel
		E2		Clip SMA	7,500 / 13" Plastic reel

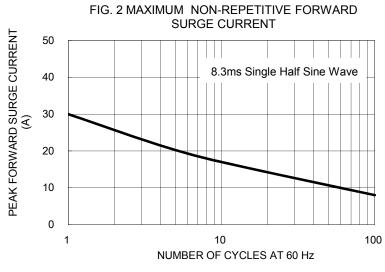
Note 1: "x" defines voltage from 50V (US1A) to 1000V (US1M)

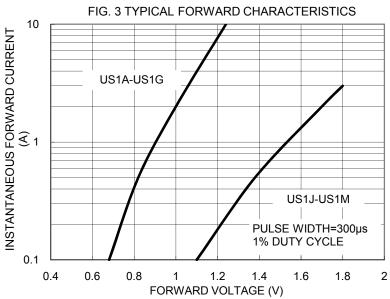
EXAMPLE							
PREFERRED P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION		
US1MHR3G	US1M	Ħ	R3	G	AEC-Q101 qualified Green compound		

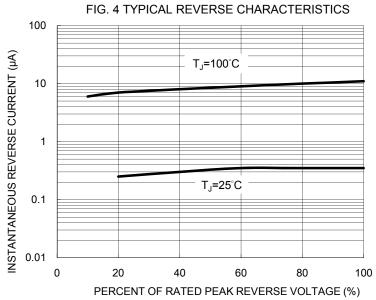
#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub>=25°C unless otherwise noted)



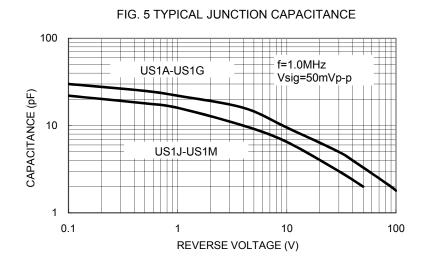








# Taiwan Semiconductor



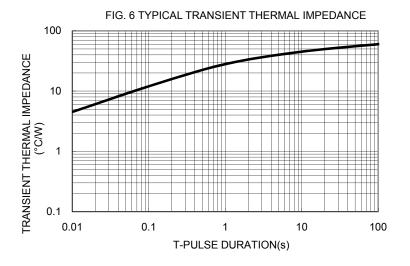
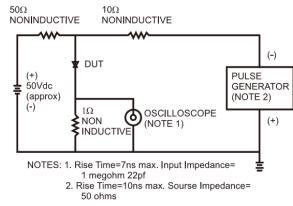
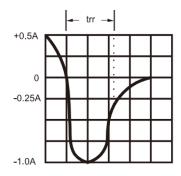
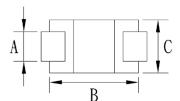


FIG.7- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

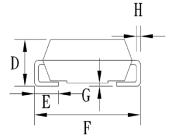




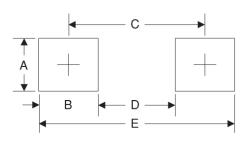
**PACKAGE OUTLINE DIMENSIONS** DO-214AC (SMA)



DIM.	Unit	(mm)	Unit (inch)		
Dilvi.	Min	Max	Min	Max	
Α	1.27	1.58	0.050	0.062	
В	4.06	4.60	0.160	0.181	
С	2.29	2.83	0.090	0.111	
D	1.99	2.50	0.078	0.098	
E	0.90	1.41	0.035	0.056	
F	4.95	5.33	0.195	0.210	
G	0.10	0.20	0.004	0.008	
Н	0.15	0.31	0.006	0.012	



# SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
А	1.68	0.066
В	1.52	0.060
С	3.93	0.155
D	2.41	0.095
Е	5.45	0.215

# **MARKING DIAGRAM**



P/N =Specific Device Code Green Compound G = YW = Date Code

F= **Factory Code** 



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