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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







20V Nch+Pch Middle Power MOSFET

Symbol	Tr1:Nch	Tr2:Pch
V_{DSS}	20V	-20V
R _{DS(on)} (Max.)	42mΩ	59mΩ
I _D	±5.5A	±5.0A
P_D	2.0)W

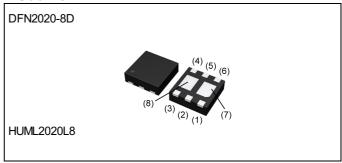
Features

- 1) Low on resistance.
- 2) Small Surface Mount Package.
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

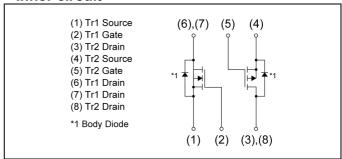
Application

Switching

Outline



•Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TCR
	Marking	MA3

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter		Cymala al	Va	lue	Unit
		Symbol	Tr1:Nch	Tr2:Pch	Unii
Drain - Source voltage		V_{DSS}	20	-20	V
Continuous drain current		I _D	±5.5	±5.0	Α
Pulsed drain current		I _{DP} *1	±12	±12	Α
Gate - Source voltage		V_{GSS}	±8	±8	V
Avalanche current, single pulse		I _{AS} *2	1.8	-2.3	Α
Avalanche energy, single pulse		E _{AS} *2	2.0	4.0	mJ
Dougr discipation	total	P _D *3	2	.0	W
Power dissipation element		FD.	1	.4	VV
Junction temperature		T _j	15	50	°C
Operating junction and storage temperation	ture range	T _{stg}	-55 to	+150	°C

●Thermal resistance

Parameter	Symbol		Lloit		
	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R _{thJA} *3	-	-	62.5	°C/W

● Electrical characteristics (T_a = 25°C)

Davanatas	Curada a l	T	Conditions		Values		Unit
Parameter	Symbol	Type	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown	V	Tr1	V _{GS} = 0V, I _D = 1mA	20	-	-	V
voltage	V _{(BR)DSS}	Tr2	$V_{GS} = 0V$, $I_D = -1mA$	-20	-	-	V
Breakdown voltage	ΔV _{(BR)DSS}	Tr1	I _D = 1mA, referenced to 25°C	-	18	-	m) //ºC
temperature coefficient	ΔT _j	Tr2	I _D = -1mA, referenced to 25°C	-	-10.3	-	mV/°C
Zero gate voltage	_	Tr1	V _{DS} = 20V, V _{GS} = 0V	-	-	1	
drain current	I _{DSS}	Tr2	V _{DS} = -20V, V _{GS} = 0V	-	-	-1	μA
Gate - Source		Tr1	$V_{DS} = 0V$, $V_{GS} = \pm 8V$	-	-	±100	0
leakage current	I _{GSS}	Tr2	$V_{DS} = 0V, V_{GS} = \pm 8V$	-	-	±100	nA
Gate threshold	V _{GS(th)}	Tr1	$V_{DS} = V_{GS}$, $I_D = 1mA$	0.5	-	1.5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
voltage		Tr2	$V_{DS} = V_{GS}$, $I_D = -1mA$	-0.5	-	-1.5	V
Gate threshold voltage	$\Delta V_{GS(th)}$	Tr1	I _D = 1mA, referenced to 25°C	-	-1.8	-	\ //ºO
temperature coefficient	ΔT_j	Tr2	I _D = -1mA, referenced to 25°C	-	1.7	-	mV/°C
		Tr1	$V_{GS} = 4.5V, I_D = 5.5A$	-	30	42	
Static drain - source	D *4	111	$V_{GS} = 2.5V, I_D = 2.75A$	-	45	63	O
on - state resistance	R _{DS(on)} *4	Tr2	V _{GS} = -4.5V, I _D = -5A	-	42	59	mΩ
		Ir2	V _{GS} = -2.5V, I _D = -2.5A	-	54	76	
0.1	В	Tr1	f_4NAL - and a dualin	-	2.2	-	
Gate resistance	R_{G}	Tr2	f=1MHz, open drain	-	9.5	-	Ω
Forward Transfer	IV. I*4	Tr1	V _{DS} = 5V, I _D = 3A	3.1	-	-	
Admittance	Y _{fs} *4	Tr2	$V_{DS} = -5V, I_D = -3A$ 4.1 -		-	S	

^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

Tr2: L \simeq 1mH, V_{DD} = -10V, R_G = 25 Ω , STARTING T $_j$ = 25 $^{\circ}$ C Fig.6-1,6-2

^{*2} Tr1: L \simeq 1mH, V_{DD} = 10V, R_G = 25 Ω , STARTING T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*3} Mounted on a Cu board (40×40×0.8mm)

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

<Tr1>

Parameter	Cumphal	Symbol Conditions		Values			
	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	462	-		
Output capacitance	C _{oss}	V _{DS} = 10V	-	56	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	37	-		
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq 10V$, $V_{GS} = 4.5V$	-	7.8	-		
Rise time	t _r *4	I _D = 2.75A	-	5.9	-		
Turn - off delay time	t _{d(off)} *4	$R_L = 3.64\Omega$	-	24	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	5.5	-		

<Tr2>

Parameter	Symbol	Symbol Conditions -		Values			
	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	460	-		
Output capacitance	C _{oss}	V _{DS} = -10V	-	90	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	80	-		
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq$ -10V, $V_{GS} =$ -4.5V	-	9	-		
Rise time	t _r *4	I _D = -2.5A	-	36	-		
Turn - off delay time	t _{d(off)} *4	$R_L = 4\Omega$	-	50	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	30	ı		

● Gate charge characteristics (T_a = 25°C)

<Tr1>

Parameter	Symbol Conditions -		Values			Unit
			Min.	Тур.	Max.	Offic
Total gate charge	Q _g *4		-	4.0	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \approx 10V, I_{D} = 5.5A$ $V_{GS} = 4.5V$	-	1.0	-	nC
Gate - Drain charge	Q _{gd} *4	VGS 1.0 V	-	1.0	1	

<Tr2>

Parameter	Cumbal	Conditions	,	Values		Lloit
	Symbol Conditions	Min.	Тур.	Max.	Unit	
Total gate charge	Q _g *4		-	6.5	-	
Gate - Source charge	Q _{gs} *4	V _{DD} ≃ -10V, I _D = -5A V _{GS} = -4.5V	-	0.9	-	nC
Gate - Drain charge	Q _{gd} *4	765	-	2.2	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1>

Parameter	Cumphal	Conditions	,	Linit		
	Symbol Conditions -	Min.	Тур.	Max.	Unit	
Continuous forward current	I _S	T - 25°C	-	-	1.67	_
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	12	A
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 1.67A	-	-	1.2	V

<Tr2>

Parameter	Cumabal	ymbol Conditions		Values			
	Symbol Conditions	Min.	Тур.	Max.	Unit		
Continuous forward current	Is	T - 25°C	-	-	-1.67	_	
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	-12	A	
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = -1.67A	-	-	-1.2	V	

Fig.1 Power Dissipation Derating Curve

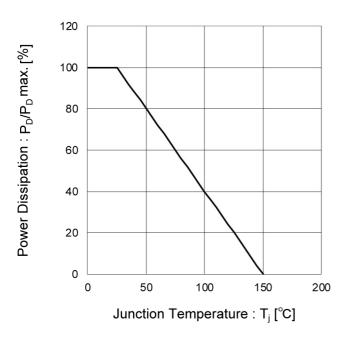
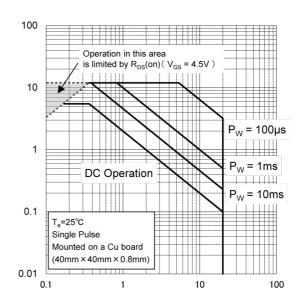


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : $V_{DS}[V]$

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

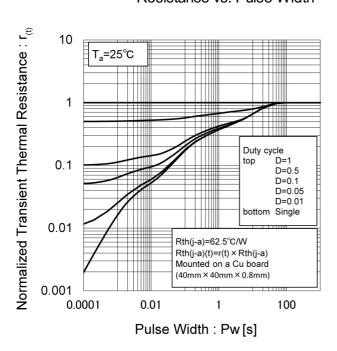
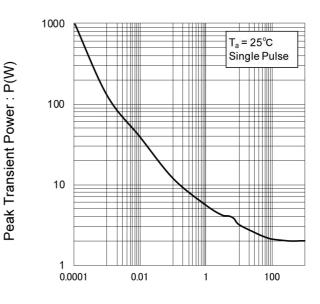
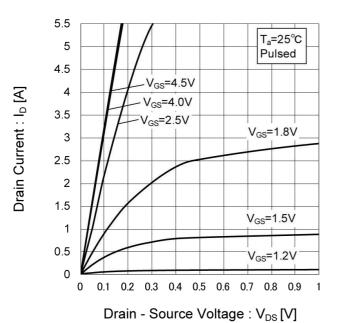


Fig.4 Single Pulse Maximum Power dissipation



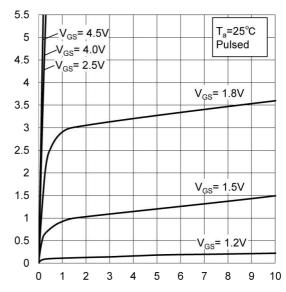
Pulse Width: Pw[s]

Fig.5 Typical Output Characteristics(I)



Drain Current : I_D [A]

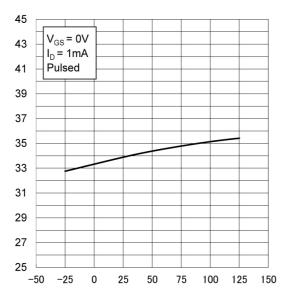
Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

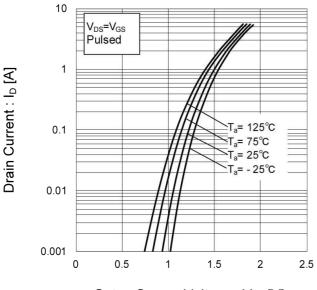
Fig.7 Breakdown Voltage vs. Junction Temperature





Junction Temperature : T_i [°C]

Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : V_{GS} [V]

Fig.9 Gate Threshold Voltage vs. Junction Temperature

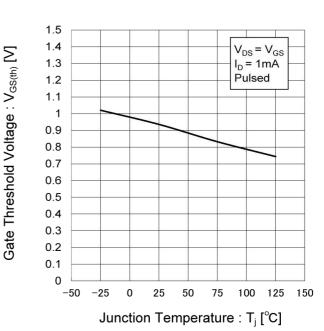


Fig.10 Forward Transfer Admittance vs. Drain Current

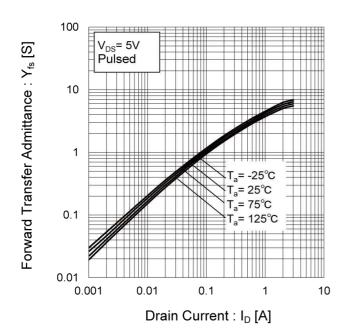


Fig.11 Drain Current Derating Curve

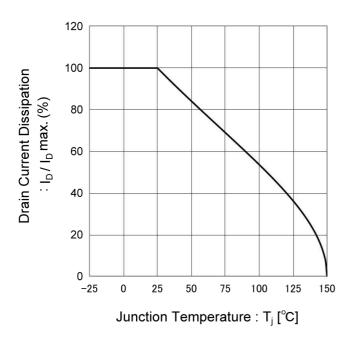
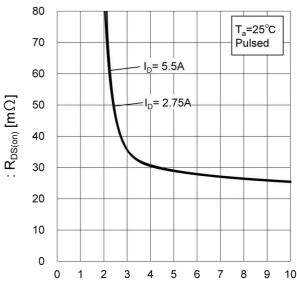


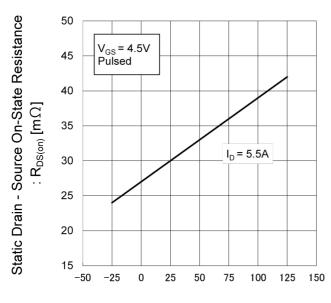
Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Static Drain - Source On-State Resistance

Gate - Source Voltage : V_{GS} [V]

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_i [°C]

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Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

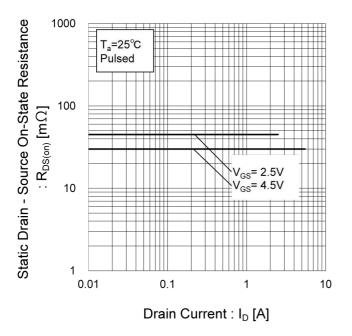


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

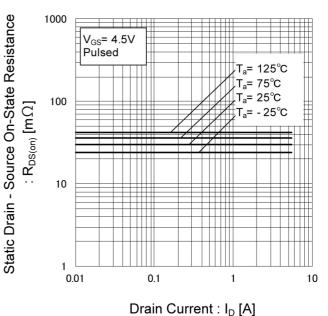


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

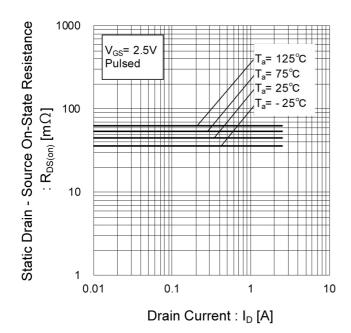


Fig.17 Typical Capacitance vs. Drain - Source Voltage

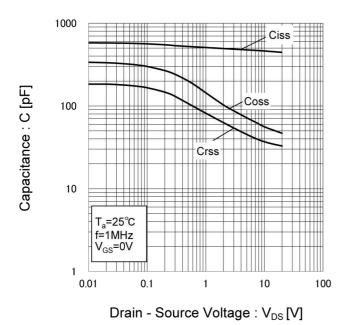


Fig.18 Switching Characteristics

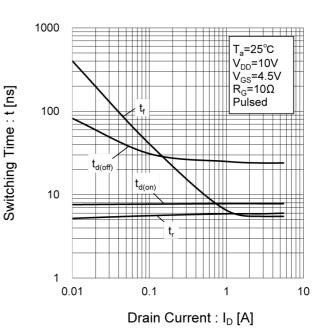


Fig.19 Dynamic Input Characteristics

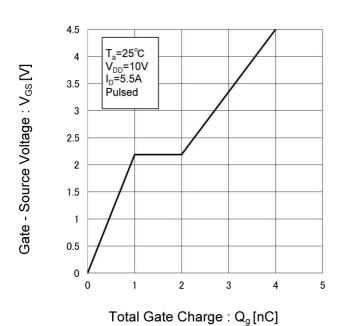
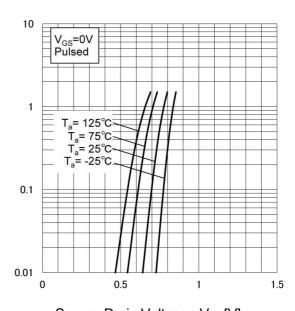


Fig.20 Source Current vs. Source Drain Voltage



Source-Drain Voltage : $V_{\text{SD}}[V]$

Source Current : Is [A]

Fig.1 Power Dissipation Derating Curve

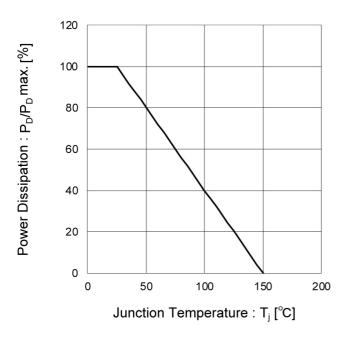
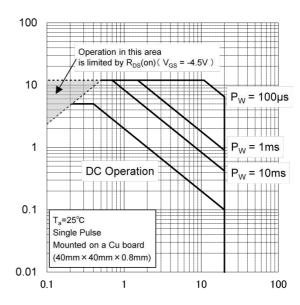


Fig.2 Maximum Safe Operating Area



Drain Current: -l_D [A]

Drain - Source Voltage: -VDS [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

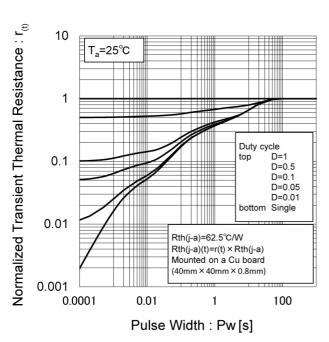
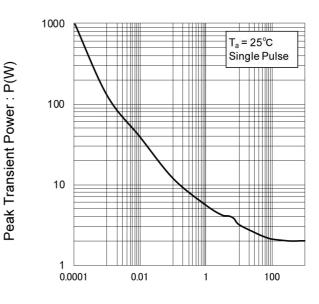


Fig.4 Single Pulse Maximum Power dissipation

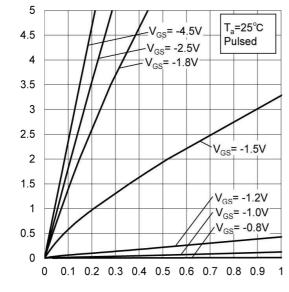


Pulse Width: Pw[s]

Drain Current: -l_D [A]

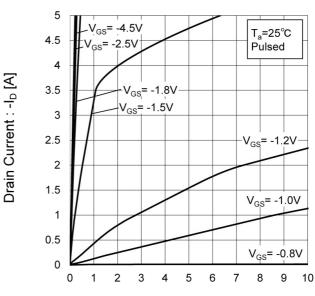
● Electrical characteristic curves < Tr2>

Fig.5 Typical Output Characteristics(I)



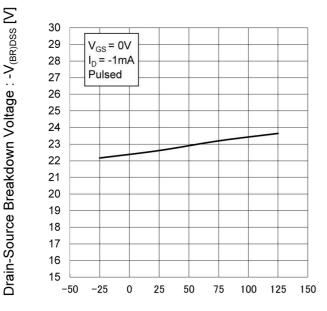
Drain - Source Voltage : -V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



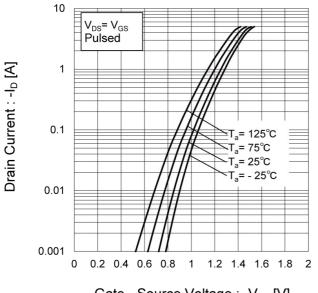
Drain - Source Voltage : -V_{DS} [V]

Fig.7 Breakdown Voltage vs. Junction Temperature



Junction Temperature : T_i [°C]

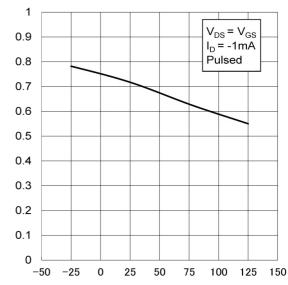
Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : -V_{GS} [V]

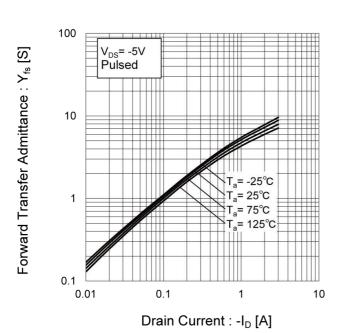
Fig.9 Gate Threshold Voltage vs. Junction **Temperature**





Junction Temperature : T_j [°C]

Fig.10 Forward Transfer Admittance vs. **Drain Current**



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Fig.11 Drain Current Derating Curve

Drain Current Dissipation

100

(%) 80

60

20

20

20

Junction Temperature : T_j [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

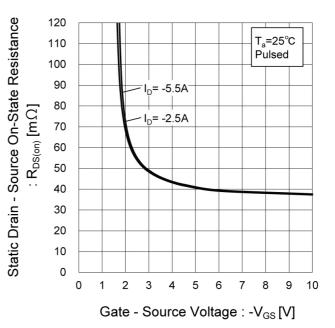


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

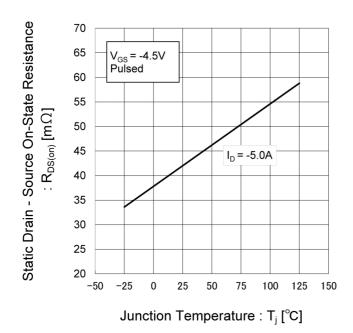


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

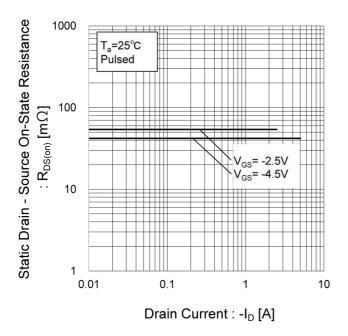


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

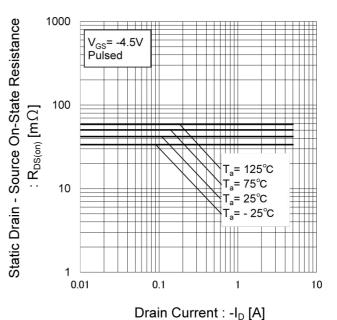
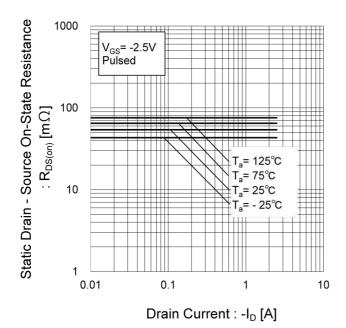


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)



ROHM

Fig.17 Typical Capacitance vs. Drain - Source Voltage

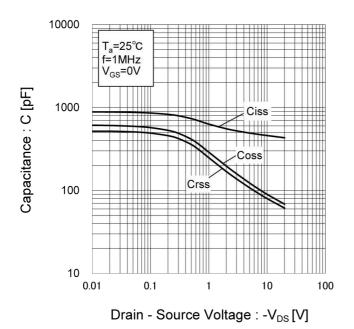


Fig.18 Switching Characteristics

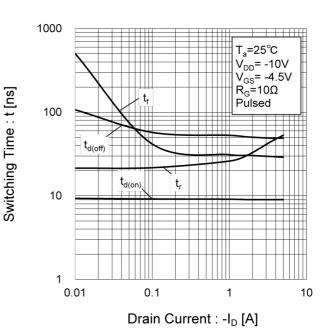


Fig.19 Dynamic Input Characteristics

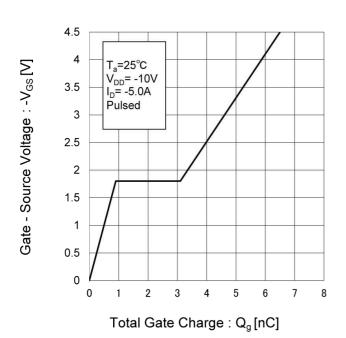
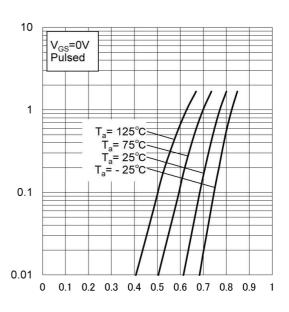


Fig.20 Source Current vs. Source Drain Voltage



Source - Drain Voltage : -V_{SD} [V]

Source Current : -I_s [A]

● Measurement circuits < Tr1>

Fig.1-1 Switching Time Measurement Circuit

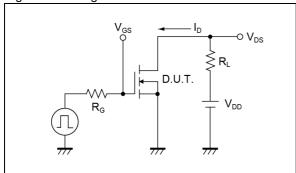


Fig.2-1 Gate Charge Measurement Circuit

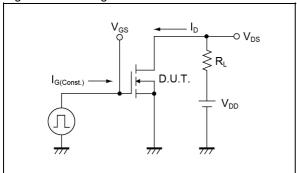


Fig.3-1 Avalanche Measurement Circuit

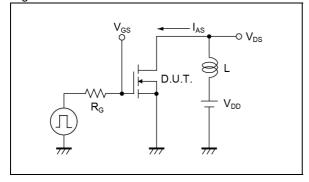


Fig.1-2 Switching Waveforms

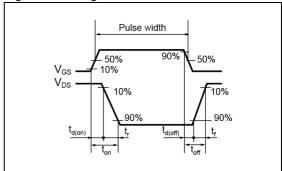


Fig.2-2 Gate Charge Waveform

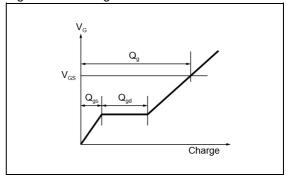
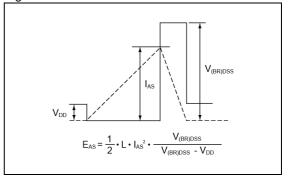


Fig.3-2 Avalanche Waveform



● Measurement circuits < Tr2>

Fig.4-1 Switching Time Measurement Circuit

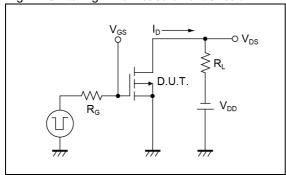


Fig.5-1 Gate Charge Measurement Circuit

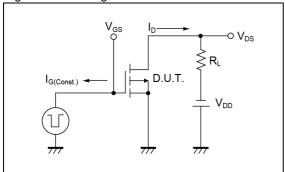


Fig.6-1 Avalanche Measurement Circuit

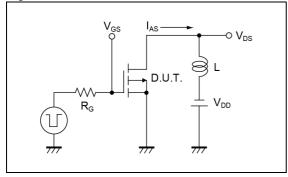


Fig.4-2 Switching Waveforms

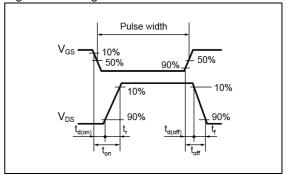


Fig.5-2 Gate Charge Waveform

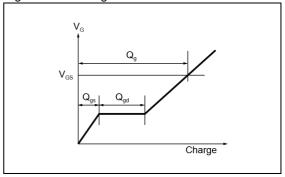
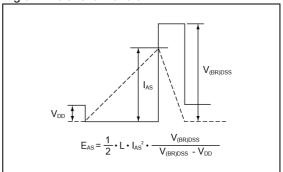


Fig.6-2 Avalanche Waveform

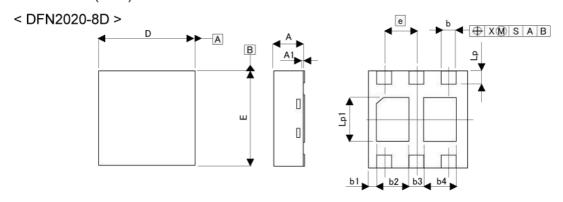


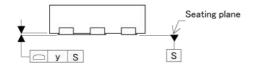
Notice

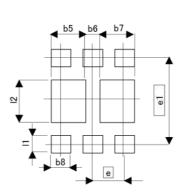
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions

HUML2020L8 (Dual)







Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.55	0.65	0.022	0.026
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	0.:	25	0.0	010
b2	0.60	0.70	0.024	0.028
b3	0	.3	0.0)12
b4	0.60	0.70	0.024	0.028
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.0	65	0.0	26
Lp	0.225	0.325	0.009	0.013
Lp1	0.80	1.00	0.031	0.039
x	-	0.10	3#3	0.004
V	-	0.10	10 5 8	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b5	-	0.70	0.90	0.028
b6	0.20	0.30	0.008	0.012
b7	*	0.70	0#3	0.028
b8	ž.	0.45		0.018
e1	1.725		0.0	068
11	-	0.425	1070	0.017
12	-	1.00	-	0.039

Dimension in mm/inches



Notice

Precaution on using ROHM Products

1. Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CL ACCTI	CLASSIIb	СГУССШ
CLASSIV	CLASSII	CLASSⅢ	CLASSIII

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - [a] Installation of protection circuits or other protective devices to improve system safety
 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3. Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

QR code printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

Precaution Regarding Intellectual Property Rights

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UT6MA3 - Web Page

Distribution Inventory

Part Number	UT6MA3	
Package	HUML2020L8(Dual)	
Unit Quantity	3000	
Minimum Package Quantity	3000	
Packing Type	Taping	
Constitution Materials List	inquiry	
RoHS	Yes	