Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!
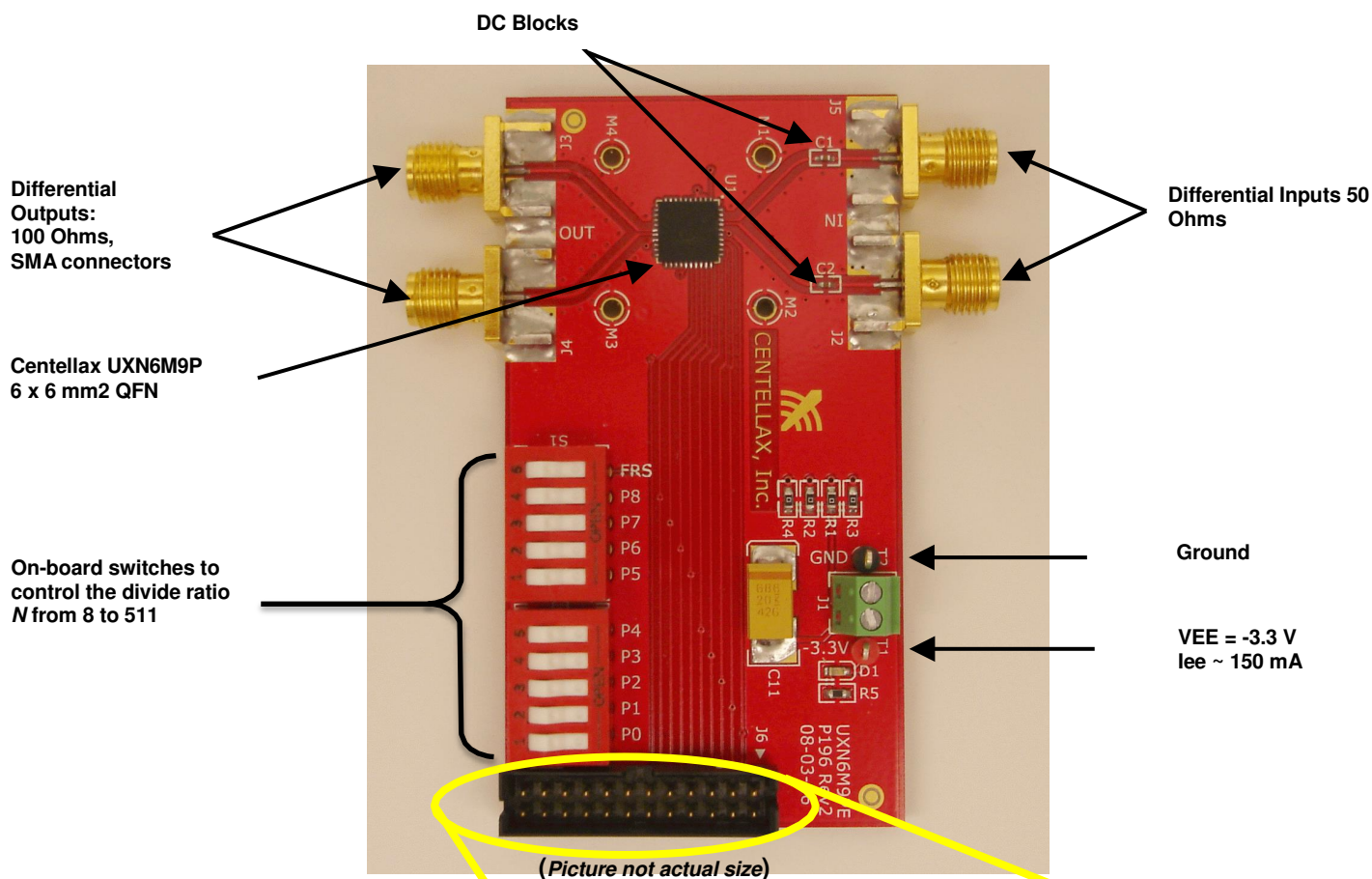
Contact us

UXN6M9PE Eval Board

The divide ratio *N* is given by the following formula:

$$N = \sum_{k=0}^{8} P^k \cdot 2^k = P^0 + P^1 \cdot 2 + P^2 \cdot 2^2 + P^3 \cdot 2^3 + \cdots + P^8 \cdot 2^8$$
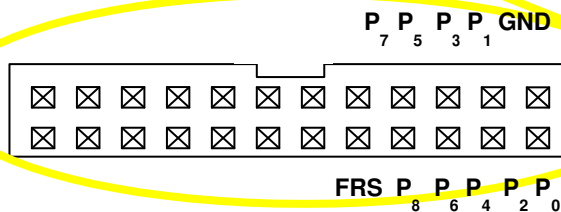
Where P_k = 0 (Low) or 1 (High); valid for *N* = 8, 9… 511

DC Blocks

Differential Outputs: 100 Ohms, SMA connectors

Differential Inputs 50 Ohms

Centellax UXN6M9P 6 x 6 mm2 QFN

On-board switches to control the divide ratio *N* from 8 to 511

Ground

VEE = -3.3 V Iee ~ 150 mA

(*Picture not actual size*)

P_7 P_5 P_3 P_1 GND

FRS P_8 P_6 P_4 P_2 P_0

(All other pins are not used.)

<u>**Dimensions**</u>

With connectors: 2.306" x 2.865" (58.57 mm x 72.79 mm)
Without connectors: 1.546" x 2.865" (39.28 mm x 72.79

Note 1: Terminate un-used output in single-ended applications
Note 2: High = 0 V; closed Low = -3.3 V; open
Note 3: When using the parallel programming interface control, <u>all</u> on-board switches must be kept in the 'open' position. 'Open' corresponds to logic 'Low' or 0. Refer to the datasheet for logic level specs.
Note 4: Set FRS=High for Fin > 6 GHz

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