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RoHS COMPLIANT

HALOGEN

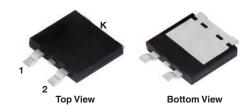
**FREE** 



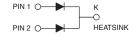
### Vishay General Semiconductor

## **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

### TMBS® eSMP® Series TO-263AC (SMPD)



#### V30D202C



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 15.0 A				
$V_{RRM}$	200 V				
I <sub>FSM</sub>	260 A				
V <sub>F</sub> at I <sub>F</sub> = 15.0 A (T <sub>A</sub> = 125 °C)	0.66 V				
T <sub>J</sub> max.	175 °C				
Package	TO-263AC (SMPD)				
Diode variations	Dual common cathode				

#### **FEATURES**

- Trench MOS Schottky technology generation 2
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-263AC (SMPD)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V30D202C	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	200	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	30	Δ.	
	per diode		15	A	
Maximum DC reverse voltage		$V_{DC}$	160	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	260	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +175	°C	



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.72	-		
	I <sub>F</sub> = 10 A			0.78	-		
	I <sub>F</sub> = 15 A			0.8	0.88	V	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.56	-		
	I <sub>F</sub> = 10 A			0.64	-		
	I <sub>F</sub> = 15 A			0.66	0.73		
Reverse current at rated V <sub>R</sub> per diode	V <sub>R</sub> = 160 V	T <sub>A</sub> = 25 °C	- I <sub>R</sub> <sup>(2)</sup>	1	-	μA	
		T <sub>A</sub> = 125 °C		2	-	mA	
	V <sub>R</sub> = 200 V	T <sub>A</sub> = 25 °C		- 1	200	μΑ	
		T <sub>A</sub> = 125 °C		5	25	mA	

#### **Notes**

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V30D202C	UNIT
	per diode	- R <sub>θJC</sub>	2.0	°C/W
Typical thermal resistance	per device		1.1	
	per device	R <sub>0</sub> JA (1)(2)	50	

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$  - junction-to-mount

(2) Free air, without heatsink

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AC (SMPD)	V30D202C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel
TO-263AC (SMPD)	V30D202CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel

#### Note

### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

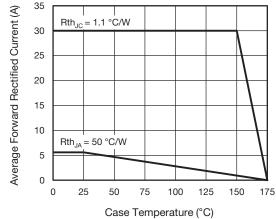


Fig. 1 - Forward Current Derating Curve

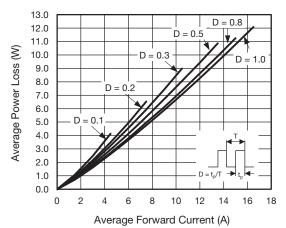


Fig. 2 - Forward Power Loss Characteristics

<sup>(1)</sup> AEC-Q101 qualified

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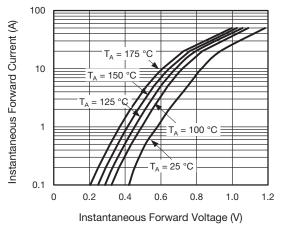


Fig. 3 - Typical Instantaneous Forward Characteristics

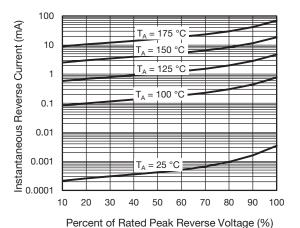


Fig. 4 - Typical Reverse Characteristics

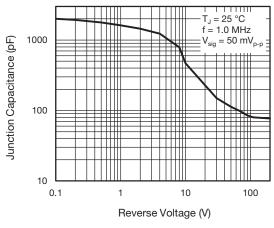


Fig. 5 - Typical Junction Capacitance

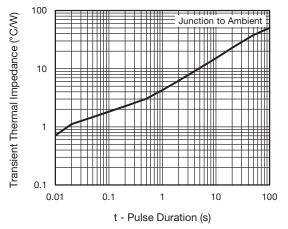


Fig. 6 - Typical Transient Thermal Impedance

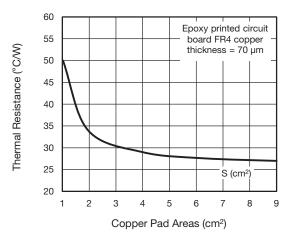
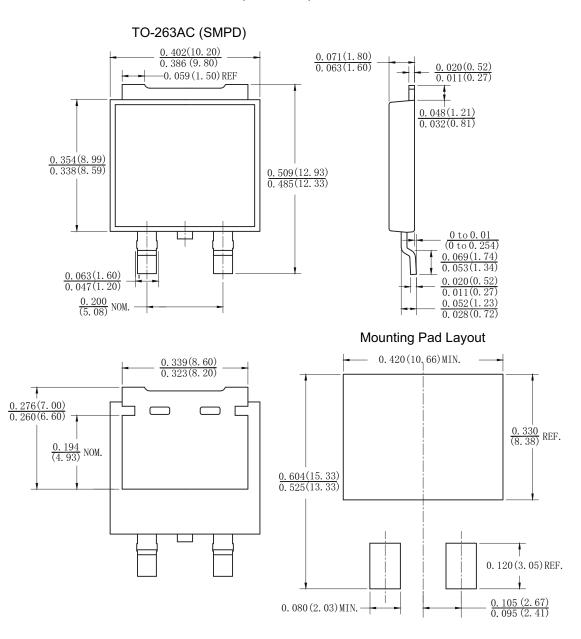


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas



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### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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