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Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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LVCMOS VCXO **Specification** Model V802

CONNO MINFIE

Description:

The Connor-Winfield, RoHS compliant, V802 is a 3.3V Voltage Controlled Crystal Oscillator (VCXO) with LVCMOS output and enable/disable function. The V802 is designed for use with applications utilizing a PLL system requiring low jitter and tight frequency stability. The surface mount package is designed for highdensity mounting and is optimum for mass production.



3.3 Vdc Operation Absolute Pull Range (APR): ± 50 ppm Temperature Range: 0 to 70°C Low Jitter: <0.5ps RMS LVCMOS Output 5x7 mm SMT Package Tri-State Enable/Disable, Pad 2 Tape and Reel Packaging **RoHS Compliant / Lead Free**

Features:

Absolute Maximum Ratings

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-40	-	85	°C	
Supply Voltage (Vcc)	-0.5	-	4.6	Vdc	
Control Voltage (Vc)	-0.5	-	Vcc + 0.5	Vdc	

Operating Specifications

Parameter	Minimum	Nominal	Maximum	Units	Notes
Center Frequency: (Fo)	65	-	130	MHz	
Operating Temp Range:	0	-	70	°C	
Supply Voltage: (Vcc)	3.135	3.3	3.465	Vdc	
Supply Current :(Icc)	-	-	40	mA	
Jitter:					
Period Jitter	-	3.0	5.0	ps RMS	
Integrated Phase Jitter	-	0.5	1.0	ps RMS	1
Typical SSB Phase Noise @ 100	MHz				
@ 10 Hz offset	-	-55	-	dBc/Hz	
@ 100 Hz offset	-	-85	-	dBc/Hz	
@ 1 KHz offset	-	-115	-	dBc/Hz	
@ 10 KHz offset	-	-135	-	dBc/Hz	
@ 100 KHz offset	-	-150	-	dBc/Hz	
@ 1 MHz offset	-	-150	-	dBc/Hz	
Start-Up Time	-	-	10	ms	

Input Characteristics

	-					
Parameter	Minimum	Nominal	Maximum	Units	Notes	
Control Voltage Range (Vc)	0.3	1.65	3.0	Vdc		
Absolute Pull Range (APR)	±50	-	-	ppm	2	
Monotonic Linearity	-10	-	10	%		
DC Input Impedance	-	60K	-	Ohm	3	
Modulation Bandwidth (3dB)	10	-	-	KHz		
Enable Input Voltage (High) (Vih) 2.4	-	-	V	4	
Disable Input Voltage (Low) (Vil)	_	-	0.4	V	4	

LVCMOS Output Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load -	-	15	-	pF	
Voltage (High) Voh	2.4	-	-	V	
(Low) Vol	-	-	0.4	V	
Duty Cycle at 50% Level	45	50	55	%	
Rise / Fall Time: 20% to 80%	-	-	2	ns	

Package Characteristics

Package Hermetically sealed ceramic surface mount package with case ground metal cover

Notes:

BW= 12 KHz to 20 MHz,
BW= 12 KHz to 20 MHz,
Absolute Pull Range (APR) is the minimum guaranteed pull range of the VCXO under all conditions over lifetime operation including calibration @ 25°C, frequency stability vs. the change in temperature, frequency vs. change in supply voltage, frequency vs. change in load, shock and vibration and 10 years aging. The APR is referenced to (Fo). Positive transfer function.
Measured from pin 1 to ground.

4. Oscillator output is enabled with no connection on pad 2.

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Bulletin	VX51 4
Page	1 of 2
Revision	04
Date	21 Apr 2011



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Environmental Characteristics

Vibration:	Vibration per Mil Std 883E Method 2007.3 Test Condition A.
Shock:	Mechanical Shock per Mil Std 883E Method 2002.4 Test Condition B.
Soldering Proc	ess; RoHS compliant lead free. See soldering profile on page 2.



Typical Phase Noise



Test Circuit



Tape and Reel Dimensions



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Ordering Information



Suggested Pad Layout



Pad Connections

_1:	Control Voltage (Vc)
2	Enable / Disable
3:	Ground
4:	Output Q
5:	N/C

6: Supply Voltage (Vcc)

Enable / Disable Function

Function:	Outputs
High or Open	Enabled
Low	Disabled (High Impedance

Output Waveform



Solder Profile

