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## RJK5033DPD

Silicon N Channel MOS FET High Speed Power Switching

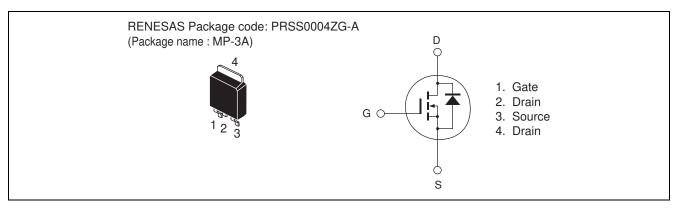
## R07DS0179EJ0100

Datasheet

#### Features

- Low on-state resistance  $R_{DS(on)} = 0.96 \ \Omega \ typ. \ (I_D = 3 \ A, \ V_{GS} = 10 \ V, \ Ta = 25^{\circ}C)$
- High speed switching

### Outline



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	500	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	Ι <sub>D</sub>	6	A
Drain peak current	I <sub>D (pulse)</sub> Note1	24	A
Avalanche current	I <sub>AP</sub> <sup>Note3</sup>	6	A
Channel dissipation	Pch Note 2	65	W
Channel to case thermal Impedance	θ ch-c	1.92	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	٥C

Notes: 1. Pulse width limited by safe operating area.

2. Value at Tc = 25°C

3. STch = 25°C, Tch  $\leq 150^{\circ}C$ 



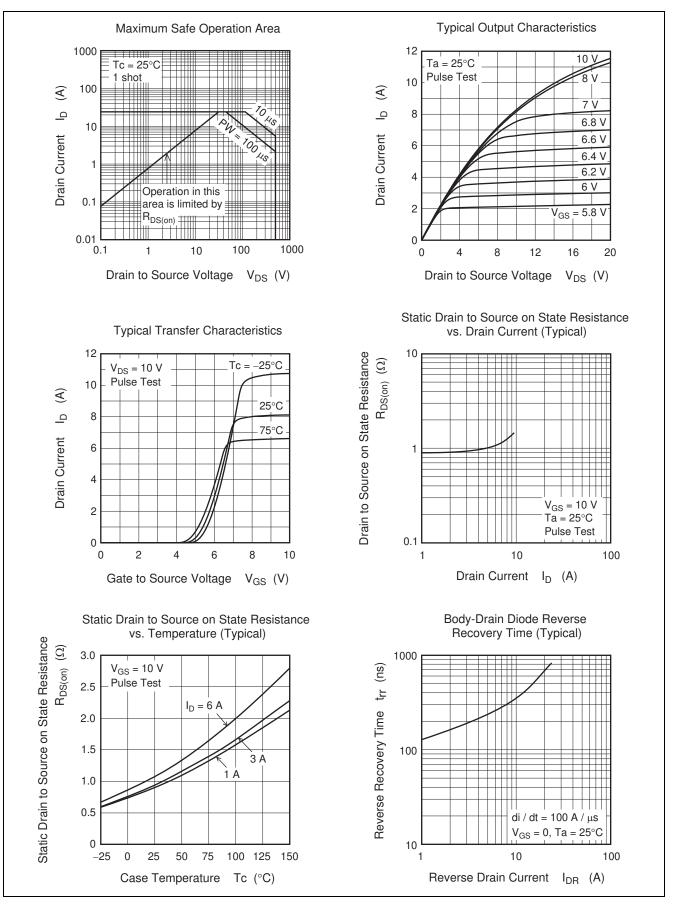
### **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	500	—	—	V	$I_{D} = 1 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	1	μA	$V_{DS} = 505 V, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>		—	±0.1	μA	$V_{GS}=\pm 30~V,~V_{DS}=0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	3.5	—	4.5	V	$V_{DS}$ = 10 V, $I_D$ = 1 mA
Static drain to source on state resistance	R <sub>DS (on)</sub>		0.96	1.3	Ω	$I_D = 3 \text{ A}, V_{GS} = 10 \text{ V}^{Note 4}$
Input capacitance	Ciss		600	—	pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss		70	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		10	—	pF	f = 1 MHz
Turn-on delay time	t <sub>d(on)</sub>		15	—	ns	V <sub>DD</sub> = 200 V
Rise time	tr		20	—	ns	I <sub>D</sub> = 3 A
Turn-off delay time	t <sub>d (off)</sub>	—	90	—	ns	$V_{GS} = 10 V$
Fall time	t <sub>f</sub>		30	—	ns	Rg = 10 Ω
Body-drain diode forward voltage	$V_{DF}$	—	0.9	1.5	V	$I_F = 6 A, V_{GS} = 0^{Note 4}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	250	—	ns	$I_F = 6 A, V_{GS} = 0$
						V <sub>DD</sub> = 250 V
						$di_F/dt = 100 \text{ A}/\mu \text{s}$

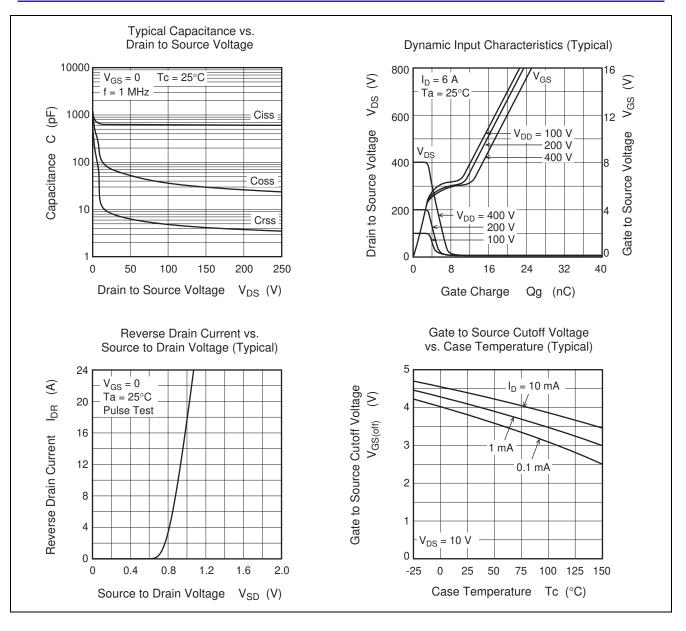
Note: 4. Pulse test



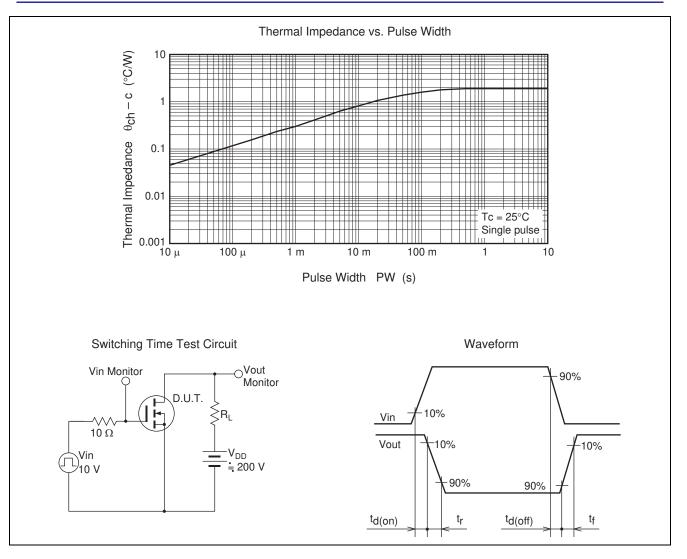
#### **Main Characteristics**





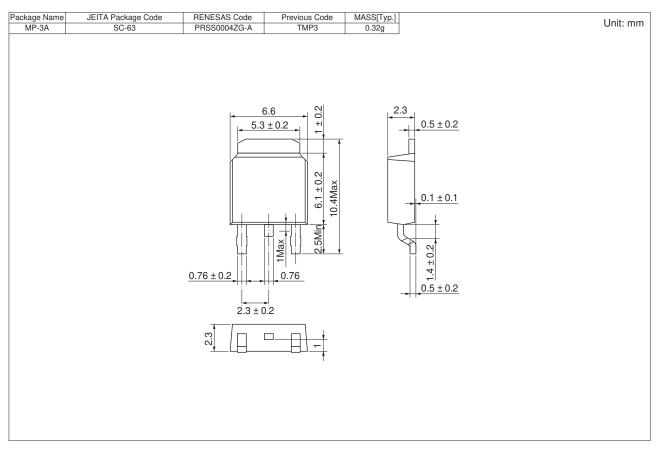








#### **Package Dimensions**



### **Ordering Information**

Part No.	Quantity	Shipping Container
RJK5033DPD-00-J2	3000 pcs	Taping



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