

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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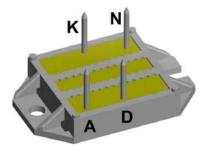
Standard Rectifier Module

1~ Rectifier				
V_{RRM}	=	1600 V		
I_{DAV}	=	70 A		
I _{FSM}	=	550 A		

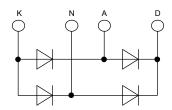
1~ Rectifier Bridge

Part number

VBO68-16NO7







Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

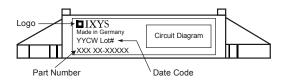
- Diode for main rectification
- For one phase bridge configurations
 Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors
- Package: ECO-PAC1
- · Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Height: 9 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling



Rectifie	r				Ratings	3	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	king voltage	$T_{VJ} = 25^{\circ}C$			1700	V
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1600	V
I _R	reverse current	V _R = 1600 V	$T_{VJ} = 25^{\circ}C$			40	μΑ
		V _R = 1600 V	$T_{VJ} = 150^{\circ}C$			1.5	mΑ
V _F	forward voltage drop	I _F = 30 A	$T_{VJ} = 25^{\circ}C$			1.14	V
		$I_F = 60 \text{ A}$				1.32	V
		I _F = 30 A	T _{VJ} = 125 °C			1.06	V
		$I_F = 60 \text{ A}$				1.30	٧
IDAV	bridge output current	T _c = 105°C	T _{VJ} = 150°C			70	Α
		rectangular d = 0.5					
V _{F0}	threshold voltage		T _{vJ} = 150°C			0.81	V
r _F	slope resistance \(\) for power	loss calculation only				7.8	mΩ
R _{thJC}	thermal resistance junction to ca	ase				0.9	K/W
R _{thCH}	thermal resistance case to heats	sink			0.4		K/W
P _{tot}	total power dissipation		T _C = 25°C			135	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			550	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			595	Α
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			470	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			505	Α
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			1.52	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.48	kA²s
		t = 10 ms; (50 Hz), sine	T _{VJ} = 150°C			1.11	kA2s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.06	kA²s
C	junction capacitance	$V_R = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		18		pF



Package ECO-PAC1					Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit	
I _{RMS}	RMS current	per terminal				100	Α	
T _{stg}	storage temperature			-40		125	°C	
T _{VJ}	virtual junction temperature		-40		150	°C		
Weight					19		g	
M _D	mounting torque			1.5		2	Nm	
d _{Spp/App}	creepage distance on surface striking distance through air		terminal to terminal	6.0			mm	
d _{Spb/Apb}			terminal to backside	10.0			mm	
V _{ISOL}	isolation voltage	t = 1 second		3000			V	
		t = 1 minute	50/60 Hz, RMS; I _{ISOL} ≤ 1 mA	2500			V	

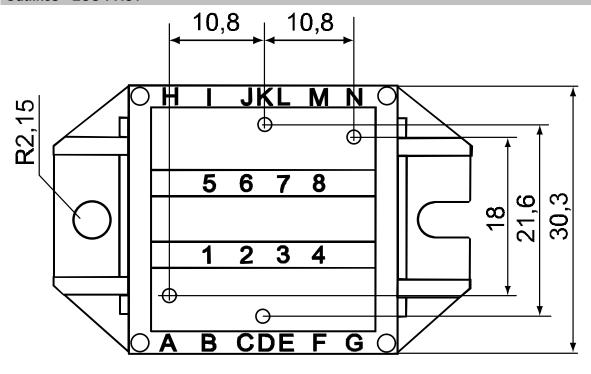


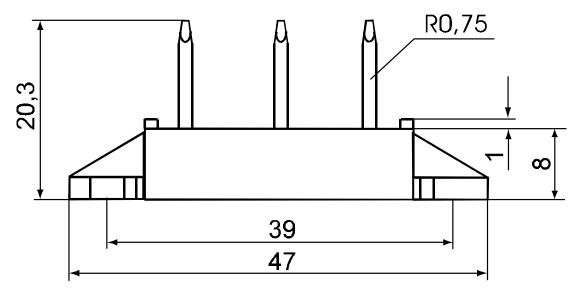
Orderi	g Pai	t Number Mari	king on Product Deli	ivery Mode Qu	uantity	Code No.
Standa	d VBC	68-16NO7 VI	BO68-16NO7	Box	25	479586

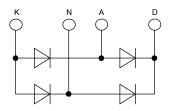
Equivalent Circuits for Simulation			* on die level	T _{VJ} = 150 °C
$I \rightarrow V_0$	R_0	Rectifier		
V _{0 max}	threshold voltage	0.81		V
$R_{0\text{max}}$	slope resistance *	6.6		$m\Omega$



Outlines ECO-PAC1

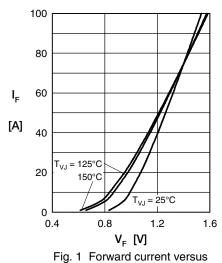


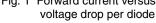






Rectifier





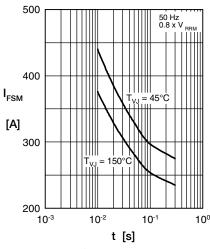


Fig. 2 Surge overload current

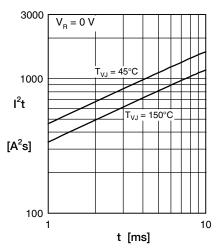


Fig. 3 I²t versus time per diode

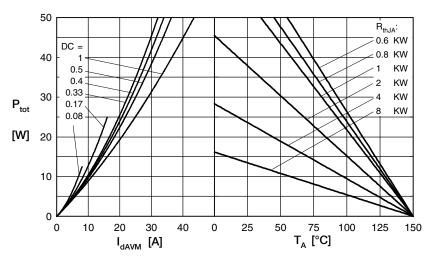


Fig. 4 Power dissipation vs. direct output current & ambient temperature

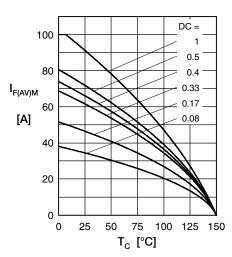


Fig. 5 Max. forward current vs. case temperature

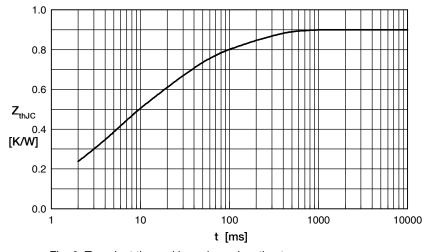


Fig. 6 Transient thermal impedance junction to case

Constants for \mathbf{Z}_{thJC} calculation:

i	R_{th} (K/W)	t _i (s)
1	0.0607	0.000
2	0.1230	0.00256
3	0.2330	0.0045
4	0.3230	0.0242
5	0.1628	0.18