## imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



### VEC2616

# Power MOSFET 60V, $80m\Omega$ , 3A, -60V, $137m\Omega$ , -2.5A, Complementary

This Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

#### Features

- Low On-Resistance
- 4V drive
- Low-Profile Package
- Complementary N-Channel and P-Channel MOSFET
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance

#### **Typical Applications**

• Motor Driver

#### SPECIFICATIONS

#### ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	N-Channel	P-Channel	Unit	
Drain to Source Voltage	VDSS	60	-60	V	
Gate to Source Voltage	VGSS	±20	±20	V	
Drain Current (DC)	ID	3	-2.5	Α	
Drain Current (Pulse) PW $\leq 10\mu$ s, duty cycle $\leq 1\%$	IDP	12	-10	А	
Power Dissipation When mounted on ceramic substrate ( $900mm^2 \times 0.8mm$ ) 1unit	PD	C	w		
Total Dissipation When mounted on ceramic substrate ( $900mm^2 \times 0.8mm$ )	ΡŢ	1.0		w	
Junction Temperature	Tj	150		°C	
Storage Temperature	Tstg	–55 te	°C		
Note 4. Other second and the second in the Maximum Defining table were demonstrated					

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm <sup>2</sup> $\times$ 0.8mm) 1unit	R <sub>θJA</sub>	138.8	°C/W

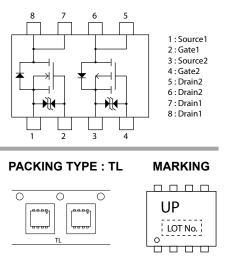


#### **ON Semiconductor®**

www.onsemi.com

-			
VDSS	R <sub>DS</sub> (on) Max	ID Max	
N-Ch	80mΩ@ 10V		
60V	106mΩ@ 4.5V	3A	
	116mΩ@ 4V		
P-Ch	137mΩ@ –10V		
-60V	180mΩ@ –4.5V	-2.5A	
	194mΩ@ –4V		

#### ELECTRICAL CONNECTION N-Channel and P-Channel



**ORDERING INFORMATION** See detailed ordering and shipping information on page 7 of this data sheet.

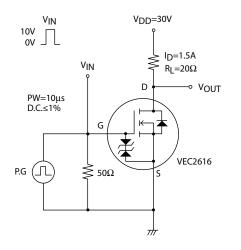
#### **ELECTRICAL CHARACTERISTICS** at $Ta = 25^{\circ}C$ (Note 2)

Parameter	Symbol	Conditions	Value			Unit
	,		min	typ	max	
[N-Channel]						
Drain to Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	60			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	μA
Gate to Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μA
Gate Threshold Voltage	VGS(th)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.2		2.6	V
Forward Transconductance	9FS	V <sub>DS</sub> =10V, I <sub>D</sub> =1.5A		2.6		S
Static Drain to Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =1.5A, V <sub>GS</sub> =10V		62	80	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =0.75A, V <sub>GS</sub> =4.5V		76	106	mΩ
	R <sub>DS</sub> (on)3	I <sub>D</sub> =0.75A, V <sub>GS</sub> =4V		83	116	mΩ
Input Capacitance	Ciss			505		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		57		pF
Reverse Transfer Capacitance	Crss			37		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			7.3		ns
Rise Time	tr			7.5		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		41		ns
Fall Time	tf			22		ns
Total Gate Charge	Qg			10		nC
Gate to Source Charge	Qgs	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =3A		1.6		nC
Gate to Drain "Miller" Charge	Qgd			2.1		nC
Forward Diode Voltage	V <sub>SD</sub>	IS=3A, VGS=0V		0.81	1.2	V
[P-Channel]						
Drain to Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-60			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V			-1	μA
Gate to Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μA
Gate Threshold Voltage	VGS(th)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.2		-2.6	V
Forward Transconductance	9FS	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1.5A		3.9		S
	R <sub>DS</sub> (on)1	ID=-1.5A, VGS=-10V		105	137	mΩ
Static Drain to Source On-State	R <sub>DS</sub> (on)2	ID=-0.75A, VGS=-4.5V		128	180	mΩ
Resistance	R <sub>DS</sub> (on)3	ID=-0.75A, VGS=-4V		138	194	mΩ
Input Capacitance	Ciss			420		pF
Output Capacitance	Coss	V <sub>DS</sub> =–20V, f=1MHz		54		pF
Reverse Transfer Capacitance	Crss			44		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			6.4		ns
Rise Time	tr			9.8		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		65		ns
Fall Time	tf	1		36		ns
Total Gate Charge	Qg			11		nC
Gate to Source Charge	Qgs	VDS=-30V, VGS=-10V, ID=-2.5A		1.4		nC
Gate to Drain "Miller" Charge	Qgd			2		nC
······································	VSD	l		-		

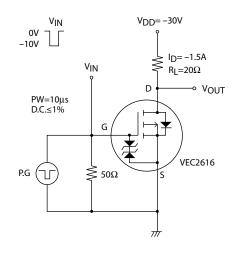
Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

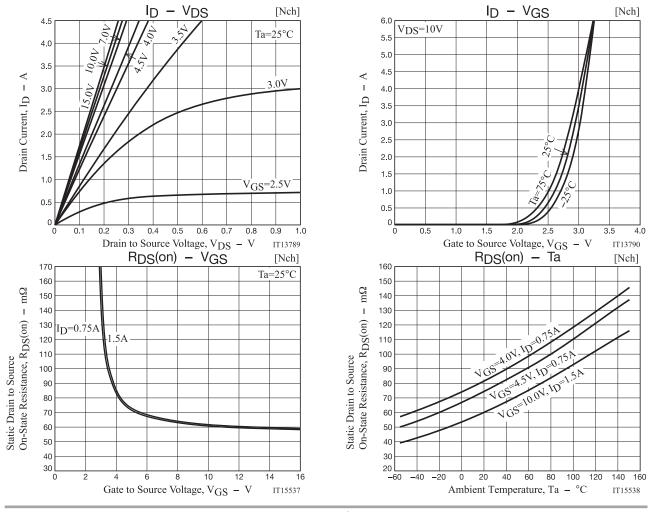
#### **VEC2616**

## Switching Time Test Circuit [N-Channel]



#### [P-Channel]





www.onsemi.com 3 **VEC2616** 

[Nch]

1.2

60

IT13796

[Nch]

5 7 100

IT15910

[Pch]

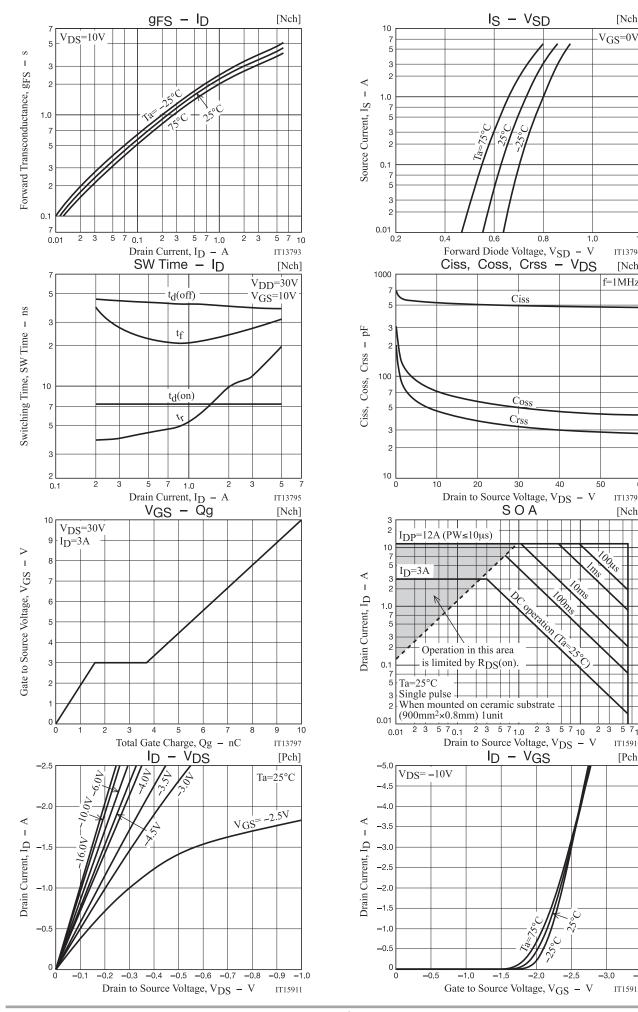
-3.5

IT15912

IT13794

[Nch]

f=1MHz



www.onsemi.com 4

[Pch]

IT15914

[Pch]

V<sub>GS</sub>=0V

-1.0

-1.2

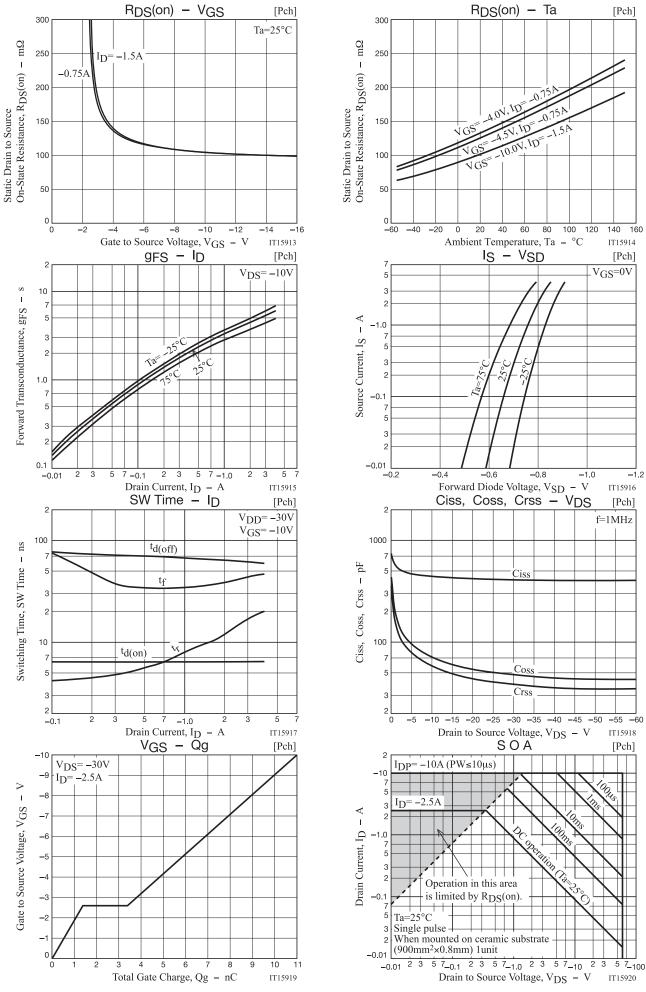
IT15916

[Pch]

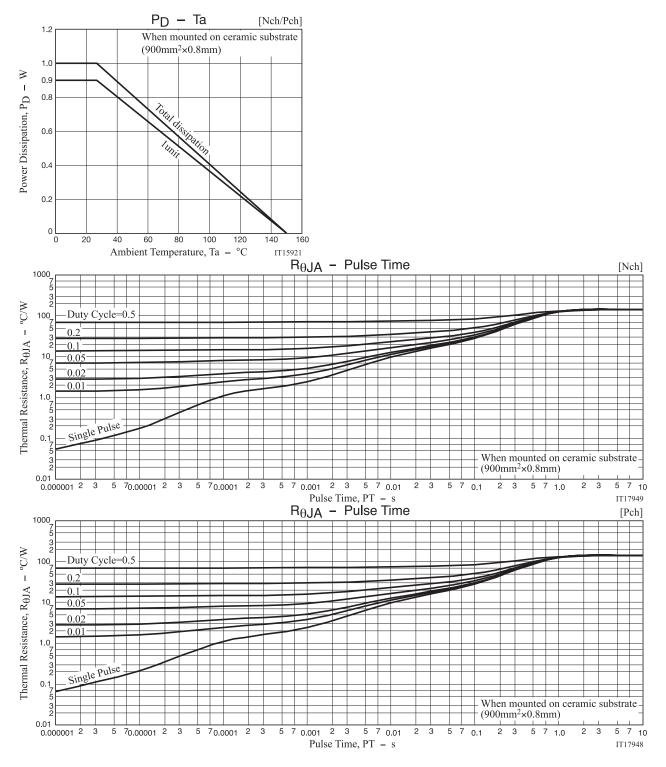
IT15918

[Pch]

f=1MHz

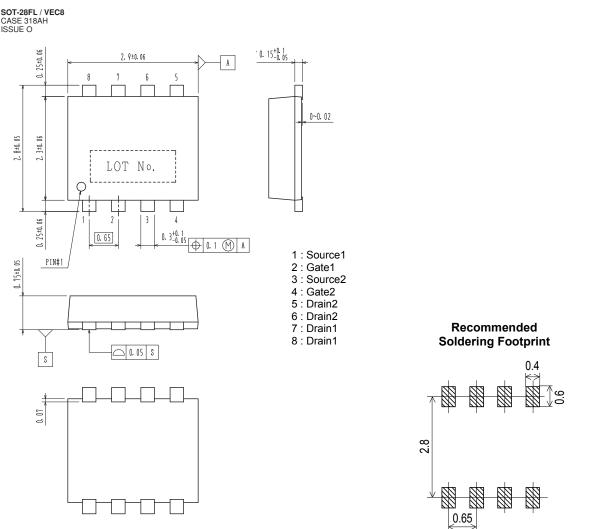






#### PACKAGE DIMENSIONS

#### unit : mm



#### **ORDERING INFORMATION**

Device	Marking	Package	Shipping (Qty / Packing)	
VEC2616-TL-H		SOT-28FL / VEC8	2 000 / Taras & Daal	
VEC2616-TL-W	UP	(Pb-Free / Halogen Free)	3,000 / Tape & Reel	

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub\_link/Collateral/BRD8011-D.PDF

## Note on usage : Since the VEC2616 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product cault create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly, or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.