## imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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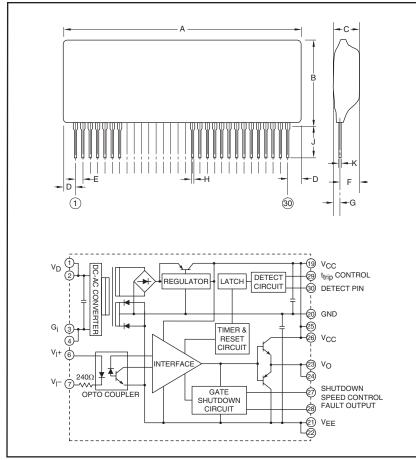
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Powerex, Inc., 173 Pavilion Lane, Youngwood, Pennsylvania 15697 (724) 925-7272 www.pwrx.com

#### Hybrid IC IGBT Gate Driver + DC/DC Converter



#### **Outline Drawing and Circuit Diagram**

Dimensions	Inches	Millimeters
А	3.46 Max.	88.0 Max.
В	1.65 Max.	42.0 Max.
С	0.67 Max.	17.0 Max.
D	0.31 Max.	8.0 Max.
E	0.1	2.54
F	0.45 Max.	11.5 Max.
G	0.24 Max.	6.0 Max.
Н	0.03±0.004	0.75±0.1
J	0.14±0.04	3.5±1.0
K	0.028 Max.	0.7 Max.

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#### **Description:**

VLA539-01R is a hybrid integrated circuit designed for driving IGBT modules. This device is a fully isolated gate drive circuit consisting of an optically isolated gate drive amplifier and an isolated DC-to-DC converter. The gate driver provides an over-current protection function based on desaturation detection.

#### Features:

- Built-in Isolated DC-DC Converter for Gate Drive
- SIP Outline Allows More Space on Mounting Area
- Built-in Short Circuit Protection (With a Pin for Fault Output)
- □ Variable Fall Time on Short-Circuit Protection
- □ Electrical Isolation Voltage Between Input and Output (4000 V<sub>rms</sub> for 1 Minute)
- □ CMOS, TTL Compatible Input

#### Application:

To Drive IGBT modules for general industrial use apparatus.

#### **Recommended IGBT Modules:**

 $V_{CES} = 600V$  Series Up to 600A  $V_{CES} = 1200V$  Series Up to 3600A  $V_{CES} = 1700V$  Series Up to 3600A



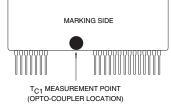
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VLA539-01R Hybrid IC Gate Driver + DC/DC converter

#### Absolute Maximum Ratings, $T_a = 25^{\circ}C$ unless otherwise specified

Symbol	VLA539-01R	Units
VD	-1 ~ 16.5	Volts
Vi	-1 ~ 7	Volts
I <sub>OHP</sub>	-24	Amperes
I <sub>OLP</sub>	24	Amperes
V <sub>ISO</sub>	4000	V <sub>rms</sub>
T <sub>C1</sub>	85	°C
T <sub>C2</sub>	100	°C
T <sub>opr</sub>	-20 to 60	°C
T <sub>stg</sub>	-25 to 100*	°C
I <sub>FO</sub>	20	mA
V <sub>R30</sub>	60	Volts
I <sub>drive</sub>	210**	mA
	VD   Vi   IOHP   IOLP   VISO   TC1   TC2   Topr   Tstg   IFO   VR30	$\begin{tabular}{ c c c c c } \hline V_D & -1 ~ 16.5 \\ \hline V_i & -1 ~ 7 \\ \hline \hline I_{OHP} & -24 \\ \hline I_{OLP} & 24 \\ \hline V_{ISO} & 4000 \\ \hline T_{C1} & 85 \\ \hline T_{C2} & 100 \\ \hline T_{opr} & -20 to 60 \\ \hline T_{stg} & -25 to 100^* \\ \hline I_{FO} & 20 \\ \hline V_{R30} & 60 \\ \hline \end{tabular}$

\*Differs from temperature cycle condition. \*\*Refer to I<sub>drive</sub> VS. T<sub>a</sub> CHARACTERISTICS (TYPICAL) graph. (Needs Derating) \*\*\*T<sub>C1</sub> Measurement Point (opto-coupler location)





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VLA539-01R Hybrid IC Gate Driver + DC/DC converter

#### **Electrical and Mechanical Characteristics**,

#### $T_a$ = 25°C unless otherwise specified, $V_D$ = 15V, $R_G$ = 10, CL = 1.6 \mu F, f= 3 kHz

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Supply Voltage	VD	Recommended Range	14.2	15	15.8	Volts
Pull-up Voltage on Input Side	V <sub>IN</sub>	Recommended Range	4.75	5	5.25	Volts
Input Signal Current	IIН	Recommended Range	10	12	16	mA
Switching Frequency	f	Recommended Range	_	_	10	kHz
Gate Resistance	R <sub>G</sub>	Recommended Range	0.33	_	_	Ω
Input Signal Current	IIH	V <sub>IN</sub> = 5V, HCMOS Drive	_	12	_	mA
Gate Positive Supply Voltage	V <sub>CC</sub>	_	15.2	16.5	17.5	Volts
Gate Negative Supply Voltage	V <sub>EE</sub>	_	-6	-8	-11.5	Volts
Gate Supply Efficiency	E <sub>ta</sub>	Load Current = 210mA	60	75	_	%
		$E_{ta} = (V_{CC} +  V_{EE} ) \times 0.21 / (15 \times I_D) \times 100$				
"H" Output Voltage	V <sub>OH</sub>	$10k\Omega$ Connected Between Pin 23-20	14	15.3	16.5	Volts
"L" Output Voltage	V <sub>OL</sub>	$10k\Omega$ Connected Between Pin 23-20	-5.5	-7	-11	Volts
"L-H" Propagation Time	t <sub>PLH</sub>	I <sub>IH</sub> = 12mA	0.5	0.9	1.5	μs
"L-H" Rise Time	t <sub>r</sub>	I <sub>IH</sub> = 12mA	_	0.6	1.2	μs
"H-L" Propagation Time	t <sub>PHL</sub>	I <sub>IH</sub> = 12mA	0.5	1.0	1.5	μs
"H-L" Fall Time	t <sub>f</sub>	I <sub>IH</sub> = 12mA	_	0.3	1.2	μs
Timer	t <sub>timer</sub>	Between Start and Cancel	1	_	2	ms
		(Under Input Sign "L")				
Fault Output Current	I <sub>FO</sub>	Applied Pin 28, $R = 4.7 k\Omega$	_	5		mA
Controlled Time Detect Short-Circuit 1	<sup>t</sup> trip1	Pin 30 : 15V and More, Pin 29 : Open	_	3.5	_	μs
Controlled Time Detect Short-Circuit 2*	t <sub>trip2</sub>	Pin 30 : 15V and More, Pin 29-21, 22 : 10pF	_	3.9	_	μs
		(Connective Capacitance)				
SC Detect Voltage	V <sub>SC</sub>	Collector Voltage of IGBT	15	_	_	Volts

\*Length of wiring from  $C_{trip}$  to Pins 21, 22, and 29 must be less than 5cm.