# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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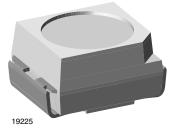
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## Power SMD LED PLCC-2



#### DESCRIPTION

This device has been designed to meet the increasing demand for white SMD LED.

The package of the VLMW33.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled with a mixture of epoxy and TAG phosphor.

The TAG phosphor converts the blue emission partially to yellow, which mixes with the remaining blue to give white.

#### **PRODUCT GROUP AND PACKAGE DATA**

- Product group: LED
- Package: SMD PLCC-2
- Product series: power
- Angle of half intensity: ± 60°

#### **FEATURES**

- High efficient InGaN technology
- Chromaticity coordinate categorized according to CIE1931 per packing unit
- Typical color temperature 5500 K
- · ESD-withstand voltage: up to 1 kV according to JESD22-A114-B
- EIA and ICE standard package
- · Compatible with IR-reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape reel
- Preconditioning according to JEDEC<sup>®</sup> level 2a
- AEC-Q101 gualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- Automotive: backlighting in dashboards and switches
- Telecommunication: indicator and backlighting in telephone and fax
- Backlighting for audio and video equipment
- Backlighting in office equipment
- Indoor and outdoor message boards
- Flat backlight for LCDs, switches, and symbols
- · Illumination purposes, alternative to incandescent lamps
- General use

PARTS TABLE														
PART	COLOR		JMINO TENSI (mcd)	ΤY	at I <sub>F</sub> (mA)	CO	ORDIN (x, y)	ATE	at I <sub>F</sub> (mA)		ORWAI OLTAC (V)		at I <sub>F</sub> (mA)	TECHNOLOGY
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMW33S2V1-5K8L-08	White	224	635	900	20	-	0.33, 0.33	-	20	-	3.7	4.2	20	InGaN / TAG on SiC
VLMW33S2V1-5K8L-18	White	224	635	900	20	-	0.33, 0.33	-	20	-	3.7	4.2	20	InGaN / TAG on SiC
VLMW33T2U2-5K8L-08	White	355	615	710	20	-	0.33, 0.33	-	20	-	3.7	4.2	20	InGaN / TAG on SiC
VLMW33T2U2-5K8L-18	White	355	615	710	20	-	0.33, 0.33	-	20	-	3.7	4.2	20	InGaN / TAG on SiC
VLMW33U2AA-5K8L-08	White	560	650	1400	20	-	0.33, 0.33	-	20	-	3.7	4.2	20	InGaN / TAG on SiC
VLMW33U2AA-5K8L-18	White	560	650	1400	20	-	0.33, 0.33	-	20	-	3.7	4.2	20	InGaN / TAG on SiC
VLMW33T2AA-5K8L-08	White	355	635	1400	20	-	0.33, 0.33	-	20	-	3.7	4.2	20	InGaN / TAG on SiC
VLMW33T2AA-5K8L-18	White	355	635	1400	20	-	0.33, 0.33	-	20	-	3.7	4.2	20	InGaN / TAG on SiC





e:-

FREE **GREEN** (5-2008)

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## **Vishay Semiconductors**

ABSOLUTE MAXIMUM RAT VLMW33	<b>INGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwis	e specified)		
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage (1)		V <sub>R</sub>	5	V
DC forward current	T <sub>amb</sub> ≤ 70 °C	I <sub>F</sub>	30	mA
Surge forward current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	0.1	A
Power dissipation		P <sub>V</sub>	127	mW
Junction temperature		Тj	110	°C
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C
Operating temperature range		T <sub>amb</sub>	-40 to +100	°C
Thermal resistance junction / ambient	Mounted on PC board (pad size > 16 mm <sup>2</sup> )	R <sub>thJA</sub>	400	K/W

#### Note

<sup>(1)</sup> Driving the LED in reverse direction is suitable for short term application

## **OPTICAL AND ELECTRICAL CHARACTERISTICS** ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified) **VLMW33...** WHITE

			1		1	1	1
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN	TYP.	MAX	UNIT
		VLMW33S2V1	Ι <sub>V</sub>	224	635	900	mcd
	I 20 mA	VLMW33T2U2	Ι <sub>V</sub>	355	615	710	mcd
Luminous intensity	I <sub>F</sub> = 20 mA	VLMW33U2AA	Ι <sub>V</sub>	560	650	1400	mcd
		VLMW33T2AA	Ι <sub>V</sub>	355	635	1400	mcd
Chromaticity coordinate x acc. to CIE 1931	I <sub>F</sub> = 20 mA	VLMW33	х	-	0.33	-	
Chromaticity coordinate y acc. to CIE 1931	I <sub>F</sub> = 20 mA	VLMW33	у	-	0.33	-	
Angle of half intensity	I <sub>F</sub> = 20 mA		φ	-	± 60	-	deg
Forward voltage	I <sub>F</sub> = 20 mA		V <sub>F</sub>	-	3.7	4.2	V
Reverse voltage	I <sub>R</sub> = 10 μA		V <sub>R</sub>	5	-	-	V
Temperature coefficient of $V_F$	I <sub>F</sub> = 20 mA		TC <sub>VF</sub>	-	-4	-	mV/K
Temperature coefficient of ${\rm I}_{\rm V}$	I <sub>F</sub> = 20 mA		TCIV	-	-0.5	-	%/K

LUMINOUS	JS INTENSITY CLASSIFICATIONLIGHT INTENSITY (mcd)OPTIONALMIN.1180224						
GROUP	LIGH	LIGHT INTENSITY (mcd)					
STANDARD	OPTIONAL	MIN.	MAX.				
S	1	180	224				
3	2	224	280				
Ŧ	1	280	355				
1	2	355	450				
U	1	450	560				
0	2	560	710				
V	1	710	900				
v	2	900	1120				
AA	1	1120	1400				

#### Note

• Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of  $\pm$  11 %.

The above type numbers represent the order groups which include only a few brightness groups.

Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups will not be orderable.

CROSSING TABLE	
VISHAY	OSRAM
VLMW33S2V1	LWT67C-S2V1
VLMW33T2U2	LWT67C-T2U2

VLMW33..



## **Vishay Semiconductors**

CHROMATICI	TY COORDINATI	D GROUPS FO	R WHI	TE SMD LED		
	X	Y			X	Y
	0.291	0.268			0.330	0.330
5L	0.285	0.279		7L	0.330	0.347
5L	0.307	0.312		76	0.347	0.371
	0.310	0.297			0.345	0.352
	0.296	0.259			0.330	0.310
5K	0.291	0.268		71/	0.330	0.330
JK	0.310	0.297			0.338	0.342
	0.313	0.284			0.352	0.344
	0.310	0.297		7K	0.345	0.352
6L	0.307	0.312		8L	0.347	0.371
0L	0.330	0.347		OL	0.367	0.401
	0.330	0.330			0.364	0.380
	0.313	0.284			0.352	0.344
6K	0.310	0.297		8K	0.338	0.342
UN	0.330	0.330		ON	0.364	0.380
	0.330	0.310			0.360	0.357

Note

Chromaticity coordinate groups are tested at a current pulse duration of 25 ms and a tolerance of ± 0.01

**TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

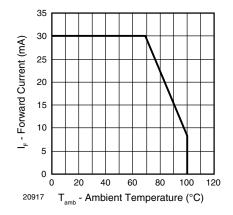
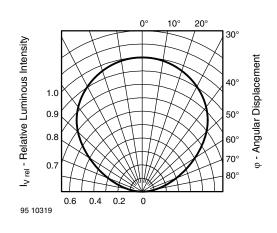
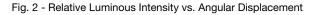


Fig. 1 - Forward Current vs. Ambient Temperature





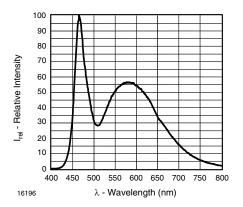


Fig. 3 - Relative Intensity vs. Wavelength

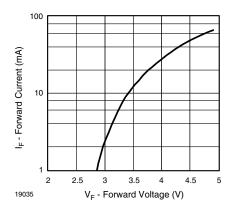
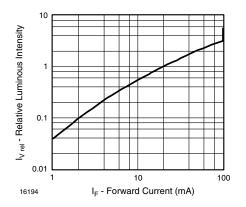


Fig. 4 - Forward Current vs. Forward Voltage

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Fig. 5 - Relative Luminous Intensity vs. Forward Current

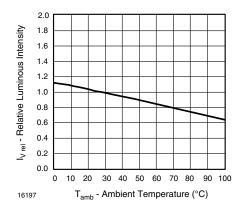


Fig. 6 - Relative Luminous Intensity vs. Ambient Temperature

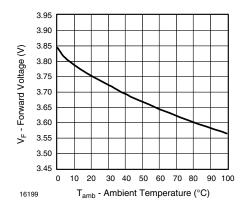


Fig. 7 - Forward Voltage vs. Ambient Temperature

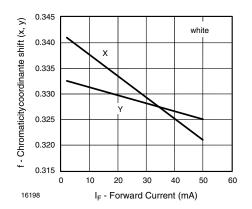


Fig. 8 - Chromaticity Coordinate Shift vs. Forward Current

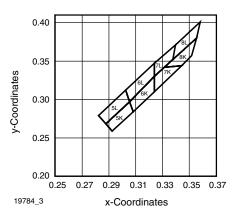


Fig. 9 - Coordinates of Colorgroups

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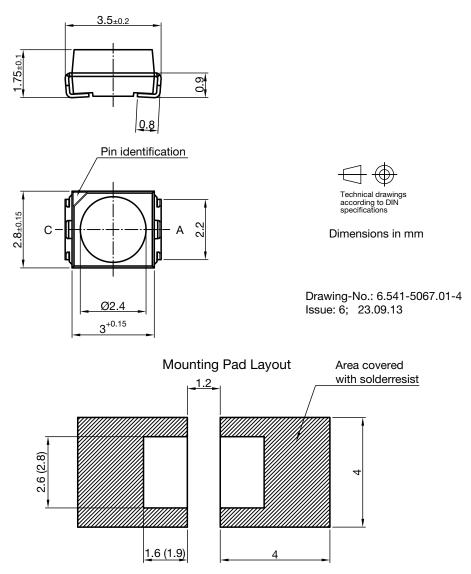
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#### **PACKAGE DIMENSIONS** in millimeters



Dimensions: Reflow and vapor phase (wave soldering)

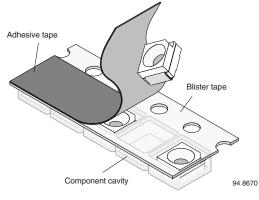


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#### METHOD OF TAPING / POLARITY AND TAPE AND REEL

#### SMD LED (VLM.3-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



#### TAPING OF VLM.3...

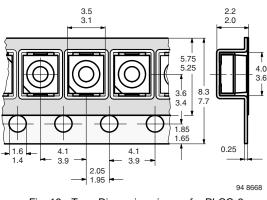


Fig. 10 - Tape Dimensions in mm for PLCC-2

#### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

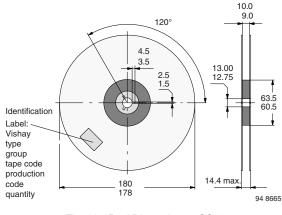


Fig. 11 - Reel Dimensions - GS08

#### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

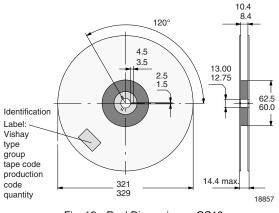


Fig. 12 - Reel Dimensions - GS18

#### **SOLDERING PROFILE**

IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC level 2a

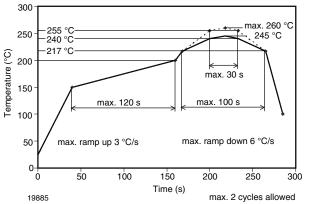


Fig. 13 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

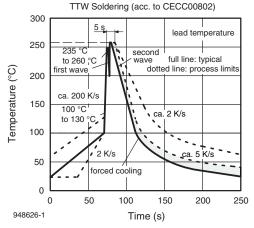
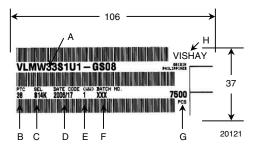


Fig. 14 - Double Wave Soldering of Opto Devices (all Packages)

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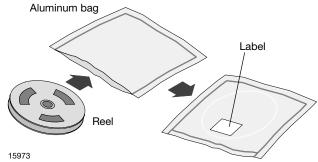
#### BAR CODE PRODUCT LABEL (example)



- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin): e.g.: S1 = code for luminous intensity group 4K = code for color group
- D) Date code year / week
- E) Day code (e.g. 1: Monday)
- F) Batch no.
- G) Total quantity
- H) Company code

#### DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



#### **FINAL PACKING**

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

### **Vishay Semiconductors**

#### **RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity  $\leq$  60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.

Ć	CAUTIC This bag con MOISTURE -SENSIT	tains	a
1. Shelf I	ife in sealed bag 12 months at <40°	°C and < 90% relative humid	ity (RH)
vapor 260°C a)	his bag is opened devices that will phase reflow, or equivalent proces ") must be: Mounted within <b>672 hours</b> at fac Stored at $\leq$ 10% RH.	sing (peak package body tem	p.
3. Device a) b)	es require baking before mounting i Humidity Indicator Card is >10% 2a or 2b is not met.		
19 96	ng is required, devices may be baka <b>2 hours</b> at 40°C + 5°C/-0°C and < <b>hours</b> at 60±5°Cand <5%RH <b>hours</b> at 100±5°C	5%RH (dry air/nitrogen)	
Bag Seal	Date:(If blank, see bar cod	e label)	
	Note: LEVEL defined by EIA JEI	DEC Standard JESD22-A113	

Example of JESD22-A112 level 2a label

#### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

#### VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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