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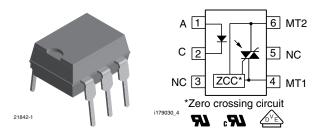




www.vishay.com

Vishay Semiconductors

Optocoupler, Phototriac Output, Zero Crossing, High dV/dt, Low Input Current



DESCRIPTION

The VO4154 and VO4156 consists of a GaAs IRLED optically coupled to a photosensitive zero crossing TRIAC packaged in a DIP-6 package.

High input sensitivity is achieved by using an emitter follower phototransistor and a cascaded SCR predriver resulting in an LED trigger current of 1.6 mA for bin D, 2 mA for bin H, and 3 mA for bin M.

The new phototriac zero crossing family uses a proprietary dV/dt clamp resulting in a static dV/dt of greater than 5 $kV/\mu s$.

The VO4154 and VO4156 isolates low-voltage logic from 120 V_{AC} , 240 V_{AC} , and 380 V_{AC} lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

FEATURES

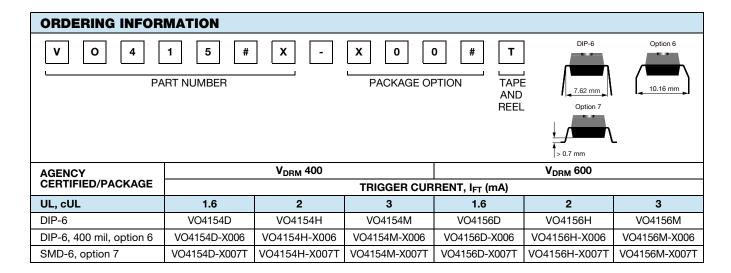
- High static dV/dt 5 kV/µs
- High input sensitivity I_{FT} = 1.6 mA, 2 mA, and 3 mA
- 300 mA on-state current
- · Zero voltage crossing detector
- 400 V and 600 V blocking voltage
- Isolation test voltage 5300 V_{RMS}
- Compliant to RoHS Directive 2011/65/EU

APPLICATIONS

- · Solid-state relays
- Industrial controls
- Office equipment
- Consumer appliances

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- cUL file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-2 (VDE 0884) available with option 1





ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT			
INPUT	•							
Reverse voltage			V_{R}	6	V			
Forward current			I _F	60	mA			
Surge current			I _{FSM}	2.5	Α			
Power dissipation			P _{diss}	100	mW			
Derate from 25 °C				1.33	mW/°C			
OUTPUT								
Dook off state voltage		VO4154D/H/M	V_{DRM}	400	V			
Peak off-state voltage		VO4156D/H/M	V_{DRM}	600	V			
RMS on-state current			I _{TM}	300	mA			
Total power dissipation			P _{diss}	500	mW			
Derate from 25 °C				6.6	mW/°C			
COUPLER	•							
Isolation test voltage (between emitter and detector, climate per DIN 500414, part 2, Nov. 74)	t = 1 min		V _{ISO}	5300	V _{RMS}			
Storage temperature range			T _{stg}	- 55 to + 150	°C			
Ambient temperature range			T _{amb}	- 55 to + 100	°C			
Soldering temperature	max. ≤ 10 s dip soldering ≥ 0.5 mm from case bottom		T _{sld}	260	°C			

Note

• Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

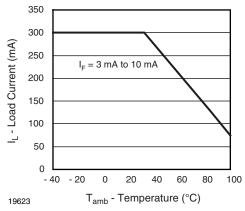


Fig. 1 - Recommended Operating Condition



THERMAL CHARACTERISTICS			
PARAMETER	SYMBOL	VALUE	UNIT
LED power dissipation	P _{diss}	100	mW
Output power dissipation	P _{diss}	500	mW
Maximum LED junction temperature	T _{jmax} .	125	°C
Maximum output die junction temperature	T _{jmax.}	125	°C
Thermal resistance, junction emitter to board	θ_{JEB}	150	°C/W
Thermal resistance, junction emitter to case	θ_{JEC}	139	°C/W
Thermal resistance, junction detector to board	θ_{JDB}	78	°C/W
Thermal resistance, junction detector to case	θ_{JDC}	103	°C/W
Thermal resistance, junction emitter to junction detector	θ_{JED}	496	°C/W
Thermal resistance, case to ambient	θ_{CA}	3563	°C/W

Note

The thermal characteristics table above were measured at 25 °C and the thermal model is represented in the thermal network below. Each
resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal
resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation
of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT					•			
Forward voltage	I _F = 10 mA		V_{F}		1.2	1.4	V	
Reverse current	V _R = 6 V		I _R		0.1	10	μA	
Input capacitance	V _F = 0 V, f = 1 MHz		Cı		25		pF	
OUTPUT	•							
Repetitive peak off-state voltage	1004	VO4154D/H/M	V_{DRM}	400			V	
	$I_{DRM} = 100 \mu A$	VO4156D/H/M	V_{DRM}	600			V	
Off-state current	$V_D = V_{DRM}$, $I_F = 0$ A		I _{DRM}			100	μA	
On-state voltage	I _T = 300 mA		V_{TM}			3	V	
On-state current	PF = 1, V _{T(RMS)} = 1.7 V		I _{TM}			300	mA	
Off-state current in inhibit state	$I_F = 2 \text{ mA}, V_{DRM}$		I _{DINH}			200	μA	
Holding current			I _H			500	μΑ	
Zero cross inhibit voltage	I _F = rated I _{FT}		V_{IH}			20	V	
Critical rate of rise of off-state voltage	V _D = 0.67 V _{DRM} , T _J = 25 °C		dV/dt _{cr}	5000			V/µs	
Critical rate of rise of on-state			dV/dt _{cr}	8			A/µs	
COUPLER								
		VO4154D	I _{FT}			1.6	mA	
LED trigger current, current required to latch output		VO4154H	I _{FT}			2	mA	
	V _D = 3 V	VO4154M	I _{FT}			3	mA	
	v _D = 3 v	VO4156D	I _{FT}			1.6	mA	
		VO4156H	I _{FT}			2	mA	
		VO4156M	I _{FT}			3	mA	
Common mode coupling capacitance			C _{CM}		0.01		pF	
Capacitance (input to output)	f = 1 MHz, V _{IO} = 0 V		C _{IO}		0.8		pF	

Note

Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.



SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC68 part 1)				55/100/21				
Pollution degree (DIN VDE 0109)				2				
Comparative tracking index per DIN IEC112/VDE 0303 part 1, group IIIa per DIN VDE 6110 175 399			175		399			
V _{IOTM}		V _{IOTM}	8000			V		
V _{IORM}		V_{IORM}	890			V		
P _{SO}		P _{SO}			500	mW		
I _{SI}		I _{SI}			250	mA		
T _{SI}		T _{SI}			175	°C		
Creepage distance			7			mm		
Crearance distance			7			mm		

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

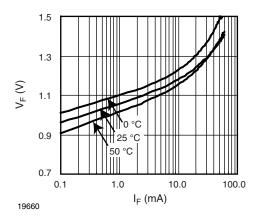


Fig. 2 - Diode Forward Voltage vs. Forward Current

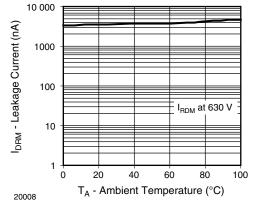


Fig. 4 - Leakage Current vs. Ambient Temperature

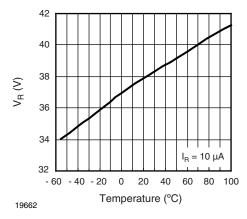


Fig. 3 - Diode Reverse Voltage vs. Temperature

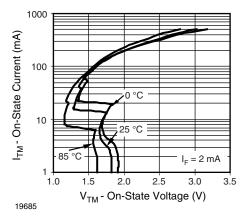


Fig. 5 - On-State Current vs. On-State Voltage

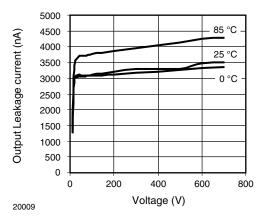


Fig. 6 - Output Off Current (Leakage) vs. Voltage

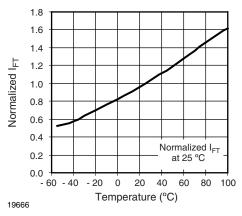


Fig. 7 - Normalized Trigger Input Current vs. Temperature

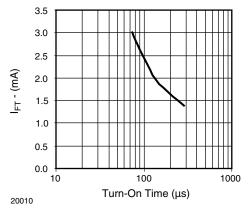


Fig. 8 - I_{FT} (mA) vs. Turn-On Time (μ s)

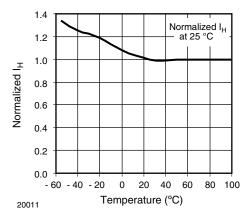


Fig. 9 - Normalized Holding Current vs. Temperature

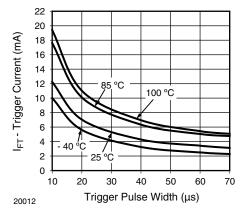
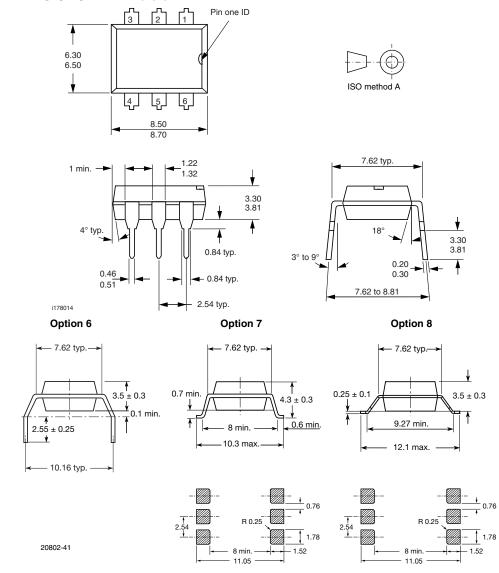


Fig. 10 - I_{FT} vs. LED Pulse Width



PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING (example)



Notes

- Only options 1, 7, and 8 are reflected in the package marking.
- The VDE Logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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