imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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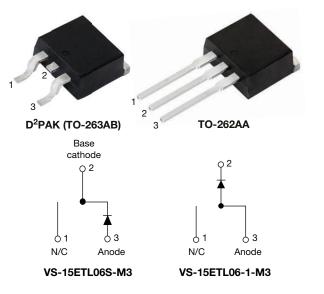


Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



Ultralow V_F Hyperfast Rectifier for Discontinuous Mode PFC, 15 A FRED Pt[®]



www.vishay.com

PRIMARY CHARACTERISTICS						
I _{F(AV)}	15 A					
V _R	600 V					
V _F at I _F	0.85 V					
t _{rr} (typ.)	60 ns					
T _J max.	175 °C					
Package	D ² PAK (TO-263AB), TO-262AA					
Circuit configuration	Single					

FEATURES

- Benchmark ultralow forward voltage drop
- · Hyperfast recovery time
- Low leakage current
- 175 °C operating junction temperature
- · Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C

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 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

State of the art, ultralow V_F, soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 154 °C	15	
Non-repetitive peak surge current	I _{FSM}	$T_J = 25 \ ^{\circ}C$	250	А
Peak repetitive forward current	I _{FM}		30	
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPEC	LECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-		
Forward voltage	M	I _F = 15 A	-	0.99	1.05	V	
Forward voltage	V _F	I _F = 15 A, T _J = 150 °C	-	0.85	0.92		
Deverse leekege eurrent		$V_{R} = V_{R}$ rated	-	0.1	10		
Reverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	15	120	μA	
Junction capacitance	CT	V _R = 600 V	-	20	-	pF	
Series inductance	Ls	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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Document Number: 96315

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RoHS COMPLIANT

HALOGEN

FREE



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DYNAMIC RECOVERY CH	ARACTER	STICS (T _C = 25	°C unless otherwi	ise specif	ied)		
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \ dI_F/dt = 10$	00 A/µs, V _R = 30 V	-	60	120	
Reverse recovery time	+	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 1000 $	-	190	270	20	
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 15 A dI _F /dt = 200 A/μs V _R = 390 V	-	220	-	ns
		T _J = 125 °C		-	320	-	
Deek receivery ourrent	I _{RRM}	T _J = 25 °C		-	19	-	А
Peak recovery current		T _J = 125 °C		-	26	-	~
Reverse recovery charge	0	T _J = 25 °C		-	2.2	-	uС
neverse recovery charge	Q _{rr}	T _J = 125 °C		-	4.3	-	μC

THERMAL - MECHANICA	L SPECIFI	CATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	1.0	1.3	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking davias		Case style D ² PAK (TO-263AB)	15ETL06S			
Marking device		Case style TO-262AA	15ETL06-1			

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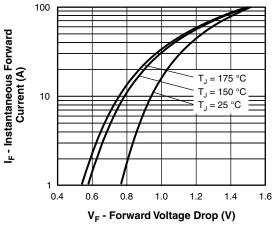
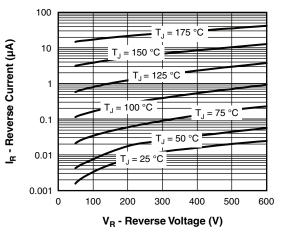
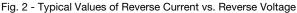


Fig. 1 - Typical Forward Voltage Drop Characteristics





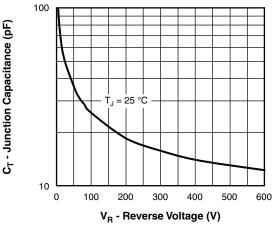


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

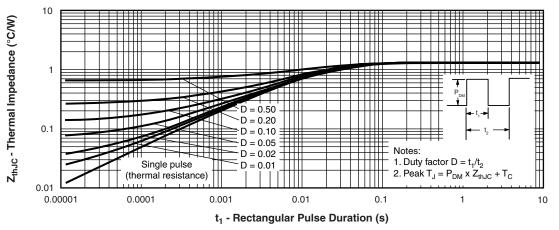
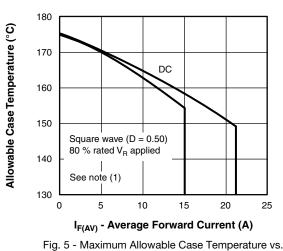


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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SHA

Average Forward Current

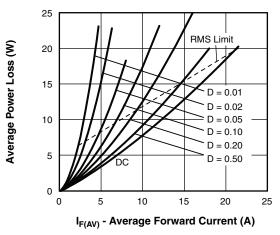
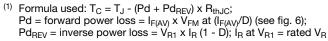


Fig. 6 - Forward Power Loss Characteristics

Note



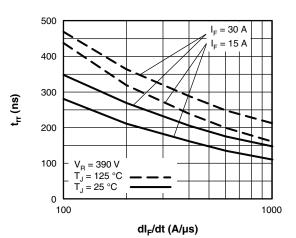


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

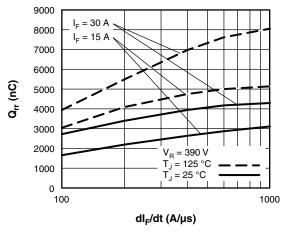


Fig. 8 - Typical Stored Charge vs. dl_F/dt

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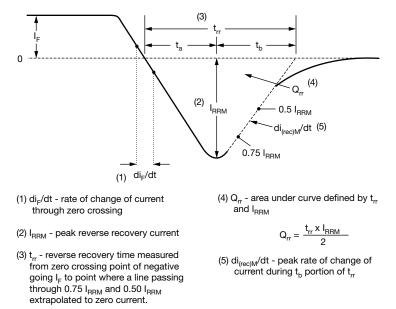


Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	VS-	15	Е	т	L	06	S	TRL	-M3
		2	3	4	5	6	7	8	9
	1	- Visl	nay Sem	niconduc	ctors pro	oduct			
	2	- Cur	rent rati	ng (15 A	A)				
	3	- E=	single c	liode					
	4	- T =	TO-220	, D ² PAł	<				
	5	- L=	ultralow	V _F hyp	erfast re	ecovery			
	6	- Voli	tage rati	ng (06 =	= 600 V)				
	7.	• s	= D ² PA	К					
		• -1	= TO-2	62					
	8	• N	one = tu	be (50 p	oieces)				
	_	• TI	RL = tap	e and re	eel (left	oriented	l, for D ²	PAK pa	ickage)
	 TRR = tape and reel (right oriented, for D²PAK package) 								
	9.	- Env	rironmer	ntal digit gen-free	:				

	LINKS TO RELATED DOCUMENTS					
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164				
Dimensions	TO-262AA	www.vishay.com/doc?96165				
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444				
Part marking information	TO-262AA	www.vishay.com/doc?95443				
Packaging information		www.vishay.com/doc?96424				
SPICE model		www.vishay.com/doc?96051				

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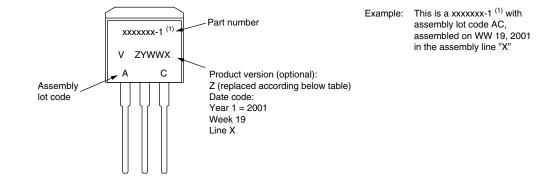
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Part Marking Information

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TO-262



Note

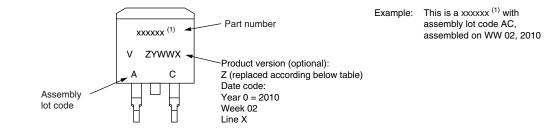
⁽¹⁾ If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION		
A Termination lead (Pb)-free			
B Totally lead (Pb)-free			
E	RoHS-compliant and termination lead (Pb)-free		
F	RoHS-compliant and totally lead (Pb)-free		
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free		
N	Halogen-free, RoHS-compliant and totally lead (Pb)-free		
G	Green		



Vishay Semiconductors

D²PAK



Note

⁽¹⁾ If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z) PRODUCT DEFINITION				
A Termination lead (Pb)-free				
B Totally lead (Pb)-free				
E RoHS-compliant and termination lead (Pb)-free				
F	RoHS-compliant and totally lead (Pb)-free			
М	Halogen-free, RoHS-compliant, and termination lead (Pb)-free			
Ν	Halogen-free, RoHS-compliant, and totally lead (Pb)-free			
G	Green			

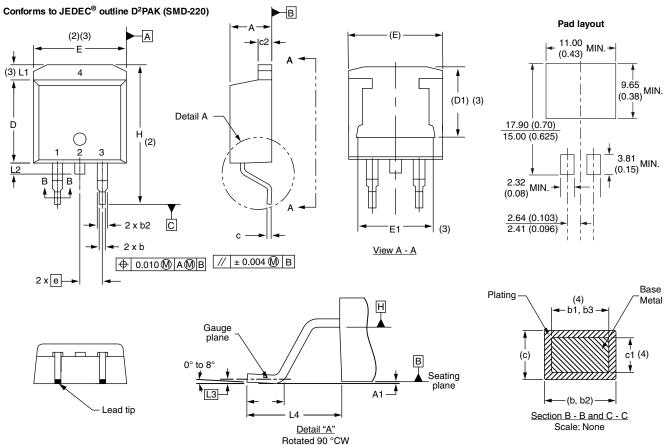
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D²PAK

DIMENSIONS in millimeters and inches

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ISHA





SYMBOL	MILLIM	MILLIMETERS		HES	NOTES	
STMBOL	MIN.	MAX.	MIN. MAX.		NOTES	
А	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
с	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100) BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inches

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 13-Jul-17

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Document Number: 96164

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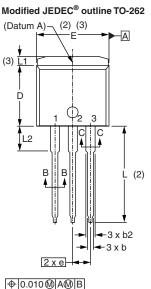
Outline Dimensions

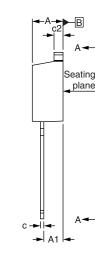


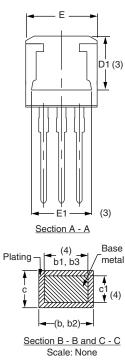
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TO-262AA

DIMENSIONS in millimeters and inches











Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode

Lead assignments

	MILLIN	IETERS	INC	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.100	BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

 ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
 ⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the state back. the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only (5)

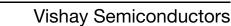
Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

Revision: 30-Nov-17

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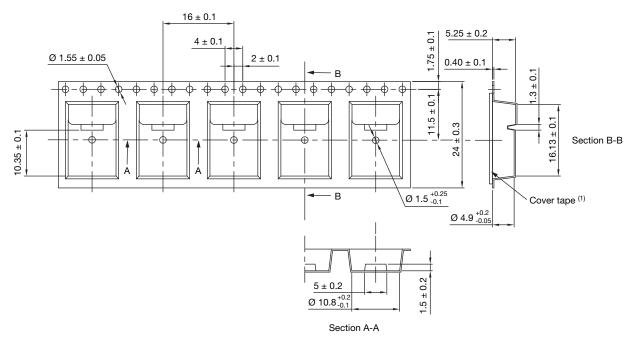
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D²PAK (TO-263AB)

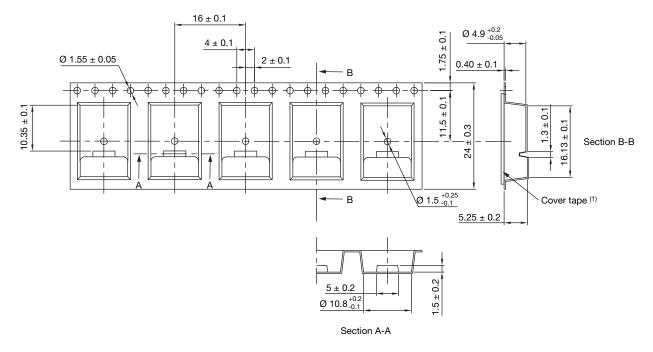
CARRIER TAPE FOR TAPE AND REEL LEFT in millimeters



Note

 $^{\left(1\right)}$ For dimensions, see next pages

CARRIER TAPE FOR TAPE AND REEL RIGHT in millimeters



Note

⁽¹⁾ For dimensions, see next pages

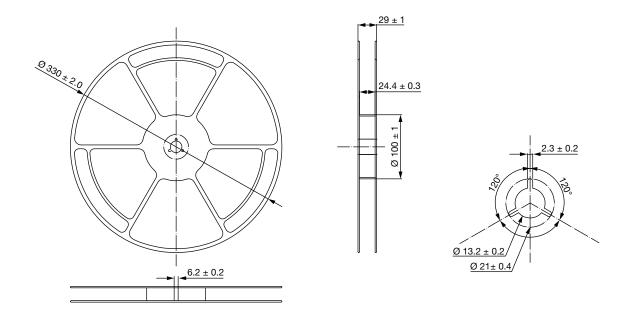
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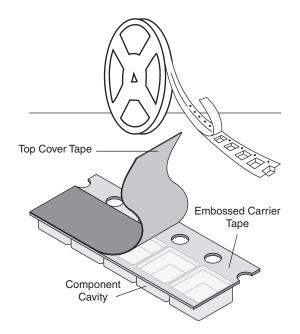
Packaging Information

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REEL FOR CARRIER TAPE in millimeters



CARRIER TAPE AND REEL PACKAGING D²PAK (TO-263AB)

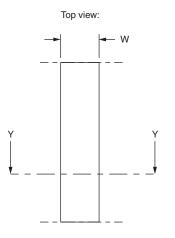


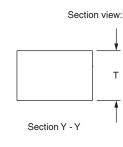
Packaging Information

Vishay Semiconductors



COVER TAPE FOR CARRIER TAPE in millimeters





APPLICATION	COVER TAPE WIDTH W	COVER TAPE THICKNESS T	CARRIER TAPE WIDTH	MATERIAL
D ² PAK (TO-263AB)	21.3 ± 0.1	0.060 ± 0.01	24	Antistatic/treated/transparent/polyester



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