# imall

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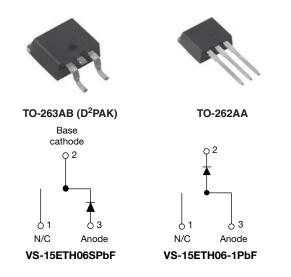
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**Vishay Semiconductors** 

### Hyperfast Rectifier, 15 A FRED Pt<sup>®</sup>



PRODUCT SUMMARY	
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA
I <sub>F(AV)</sub>	15 A
V <sub>R</sub>	600 V
V <sub>F</sub> at I <sub>F</sub>	1.3 V
t <sub>rr</sub> (typ.)	22 ns
T <sub>J</sub> max.	175 °C
Diode variation	Single diode

#### **FEATURES**

- · Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Single die center tap module
- HALOGEN Meets MSL level 1, per J-STD-020, LF maximum FREE peak of 260 °C
- AEC-Q101 qualified
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V		
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 140 °C	15			
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	120	А		
Peak repetitive forward current	I <sub>FM</sub>		30			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C		

ELECTRICAL SPEC	<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-		
	VF	I <sub>F</sub> = 15 A	-	1.8	2.2	V	
Forward voltage	۷F	I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	1.3	1.6		
Reverse leakage current		$V_{R} = V_{R}$ rated	-	0.2	50		
neverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	30	500	μA	
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	20	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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**Vishay Semiconductors** 

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_C = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \ dI_F/dt = 1$	00 A/µs, V <sub>R</sub> = 30 V	-	22	30	
Reverse recovery time	+	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 1000 $	100 A/µs, V <sub>R</sub> = 30 V	-	28	35	20
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	29	-	ns
		T <sub>J</sub> = 125 °C		-	75	-	
De els verses essent	$I_{RRM} = \frac{T_J = 25 \text{ °C}}{T_J = 125 \text{ °C}}$	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = 200 A/μs V <sub>B</sub> = 390 V	-	3.5	-	A
Peak recovery current		T <sub>J</sub> = 125 °C		-	7	-	
	0	T <sub>J</sub> = 25 °C		-	57	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	300	-	μC
Reverse recovery time	t <sub>rr</sub>		I <sub>F</sub> = 15 A	-	51	-	ns
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	dI <sub>F</sub> /dt = 800 A/µs	-	20	-	А
Reverse recovery charge	Q <sub>rr</sub>		V <sub>R</sub> = 390 V	-	580	-	nC

THERMAL - MECHANICAL	RMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	1.0	1.3	
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-	
Mainht			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking davias		Case style TO-263AB (D <sup>2</sup> PAK)	15ETH06S			
Marking device		Case style TO-262AA		15ET	H06-1	



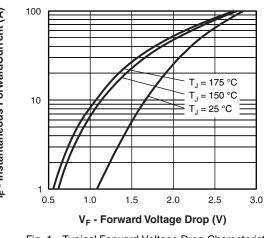


Fig. 1 - Typical Forward Voltage Drop Characteristics

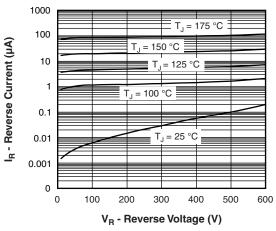


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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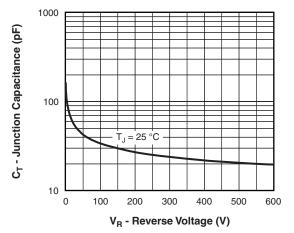


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

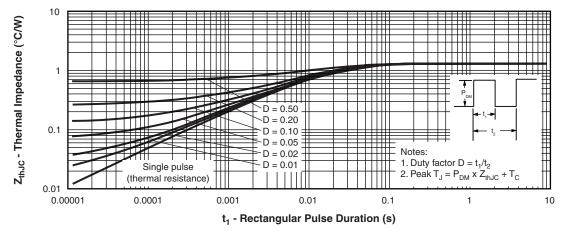
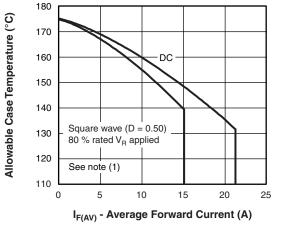
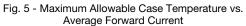


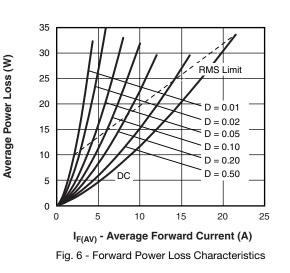
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics







<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R at V_{R1} = Rated V_R$ 



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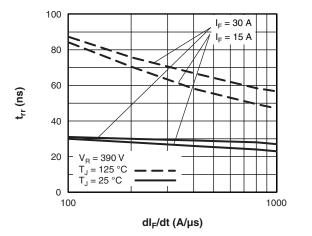


Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

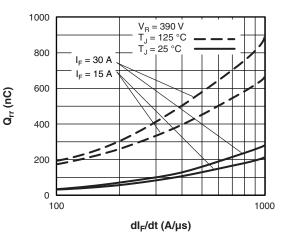


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

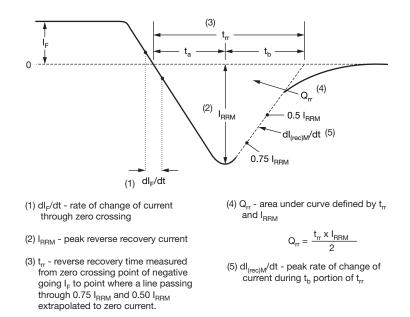
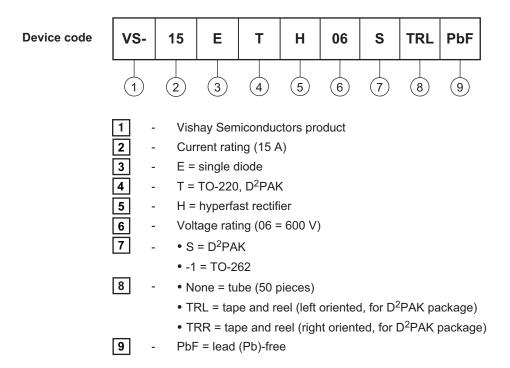


Fig. 9 - Reverse Recovery Waveform and Definitions



#### **Vishay Semiconductors**

#### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-15ETH06SPBF	50	1000	Antistatic plastic tubes		
VS-15ETH06TRRSPBF	800	800	13" diameter plastic tape and reel		
VS-15ETH06STRLPBF	800	800	13" diameter plastic tape and reel		
VS-15ETH06-1PBF	50	1000	Antistatic plastic tubes		

LINKS TO RELATED DOCUMENTS					
TO-263AB (D <sup>2</sup> PAK) <u>www.vishay.com/doc?95046</u>					
Dimensions	TO-262AA	www.vishay.com/doc?95419			
Part marking information		www.vishay.com/doc?95008			
Packaging information		www.vishay.com/doc?95032			

INCHES

0.100 BSC

0.010 BSC

MAX.

0.315

0.420

0.346

0.625

0.110

0.066

0.070

0.208

MIN.

0.270

0.380

0.311

0.575

0.070

0.050

0.188

MILLIMETERS

2.54 BSC

0.25 BSC

<sup>(7)</sup> Outline conforms to JEDEC outline TO-263AB

MAX.

8.00

10.67

8.80

15.88

2.79

1.65

1.78

5.28

MIN.

6.86

9.65

7.90

14.61

1.78

1.27

4.78

SYMBOL

D1

Е

E1

e H

L

L1

L2

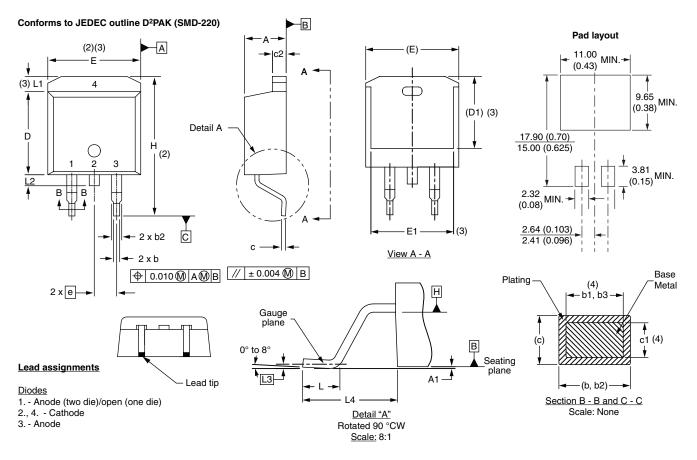
L3

L4

Vishay High Power Products

### D<sup>2</sup>PAK, TO-262

#### DIMENSIONS FOR D<sup>2</sup>PAK in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS		INCHES	
STMDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
с	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

-		
N	nt	69

- <sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only
- <sup>(5)</sup> Datum A and B to be determined at datum plane H
- <sup>(6)</sup> Controlling dimension: inch

NOTES

3

2, 3

3

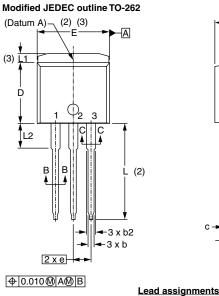
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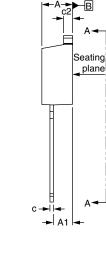
**Vishay High Power Products** 

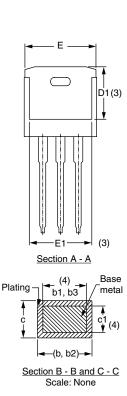
D<sup>2</sup>PAK, TO-262



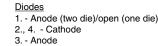
#### DIMENSIONS FOR TO-262 in millimeters and inches







Lead tip



SYMBOL	MILLIM	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100	BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- <sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1
- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Controlling dimension: inches

<sup>(6)</sup> Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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