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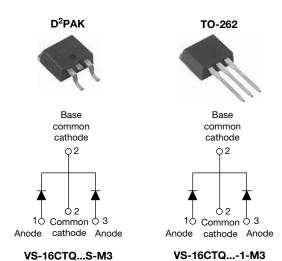




www.vishay.com

Vishay Semiconductors

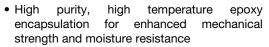
High Performance Schottky Rectifier, 2 x 8 A



| PRODUCT SUMMARY | | | | | | |
|----------------------------------|---|--|--|--|--|--|
| I _{F(AV)} | 2 x 8 A | | | | | |
| V_{R} | 60 V to 100 V | | | | | |
| V _F at I _F | 0.58 V | | | | | |
| I _{RM} | 7.0 mA at 125 °C | | | | | |
| T _J max. | 175 °C | | | | | |
| E _{AS} | 7.5 mJ | | | | | |
| Package | TO-263AB (D ² PAK), TO-262AA | | | | | |
| Diode variation | Common cathode | | | | | |

FEATURES

- 175 °C T_J operation
- Center tap configuration
- · Low forward voltage drop





- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

| MAJOR RATINGS | MAJOR RATINGS AND CHARACTERISTICS | | | | | | | |
|--------------------|---|------------|-------|--|--|--|--|--|
| SYMBOL | CHARACTERISTICS | VALUES | UNITS | | | | | |
| I _{F(AV)} | Rectangular waveform | 16 | Α | | | | | |
| V _{RRM} | | 60 to 100 | V | | | | | |
| I _{FSM} | t _p = 5 μs sine | 850 | Α | | | | | |
| V _F | 8 A _{pk} , T _J = 125 °C (per leg) | 0.58 | V | | | | | |
| T _J | Range | -55 to 175 | °C | | | | | |

| VOLTAGE RATINGS | | | | | |
|--------------------------------------|-----------|-------------------------------------|-------------------------------------|-------------------------------------|-------|
| PARAMETER | SYMBOL | VS-16CTQ060S-M3 VS-16CTQ060-1-M3 | VS-16CTQ080S-M3 VS-16CTQ080-1-M3 | VS-16CTQ100S-M3 VS-16CTQ100-1-M3 | UNITS |
| Maximum DC reverse voltage | V_{R} | 60 | 80 | 100 | V |
| Maximum working peak reverse voltage | V_{RWM} | 00 | 80 | 100 | V |



VS-16CTQ...S-M3, VS-16CTQ...-1-M3 Series

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| ABSOLUTE MAXIMUM RATINGS | | | | | | | |
|--|----------------------|--|---|--------|-------|--|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS | | |
| Maximum average per le | eg | | | 8 | | | |
| forward current See fig. 5 per devi | e I _{F(AV)} | 50 % duty cycle at T _C = 148 °C, rectangular waveform | | 16 | Α | | |
| Maximum peak one cycle | | 5 μs sine or 3 μs rect. pulse | Following any rated load | 850 | | | |
| non-repetitive surge current per leg See fig. 7 | I _{FSM} | 10 ms sine or 6 ms rect. pulse | condition and with rated V _{RRM} applied | 275 | Α | | |
| Non-repetitive avalanche energy per leg | E _{AS} | $T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.50 \text{A}, L = 60 \text{C}$ |) mH | 7.50 | mJ | | |
| Repetitive avalanche current per leg | I _{AR} | Current decaying linearly to zer Frequency limited by T _J maxim | • | 0.50 | Α | | |

| ELECTRICAL SPECIFICATIONS | | | | | | |
|--|--------------------------------|--|---------------------------------------|--------|-------|--|
| PARAMETER | SYMBOL | TEST CO | NDITIONS | VALUES | UNITS | |
| | | 8 A | T. ₁ = 25 °C | 0.72 | | |
| Maximum forward voltage drop per leg See fig. 1 | V _{FM} ⁽¹⁾ | 16 A | 1J=25 C | 0.88 | V | |
| | VFM (1) | 8 A | T _{.1} = 125 °C | 0.58 | | |
| | | 16 A | 1 J = 125 C | 0.69 | | |
| Maximum reverse leakage current per leg | I _{RM} ⁽¹⁾ | T _J = 25 °C | V _R = Rated V _R | 0.55 | mA | |
| See fig. 2 | IRM ("/ | T _J = 125 °C | v _R = nateu v _R | 7.0 | | |
| Threshold voltage | V _{F(TO)} | T - T mayimum | | 0.415 | V | |
| Forward slope resistance | r _t | $T_J = T_J$ maximum | | 11.07 | mΩ | |
| Maximum junction capacitance per leg | C _T | $V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C | | 500 | pF | |
| Typical series inductance per leg | L _S | Measured lead to lead 5 m | 8.0 | nΗ | | |
| Maximum voltage rate of change | dV/dt | Rated V _R | | 10 000 | V/µs | |

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | | |
|--|---------|-----------------------------------|--------------------------------------|------------|------------|--|--|
| PARAMETER | | SYMBOL | SYMBOL TEST CONDITIONS | | UNITS | | |
| Maximum junction and storage temperature range | | T _J , T _{Stg} | | -55 to 175 | °C | | |
| Maximum thermal resistance, junction to case per leg | | Б | DC energtion | 3.25 | | | |
| Maximum thermal resistance, junction to case per package | | R_{thJC} | DC operation | 1.63 | °C/W | | |
| Typical thermal resistance, case to heatsink | | R _{thCS} | Mounting surface, smooth and greased | 0.50 | | | |
| Annuavimenta weight | | | | 2 | g | | |
| Approximate weight | | | | 0.07 | OZ. | | |
| Mounting toward | minimum | | | 6 (5) | kgf · cm | | |
| Mounting torque | maximum | | | 12 (10) | (lbf · in) | | |
| Marking davisa | | | Case style D ² PAK | 16CT | QS | | |
| Marking device | | | Case style TO-262 | 16CT | Q1 | | |



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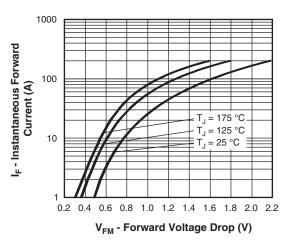


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

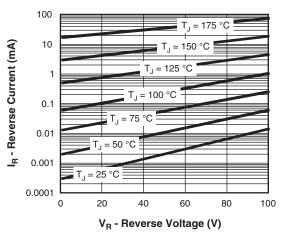


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

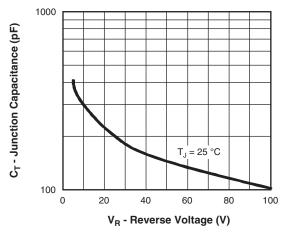


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

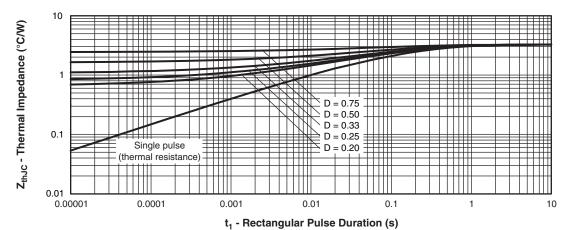


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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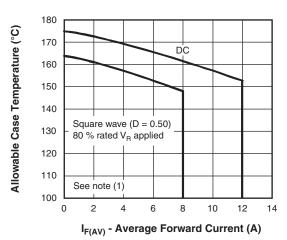


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

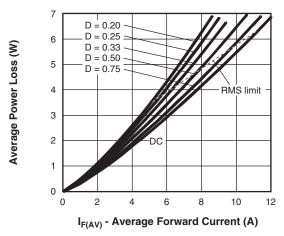


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

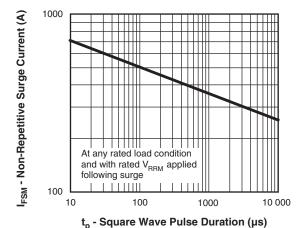


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

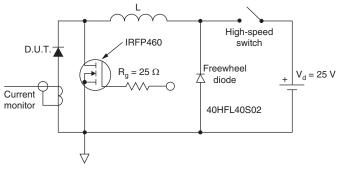


Fig. 8 - Unclamped Inductive Test Circuit

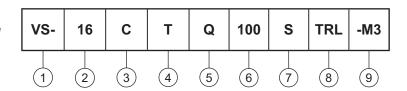
Note

[1] Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse$ power loss $= V_{R1} \times I_{R}$ (1 - D); I_{R} at $V_{R1} = 80$ % rated V_{R} applied

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ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (16 A)

- Circuit configuration: C = Common cathode

4 - T = TO-220

5 - Schottky "Q" series 060 = 60 V 080 = 80 V 100 = 100 V

7 - • S = D²PAK

• -1 = TO-262

8 - • None = Tube

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

9 - -M3 = Halogen-free, RoHS-compliant and termination lead (Pb)-free

| ORDERING INFORMAT | ION | | |
|--------------------|------------------|------------------------|--------------------------|
| PREFERRED P/N | QUANTITY PER T/R | MINIMUM ORDER QUANTITY | PACKAGING DESCRIPTION |
| VS-16CTQ060S-M3 | 50 | 1000 | Antistatic plastic tubes |
| VS-16CTQ060STRR-M3 | 800 | 800 | 13" diameter reel |
| VS-16CTQ060STRL-M3 | 800 | 800 | 13" diameter reel |
| VS-16CTQ060-1-M3 | 50 | 1000 | Antistatic plastic tubes |
| VS-16CTQ080S-M3 | 50 | 1000 | Antistatic plastic tubes |
| VS-16CTQ080STRR-M3 | 800 | 800 | 13" diameter reel |
| VS-16CTQ080STRL-M3 | 800 | 800 | 13" diameter reel |
| VS-16CTQ080-1-M3 | 50 | 1000 | Antistatic plastic tubes |
| VS-16CTQ100S-M3 | 50 | 1000 | Antistatic plastic tubes |
| VS-16CTQ100STRR-M3 | 800 | 800 | 13" diameter reel |
| VS-16CTQ100STRL-M3 | 800 | 800 | 13" diameter reel |
| VS-16CTQ100-1-M3 | 50 | 1000 | Antistatic plastic tubes |

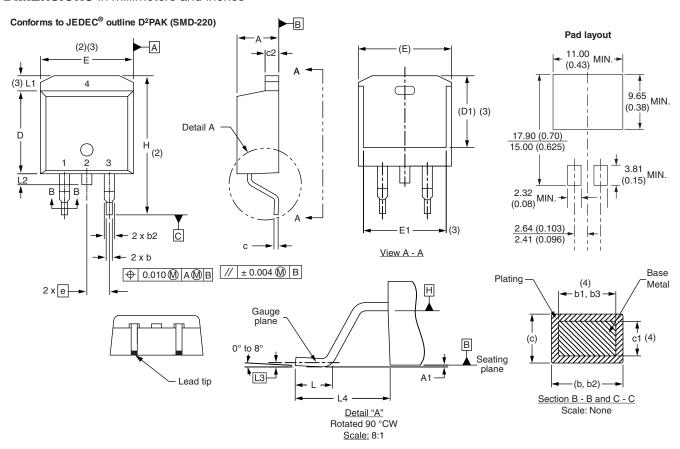
| | LINKS TO RELATED DOCUMENTS | | | | | | |
|--------------------------|-------------------------------|--------------------------|--|--|--|--|--|
| Dimensions | TO-263AB (D ² PAK) | www.vishay.com/doc?95046 | | | | | |
| Dimensions | TO-262AA | www.vishay.com/doc?95419 | | | | | |
| Part marking information | TO-263AB (D ² PAK) | www.vishay.com/doc?95444 | | | | | |
| Part marking information | TO-262AA | www.vishay.com/doc?95443 | | | | | |
| Packaging information | | www.vishay.com/doc?95032 | | | | | |
| SPICE model | | www.vishay.com/doc?95279 | | | | | |



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D²PAK

DIMENSIONS in millimeters and inches



| SYMBOL | MILLIMETERS | | INCHES | | NOTES SYMBOL | MILLIM | ETERS | INC | HES | NOTES | | |
|----------|-------------|-------|--------|-------|--------------|--------|---------|-------|-------|-------|-------|-------|
| STIVIBUL | MIN. | MAX. | MIN. | MAX. | NOIES | NOTES | STWIDOL | MIN. | MAX. | MIN. | MAX. | NOTES |
| Α | 4.06 | 4.83 | 0.160 | 0.190 | | | D1 | 6.86 | 8.00 | 0.270 | 0.315 | 3 |
| A1 | 0.00 | 0.254 | 0.000 | 0.010 | | | Е | 9.65 | 10.67 | 0.380 | 0.420 | 2, 3 |
| b | 0.51 | 0.99 | 0.020 | 0.039 | | | E1 | 7.90 | 8.80 | 0.311 | 0.346 | 3 |
| b1 | 0.51 | 0.89 | 0.020 | 0.035 | 4 | | е | 2.54 | BSC | 0.100 |) BSC | |
| b2 | 1.14 | 1.78 | 0.045 | 0.070 | | | Н | 14.61 | 15.88 | 0.575 | 0.625 | |
| b3 | 1.14 | 1.73 | 0.045 | 0.068 | 4 | | L | 1.78 | 2.79 | 0.070 | 0.110 | |
| С | 0.38 | 0.74 | 0.015 | 0.029 | | | L1 | - | 1.65 | - | 0.066 | 3 |
| c1 | 0.38 | 0.58 | 0.015 | 0.023 | 4 | | L2 | 1.27 | 1.78 | 0.050 | 0.070 | |
| c2 | 1.14 | 1.65 | 0.045 | 0.065 | | | L3 | 0.25 | BSC | 0.010 | BSC | |
| D | 8.51 | 9.65 | 0.335 | 0.380 | 2 | | L4 | 4.78 | 5.28 | 0.188 | 0.208 | |

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

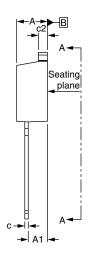


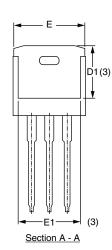
Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

-3 x b



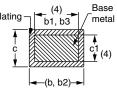


⊕ 0.010**⋒**|A**⋒**|B

Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode
3. - Anode



Section B - B and C - C Scale: None

| CVMPOL | MILLIN | METERS | INCH | IES | NOTES |
|--------|--------|--------|-------|-------|-------|
| SYMBOL | MIN. | MAX. | MIN. | MAX. | NOTES |
| Α | 4.06 | 4.83 | 0.160 | 0.190 | |
| A1 | 2.03 | 3.02 | 0.080 | 0.119 | |
| b | 0.51 | 0.99 | 0.020 | 0.039 | |
| b1 | 0.51 | 0.89 | 0.020 | 0.035 | 4 |
| b2 | 1.14 | 1.78 | 0.045 | 0.070 | |
| b3 | 1.14 | 1.73 | 0.045 | 0.068 | 4 |
| С | 0.38 | 0.74 | 0.015 | 0.029 | |
| c1 | 0.38 | 0.58 | 0.015 | 0.023 | 4 |
| c2 | 1.14 | 1.65 | 0.045 | 0.065 | |
| D | 8.51 | 9.65 | 0.335 | 0.380 | 2 |
| D1 | 6.86 | 8.00 | 0.270 | 0.315 | 3 |
| E | 9.65 | 10.67 | 0.380 | 0.420 | 2, 3 |
| E1 | 7.90 | 8.80 | 0.311 | 0.346 | 3 |
| е | 2.54 | BSC | 0.100 | BSC | |
| L | 13.46 | 14.10 | 0.530 | 0.555 | |
| L1 | - | 1.65 | - | 0.065 | 3 |
| L2 | 3.56 | 3.71 | 0.140 | 0.146 | |

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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