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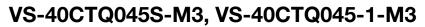
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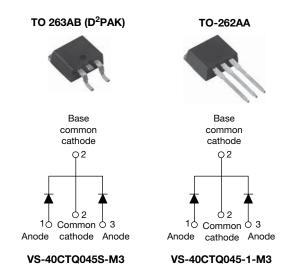


Vishay Semiconductors

RoHS

FREE

High Performance Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY								
I _{F(AV)}	2 x 20 A							
V _R	45 V							
V _F at I _F	0.48 V							
I _{RM} max.	115 mA at 125 °C							
T _J max.	150 °C							
E _{AS}	20 mJ							
Package	TO-263AB (D ² PAK), TO-262AA							
Diode variation	Common cathode							

FEATURES

- 150 °C T_J operation
- · Center tap configuration
- · Very low forward voltage drop
- High frequency operation
- COMPLIANT High purity, high temperature epoxy HALOGEN encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS VALUES UNITS								
I _{F(AV)}	Rectangular waveform	40	А						
V _{RRM}		45	V						
I _{FSM}	t _p = 5 μs sine	1240	А						
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.48	V						
ТЈ	Range	-55 to +150	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-40CTQ045S-M3 VS-40CTQ045-1-M3 UNITS							
Maximum DC reverse voltage	V _R	45	V						
Maximum working peak reverse voltage	V _{RWM}	40	V						

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ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum averageper legforward current		$I_{F(AV)}$ 50 % duty cycle at T _C = 116 °C, rectangular waveform		20				
		I _{F(AV)}	30.70 duty cycle at $1_{\rm C} = 110$ C, rectangular wavelonn		40	А		
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	1240			
			10 ms sine or 6 ms rect. pulse	V _{RRM} applied	350			
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 4.40 mH		20	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	A		

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS			
		20 A	T.I = 25 °C	0.53	V		
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1j=25 C	0.68			
See fig. 1	VFM (*)	20 A	T _{.1} = 125 °C	0.48			
		40 A	1j = 125 0	0.67			
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	3	mA		
See fig. 2		T _J = 125 °C	VR - Haleu VR	115			
Threshold voltage	V _{F(TO)}			0.27	V		
Forward slope resistance	rt	$T_J = T_J$ maximum		8.72	mΩ		
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range	2800	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 mr	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs			

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

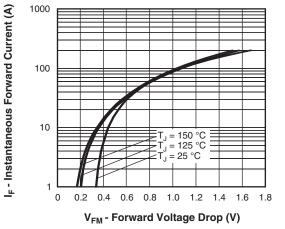
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C			
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package Typical thermal resistance, case to heatsink		P	DC operation	2.0				
		R _{thJC}	DC operation	1.0	°C/W			
		R _{thCS}	Mounting surface, smooth and greased	0.50				
Approximate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torgue	minimum			6 (5)	kgf ⋅ cm			
Mounting torque	maximum			12 (10)	(lbf · in)			
			Case style TO-263AB (D ² PAK)	40CTQ	045S			
Marking device			Case style TO-262AA	40CTQ0	045-1			

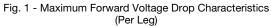
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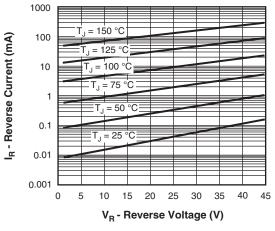
VS-40CTQ045S-M3, VS-40CTQ045-1-M3

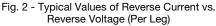


Vishay Semiconductors









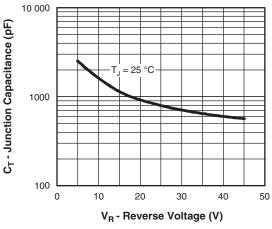


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

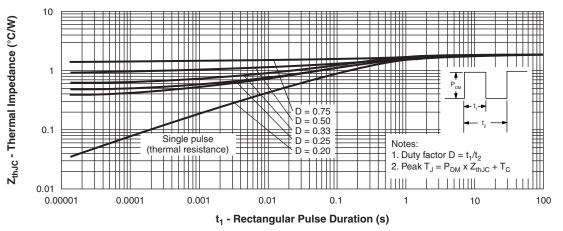
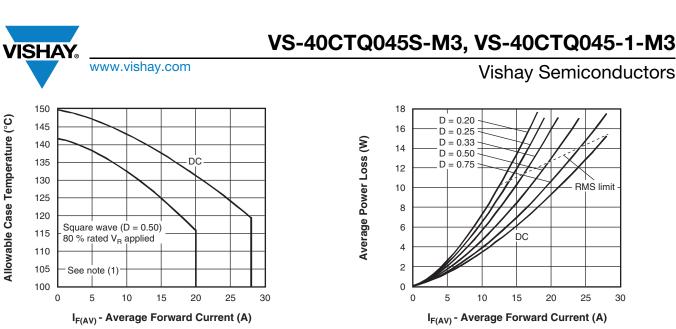
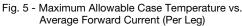


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

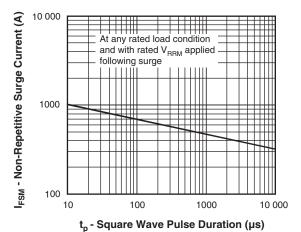
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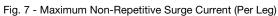
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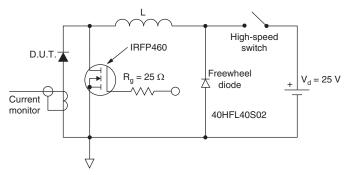














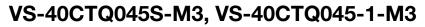
Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \ x \ V_{FM} \ at \ (\mbox{I}_{F(AV)}/D) \ (see \ fig. \ 6); \\ \mbox{Pd}_{REV} = \ \mbox{Inverse power loss} = \ \ V_{R1} \ x \ \ \ I_R \ (1 D); \ \ \ I_R \ at \ \ \ V_{R1} = \ 10 \ \ V \\ \end{array}$

Revision: 15-Aug-15

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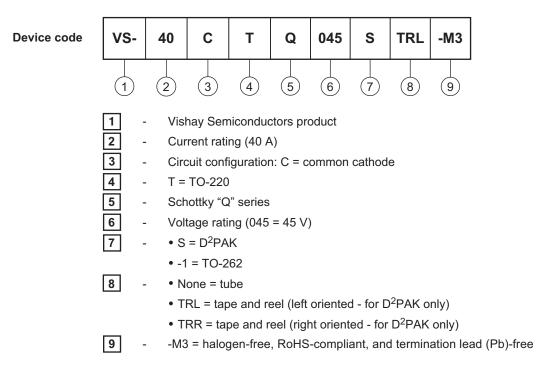
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Vishay Semiconductors

ORDERING INFORMATION TABLE

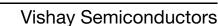
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ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-40CTQ045S-M3	50	1000	Antistatic plastic tubes						
VS-40CTQ045STRR-M3	800	800	13" diameter reel						
VS-40CTQ045STRL-M3	800	800	13" diameter reel						
VS-40CTQ045-1-M3	50	1000	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS								
Dimensions TO-263AB (D ² PAK) www.vishay.com/doc?95046								
Dimensions	TO-262AA	www.vishay.com/doc?95419						
Part marking information	TO-263AB (D ² PAK)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?95032						

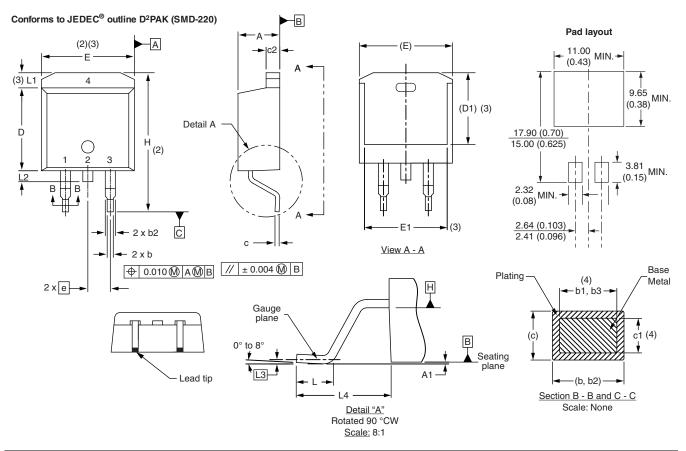
Outline Dimensions



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D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	e 2.54 BSC		0.100 BSC		
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
с	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010) BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

- ⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1
- ⁽⁴⁾ Dimension b1 and c1 apply to base metal only
- ⁽⁵⁾ Datum A and B to be determined at datum plane H
- ⁽⁶⁾ Controlling dimension: inch
- ⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

Document Number: 95046

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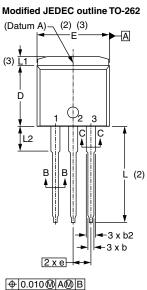


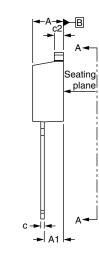
Outline Dimensions

Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

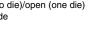




Lead assignments



Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode



D1(3) (3) E1 Section A - A Base (4) Plating b1, b3 metal Ā ///// (4)<--(b, b2)-►

Е

Section B - B and C - C Scale: None

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 0.160 0.190 А 4.06 4.83 0.080 A1 2.03 3.02 0.119 0.51 0.99 0.020 0.039 b b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 b3 1.14 1.73 0.045 0.068 4 0.38 0.74 0.015 0.029 с 0.38 0.015 0.023 4 c1 0.58 0.045 0.065 c2 1.14 1.65 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2.3 E1 7.90 8.80 0.311 0.346 3 2.54 BSC 0.100 BSC е L 13.46 0.530 0.555 14.10 L1 1.65 0.065 3 3.56 L2 3.71 0.140 0.146

Notes

Revision: 04-Oct-10

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(4) Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

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