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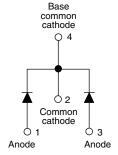
RoHS

HALOGEN

FREE

Schottky Rectifier, 2 x 3.5 A

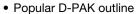




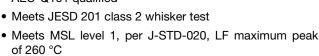
PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I _{F(AV)}	2 x 3.5 A			
V_{R}	40 V			
V _F at I _F	See Electrical table			
I _{RM}	24 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Common cathode			
E _{AS}	8 mJ			

FEATURES

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability



- · Center tap configuration
- · Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- of 260 °C
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912



DESCRIPTION

The VS-6CWQ04FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	7	A			
V _{RRM}		40	V			
I _{FSM}	t _p = 5 μs sine	500	A			
V _F	3 A _{pk} , T _J = 125 °C (per leg)	0.49	V			
T _J	Range	- 40 to 150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-6CWQ04FNHM3	UNITS		
Maximum DC reverse voltage	V_{R}	40	V		
Maximum working peak reverse voltage	V_{RWM}	40	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg	I	50 % duty cycle at T_C = 135 °C, rectangular waveform		3.5	
See fig. 5	per device	I _{F(AV)}			7	Α
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	500	
			10 ms sine or 6 ms rect. pulse	V _{RRM} applied	80	
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 16 mH		8.0	mJ
Repetitive avalanche current per leg I _{AR}		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	3 A	T 05 %C	0.53	. V
Maximum forward voltage drop per leg		6 A	− T _J = 25 °C	0.67	
See fig. 1		3 A	T _J = 125 °C	0.49	
		6 A		0.62	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	- V _R = Rated V _R	2	- mA
See fig. 2	'RM\'	T _J = 125 °C		24	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.34	V
Forward slope resistance	r _t			37.33	mΩ
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz), 25 °C		189	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.0 r		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

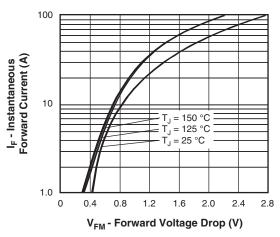
Note

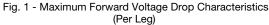
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance,	per leg	D	DC operation	4.70	°C/W
junction to case	per device	R_{thJC}	See fig. 4	2.35	C/ VV
Approximate weight				0.3	g
Approximate weight				0.01	oz.
Marking device			Case style D-PAK	6CWQ0	4FNH

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink





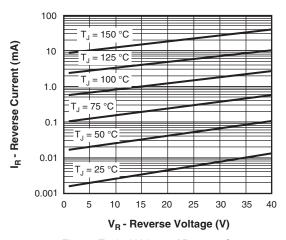


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

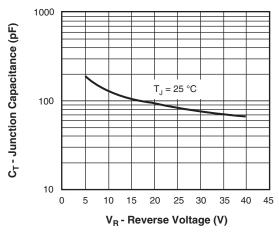


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

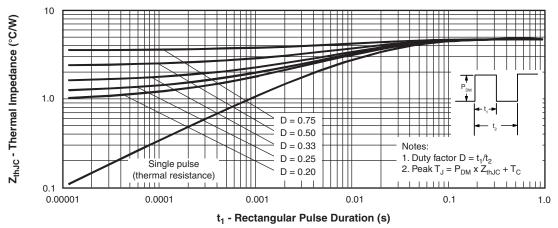


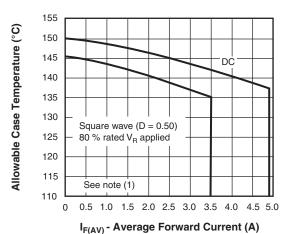
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)





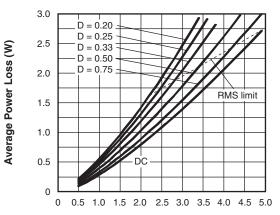
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Vishay Semiconductors



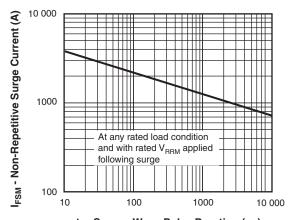
Maximum Allowable Cose Temperature

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics (Per Leg)



 t_p - Square Wave Pulse Duration (μ s)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

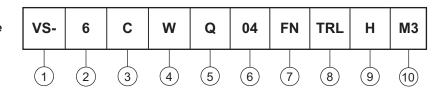
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (7 A)

- Center tap configuration

Package identifier:

W = D-PAK

5 - Schottky "Q" series

6 - Voltage rating (04 = 40 V)

7 - FN = TO-252AA

8 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-6CWQ04FNHM3	75	3000	Antistatic plastic tube		
VS-6CWQ04FNTRHM3	2000	2000	13" diameter reel		
VS-6CWQ04FNTRRHM3	3000	3000	13" diameter reel		
VS-6CWQ04FNTRLHM3	3000	3000	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?95033			



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