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Vishay Semiconductors

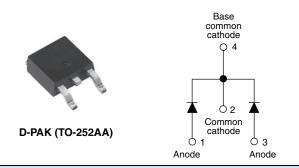
RoHS

COMPLIANT HALOGEN

FREE

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PRODUCT SUMMARY							
Package	D-PAK (TO-252AA)						
I _{F(AV)}	2 x 3.5 A						
V _R	60 V						
V _F at I _F	See Electrical table						
I _{RM}	30 mA at 125 °C						
T _J max.	150 °C						
Diode variation	Common cathode						
E _{AS}	6 mJ						

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-6CWQ06FN-M3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS VALUES UNITS								
I _{F(AV)}	Rectangular waveform	7	А						
V _{RRM}		60	V						
I _{FSM}	t _p = 5 μs sine	490	А						
V _F	3 A_{pk} , $T_J = 25 \ ^{\circ}C$ (per leg)	0.61	V						
TJ	Range	-40 to +150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-6CWQ06FN-M3	UNITS					
Maximum DC reverse voltage	V _R	60	V					
Maximum working peak reverse voltage	V _{RWM}	00	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDI	VALUES	UNITS			
Maximum average forward current	per leg	I =	50 % duty cycle at T _C = 133 °C, rectangular waveform –		3.5	A		
See fig. 5	per device	I _{F(AV)}			7			
Maximum peak one cycle			5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	490			
non-repetitive surge current See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	70			
Non-repetitive avalanche en	ergy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6	mJ		
Repetitive avalanche curren	t per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А		

Revision: 22-Nov-16 1 Document Number: 93317 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		3 A	T ₁ = 25 °C	0.61	V			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	6 A	1j = 23 0	0.76				
See fig. 1	¥FM (*)	3 A	T _J = 125 °C	0.53	v			
		6 A	1j = 125 0	0.65				
Maximum reverse	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B} = \text{Rated } V_{\rm B}$	2				
leakage current per leg See fig. 2	IRM ("	T _J = 125 °C	VR = naleu VR	30	mA			
Threshold voltage	V _{F(TO)}			0.38	V			
Forward slope resistance	r _t	$T_{J} = T_{J}$ maximum		34.31	mΩ			
Typical junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$, (test signal ran	145	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 n	5.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T_{J} ⁽¹⁾ , T_{Stg}		-40 to +150	°C			
Maximum thermal resistance,	per leg	D	DC operation See fig. 4	4.70	°C/W			
junction to case	per device	R _{thJC}		2.35				
A second s				0.3	g			
Approximate weight				0.01	oz.			
Marking device			Case style D-PAK (similar to TO-252AA)	6CWQ06FN				

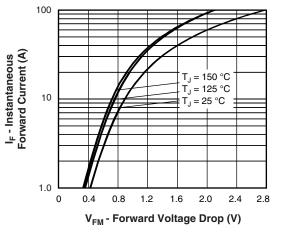
Note

(1)

 $\frac{dP_{tot}}{dT_{J}} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

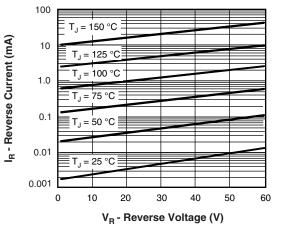


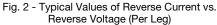
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Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





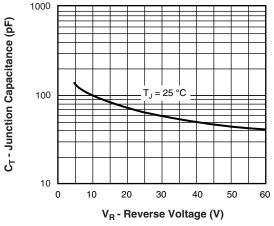


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

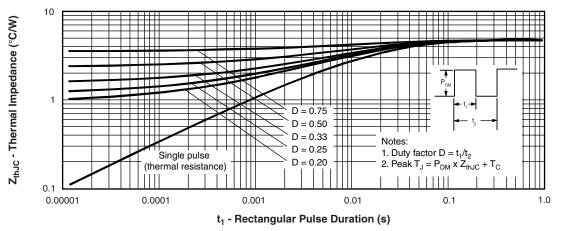
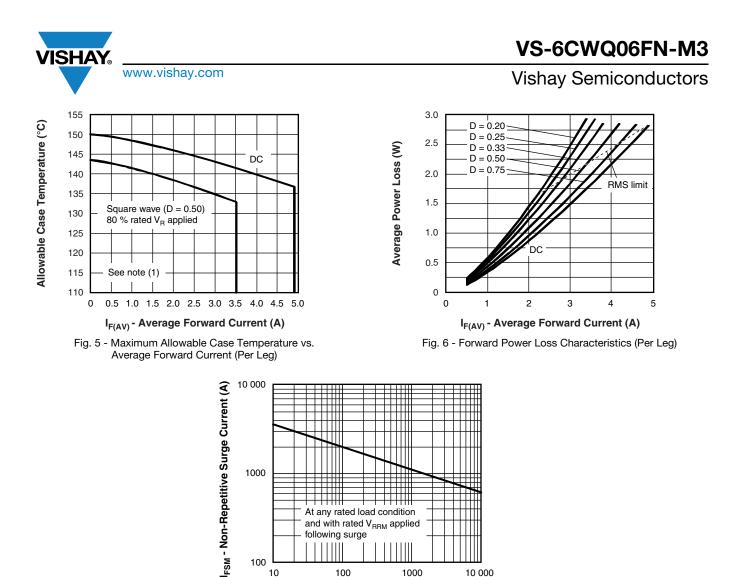


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



1000

t_p - Square Wave Pulse Duration (μs) Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

100

10 000

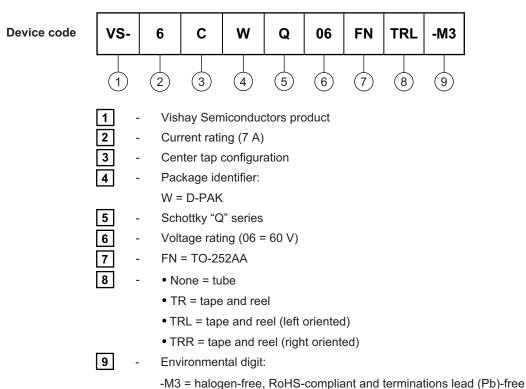
⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

100 10

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ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-6CWQ06FN-M3	75	3000	Antistatic plastic tube						
VS-6CWQ06FNTR-M3	2000	2000	13" diameter reel						
VS-6CWQ06FNTRL-M3	3000	3000	13" diameter reel						
VS-6CWQ06FNTRR-M3	3000	3000	13" diameter reel						

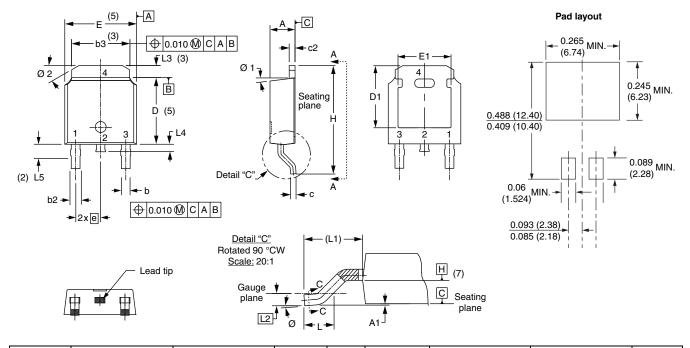
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95627					
Part marking information	www.vishay.com/doc?95176					
Packaging information	www.vishay.com/doc?95033					





D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC	
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC[®] outline TO-252AA



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