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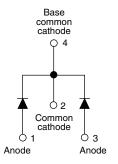
### Vishay Semiconductors

**HALOGEN** 

FREE

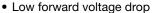
## Schottky Rectifier, 2 x 3.5 A

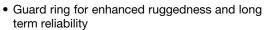


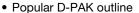


PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I <sub>F(AV)</sub>	2 x 3.5 A			
$V_{R}$	100 V			
V <sub>F</sub> at I <sub>F</sub>	See Electrical table			
I <sub>RM</sub>	4.9 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Common cathode			
E <sub>AS</sub>	5 mJ			

#### **FEATURES**







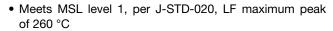
• Center tap configuration

• Small foot print, surface mountable

• High frequency operation

AEC-Q101 qualified

• Meets JESD 201 class 2 whisker test



 Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>



The VS-6CWQ10FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	7	Α		
V <sub>RRM</sub>		100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	440	Α		
V <sub>F</sub>	3 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.63	V		
T <sub>J</sub>	Range	- 40 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-6CWQ10FNHM3	UNITS		
Maximum DC reverse voltage	$V_{R}$	100 V			
Maximum working peak reverse voltage	$V_{RWM}$	100	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		_	I <sub>F(AV)</sub> 50 % duty cycle at T <sub>C</sub> = 135 °C, rectangular waveform		3.5	
See fig. 5	per device	$I_{F(AV)}$ 50 % duty cycle at $T_C$ = 135 °C, rectangular waveform		7	Α	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	440	
			10 ms sine or 6 ms rect. pulse		70	
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	$T_{J} = 25  ^{\circ}\text{C},  I_{AS} = 1  \text{A},  L = 10  \text{mH}$		5.0	mJ
Repetitive avalanche current per leg I <sub>AR</sub>		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_B$ typical		0.5	Α



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	3 A	T <sub>.1</sub> = 25 °C	0.81	
Maximum forward		6 A	- IJ=25 C	0.96	
voltage drop per leg See fig. 1		3 A	T _ 105 °C	0.63	
555 lig. 1		6 A	- T <sub>J</sub> = 125 °C	0.74	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_B$ = Rated $V_B$	1	mA
See fig. 2		T <sub>J</sub> = 125 °C	VR = nateu VR	4.9	110/4
Threshold voltage	V <sub>F(TO)</sub>	T <sub>.l</sub> = T <sub>.l</sub> maximum		0.48	V
Forward slope resistance	r <sub>t</sub>			30.89	mΩ
Typical junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C		92	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.0		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 0		10 000	V/µs

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 40 to 150	°C
Maximum thermal resistance,	per leg	$R_{thJC}$	DC operation	4.70	°C/W
junction to case	per device	1 thJC	See fig. 4	2.35	O/ VV
Approximate weight				0.3	g
Approximate weight				0.01	OZ.
Marking device			Case style D-PAK	6CWQ	10FNH

#### Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ 

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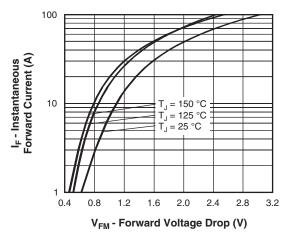


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

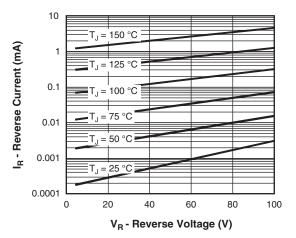


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

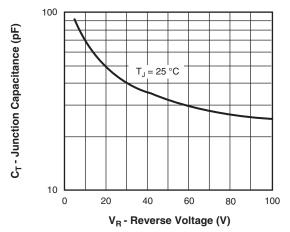


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

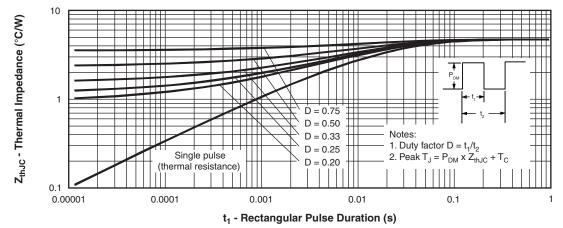


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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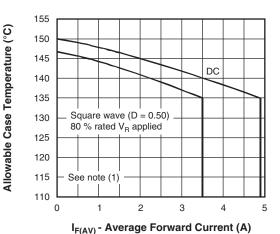


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

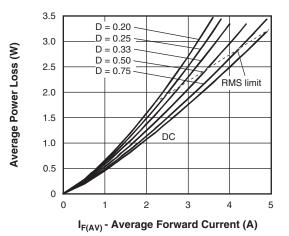


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

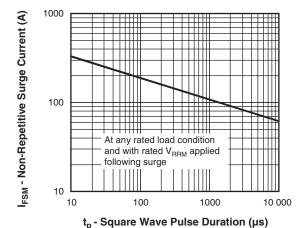


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

### Note

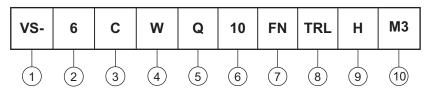
 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



### Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- 2 Current rating (7 A)
- Center tap configuration
- Package identifier:

W = D-PAK

- 5 Schottky "Q" series
- 6 Voltage rating (10 = 100 V)
- 7 FN = TO-252AA
- 8 • None = Tube
  - TR = Tape and reel
  - TRL = Tape and reel (left oriented)
  - TRR = Tape and reel (right oriented)
- 9 H = AEC-Q101 qualified
- 10 Environmental digit:

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-6CWQ10FNHM3	75	3000	Antistatic plastic tube		
VS-6CWQ10FNTRHM3	2000	2000	13" diameter reel		
VS-6CWQ10FNTRRHM3	3000	3000	13" diameter reel		
VS-6CWQ10FNTRLHM3	3000	3000	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information <u>www.vishay.com/doc?95518</u>				
Packaging information	www.vishay.com/doc?95033			



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