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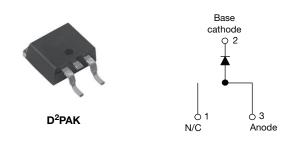
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VS-8TQ080SHM3, VS-8TQ100SHM3

Vishay Semiconductors

High Performance Schottky Rectifier, 8 A



www.vishay.com

PRODUCT SUMMARY									
I _{F(AV)}	8 A								
V _R	80 V, 100 V								
V _F at I _F	0.58 V								
I _{RM}	7 mA at 125 °C								
T _J max.	175 °C								
E _{AS}	7.5 mJ								
Package	TO-263AB (D ² PAK)								
Diode variation	Single die								

FEATURES

- 175 °C T_J operation
- · Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD 201 class 1A whisker test
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-8TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I _{F(AV)}	Rectangular waveform	8	A							
V _{RRM}	Range	80/100	V							
I _{FSM}	t _p = 5 μs sine	850	А							
V _F	8 A _{pk} , T _J = 125 °C	0.58	V							
TJ	Range	-55 to 175	°C							

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS-8TQ080SHM3	VS-8TQ100SHM3	UNITS						
Maximum DC reverse voltage	V _R	80	100	V						
Maximum working peak reverse voltage	V _{RWM}	80	100	v						

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS							
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_C = 157 °C	8	А						
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	850	А					
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V_{RRM} applied	230	~					
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 0.50 \ A, \ L = 60$	7.50	mJ						
Repetitive avalanche current	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim	0.50	А						

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VS-8TQ080SHM3, VS-8TQ100SHM3

Vishay Semiconductors

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS				
		8 A	T. = 25 °C	0.72					
Maximum forward voltage drop	V (1)	16 A	$1_{\rm J} = 25$ C	0.88					
See fig. 1	V _{FM} ⁽¹⁾	8 A	T 105 %C	0.58	V				
		16 A	T _J = 125 °C	0.69					
Maximum reverse leakage current	1 (1)	T _J = 25 °C	V Detect V	0.55	mA				
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	$V_R = Rated V_R$	7					
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal ran	ge 100 kHz to 1 MHz), 25 °C	500	pF				
Typical series inductance	L _S	Measured lead to lead 5 r	8	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs					

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MEC	THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 175	°C					
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.0	°C/W					
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	C/W					
Approximate weight				2	g					
Approximate weight				0.07	oz.					
Mounting torque	minimum			6 (5)	kgf · cm					
Mounting torque	maximum			12 (10)	(lbf · in)					
Marking davias			C_{222} at the $D^2 DAK$	8TQ0	80SH					
Marking device			Case style D ² PAK		00SH					

VS-8TQ080SHM3, VS-8TQ100SHM3 www.vishay.com **Vishay Semiconductors** 1000 100 Т_{.1} = 175 °С I_R - Reverse Current (mA) 10 = 150 I_F - Instantaneous Forward Current (A) 100 = 125 1 175 °C = = 100 °C 10 0.1 = 125 °C Г_J = 25 °С 1 = 75 °C 0.01 1 50 °C 0.001 0.1 0.0001 20 0 0.2 0.4 0.6 0.8 1.0 1.2 1.4 1.6 1.8 2.0 2.2 40 80 0 60 100 V_R - Reverse Voltage (V) V_{FM} - Forward Voltage Drop (V)

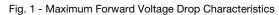


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

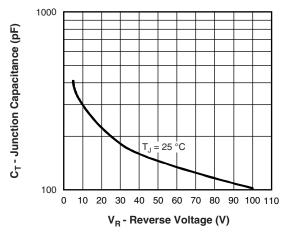


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

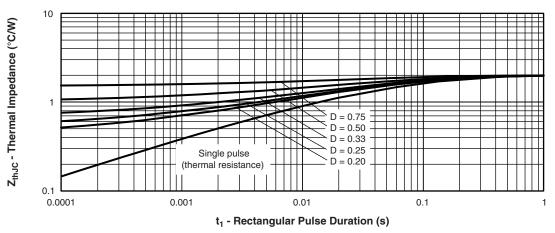
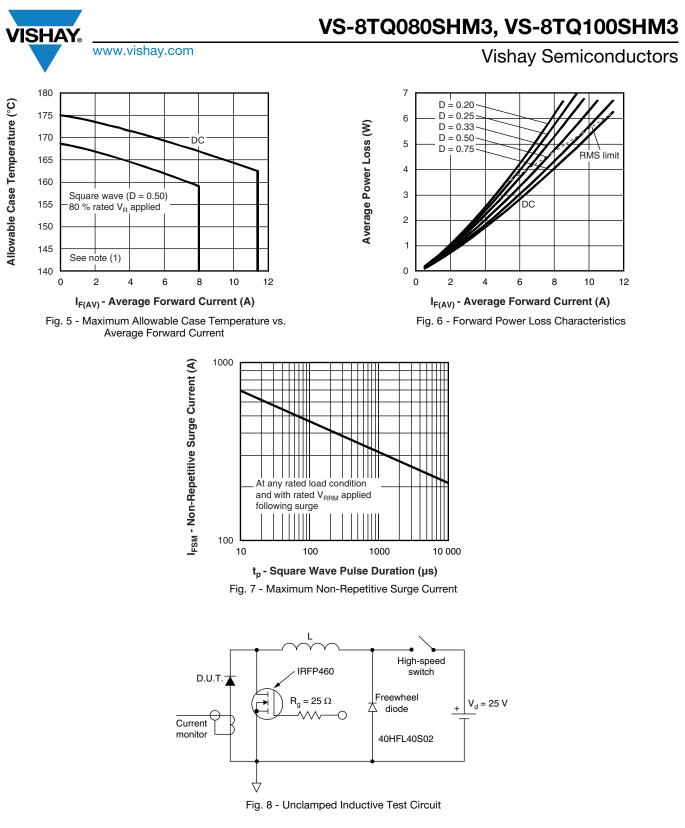


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



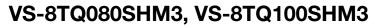
Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
- Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 D)$; I_R at V_{R1} = 80 % rated V_R

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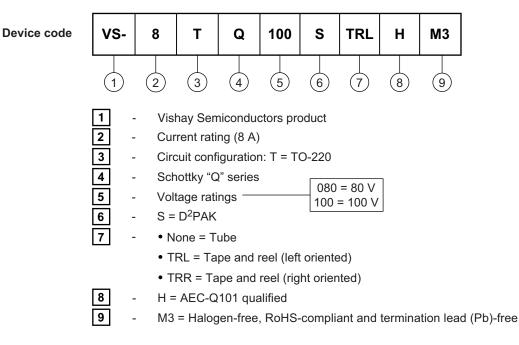
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Vishay Semiconductors

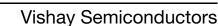
ORDERING INFORMATION TABLE



ORDERING INFORMATION										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-8TQ080SHM3	50	1000	Antistatic plastic tubes							
VS-8TQ080STRRHM3	800	800	13" diameter reel							
VS-8TQ080STRLHM3	800	800	13" diameter reel							
VS-8TQ100SHM3	50	1000	Antistatic plastic tubes							
VS-8TQ100STRRHM3	800	800	13" diameter reel							
VS-8TQ100STRLHM3	800	800	13" diameter reel							

LINKS TO RELATED DOCUMENTS								
Dimensions	www.vishay.com/doc?95046							
Part marking information	www.vishay.com/doc?95444							
Packaging information	www.vishay.com/doc?95032							
SPICE model	www.vishay.com/doc?95291							

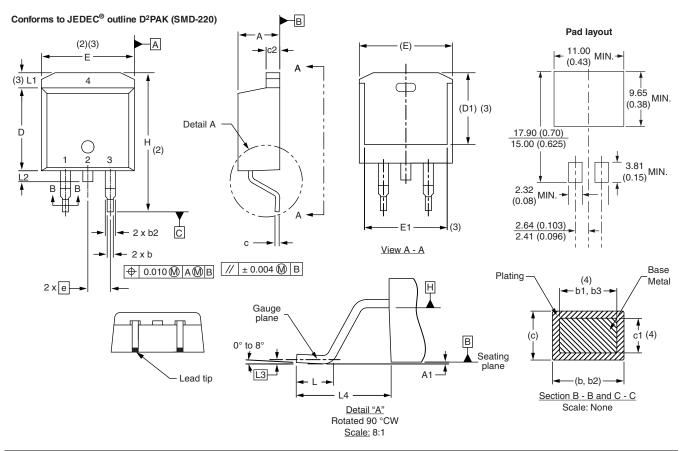
Outline Dimensions



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D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54 BSC		0.100 BSC		
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
с	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25 BSC		0.25 BSC 0.010 BSC		
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

- ⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1
- ⁽⁴⁾ Dimension b1 and c1 apply to base metal only
- ⁽⁵⁾ Datum A and B to be determined at datum plane H
- ⁽⁶⁾ Controlling dimension: inch
- ⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

Document Number: 95046

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