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## **VS-ST180SPbF Series**

**Vishay Semiconductors** 

## Phase Control Thyristors (Stud Version), 200 A



PRODUCT SUMMARY					
I <sub>T(AV)</sub>	200 A				
V <sub>DRM</sub> /V <sub>RRM</sub>	400 V, 800 V, 1200 V, 1600 V, 2000 V				
V <sub>TM</sub>	1.75 V				
I <sub>GT</sub>	150 mA				
TJ	-40 °C to 125 °C				
Package	TO-209AB (TO-93)				
Diode variation	Single SCR				

### FEATURES

- Center amplifying gate
- International standard case TO-209AB (TO-93)
- Hermetic metal case with ceramic insulator (Also available with glass-metal seal up to 1200 V)



COMPLIANT

- Compression bonded encapsulation for heavy duty operations such as severe thermal cycling
- Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **TYPICAL APPLICATIONS**

- DC motor controls
- Controlled DC power supplies
- AC controllers

MAJOR RATINGS AND CHARACTERISTICS							
PARAMETER	TEST CONDITIONS	VALUES	UNITS				
1		200	А				
I <sub>T(AV)</sub>	T <sub>C</sub>	85	°C				
I <sub>T(RMS)</sub>		314	A				
1	50 Hz	5000	•				
ITSM	60 Hz	5230	A				
l <sup>2</sup> t	50 Hz	125	kA <sup>2</sup> s				
1-1	60 Hz	114	KA-S				
V <sub>DRM</sub> /V <sub>RRM</sub>		400 to 2000	V				
t <sub>q</sub>	Typical	100	μs				
TJ		-40 to 125	°C				

#### **ELECTRICAL SPECIFICATIONS**

VOLTAGE	RATINGS			
TYPE NUMBER	VOLTAGE CODE	V <sub>DRM</sub> /V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK VOLTAGE V	$I_{DRM}/I_{RRM} MAXIMUM$ AT T <sub>J</sub> = T <sub>J</sub> MAXIMUM mA
	04	400	500	
	08	800	900	
VS-ST180S	12	1200	1300	30
	16	1600	1700	
	20	2000	2100	

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## **VS-ST180SPbF Series**



Vishay Semiconductors

ABSOLUTE MAXIMUM RATING	S						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS	
Maximum average on-state current	I <sub>T(AV)</sub>	180° condu	ction, half sine v	wave	200	Α	
at case temperature	1(7,7)		,		85	°C	
Maximum RMS on-state current	I <sub>T(RMS)</sub>	DC at 76 °C	case temperat	ure	314		
		t = 10 ms	No voltage		5000		
Maximum peak, one-cycle	L	t = 8.3 ms	reapplied		5230	A	
non-repetitive surge current	I <sub>TSM</sub>	t = 10 ms	100 % V <sub>RRM</sub>		4200		
		t = 8.3 ms	reapplied	Sinusoidal half wave,	4400		
		t = 10 ms	No voltage reapplied	initial $T_J = T_J$ maximum	125	kA <sup>2</sup> s	
loving 12t for fusing		t = 8.3 ms			114		
Maximum I <sup>2</sup> t for fusing		100 % V <sub>RRM</sub>		88	KA-S		
		t = 8.3 ms	reapplied		81	1	
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 to 10	) ms, no voltage	reapplied	1250	kA²√s	
Low level value of threshold voltage	V <sub>T(TO)1</sub>	(16.7 % x π	$x I_{T(AV)} < I < \pi x$	$I_{T(AV)}$ ), $T_J = T_J$ maximum	1.08	v	
High level value of threshold voltage	V <sub>T(TO)2</sub>	$(I > \pi \times I_{T(AV)})$	$(I > \pi x I_{T(AV)}), T_J = T_J maximum$				
Low level value of on-state slope resistance	r <sub>t1</sub>	(16.7 % x $\pi$ x $I_{T(AV)}$ < I < $\pi$ x $I_{T(AV)}$ ), T <sub>J</sub> = T <sub>J</sub> maximum			1.18	mΩ	
High level value of on-state slope resistance	r <sub>t2</sub>	$(I > \pi \times I_{T(AV)})$	1.14	11152			
Maximum on-state voltage	V <sub>TM</sub>	$I_{pk} = 570 \text{ A}, T_J = 125 \text{ °C}, t_p = 10 \text{ ms sine pulse}$			1.75	V	
Maximum holding current	Ι <sub>Η</sub>	T _ T mov	imum anada a	upply 12 V resistive load	600	mA	
Maximum (typical) latching current	١L	ij=ijinax	anode st		1000 (300)		

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum non-repetitive rate of rise of turned-on current	dl/dt	Gate drive 20 V, 20 $\Omega,t_r \le 1~\mu s$ $T_J$ = $T_J$ maximum, anode voltage $\le 80~\%~V_{DRM}$	1000	A/µs
Typical delay time	t <sub>d</sub>	Gate current 1 A, dl <sub>g</sub> /dt = 1 A/ $\mu$ s V <sub>d</sub> = 0.67 % V <sub>DRM</sub> , T <sub>J</sub> = 25 °C	1.0	
Typical turn-off time	tq	$I_{TM}$ = 300 A, $T_J$ = $T_J$ maximum, dl/dt = 20 A/µs, $V_R$ = 50 V, dV/dt = 20 V/µs, gate 0 V 100 $\Omega,$ $t_p$ = 500 µs	100	μs

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum critical rate of rise of off-state voltage	dV/dt	$T_J = T_J$ maximum linear to 80 % rated $V_{DRM}$	500	V/µs
Maximum peak reverse and off-state leakage current	I <sub>RRM</sub> , I <sub>DRM</sub>	$T_J = T_J$ maximum, rated $V_{DRM}/V_{RRM}$ applied	30	mA



## **Vishay Semiconductors**

TRIGGERING						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS			VALUES	
PARAMETER	STMBUL			TYP.	MAX.	UNITS
Maximum peak gate power	P <sub>GM</sub>	$T_J = T_J maximum,$	, $t_p \le 5 \text{ ms}$	1	0	W
Maximum average gate power	P <sub>G(AV)</sub>	$T_J = T_J maximum,$	, f = 50 Hz, d% = 50	2.	.0	vv
Maximum peak positive gate current	I <sub>GM</sub>	$T_J = T_J maximum,$	, $t_p \le 5 \text{ ms}$	3.	.0	А
Maximum peak positive gate voltage	+ V <sub>GM</sub>			20		V
Maximum peak negative gate voltage	- V <sub>GM</sub>	$T_J = T_J$ maximum, $t_p \le 5$ ms		5.	.0	v
		T <sub>J</sub> = - 40 °C		180	-	
DC gate current required to trigger	I <sub>GT</sub>	T <sub>J</sub> = 25 °C		90	150	mA
		T <sub>J</sub> = 125 °C	Maximum required gate trigger/ current/voltage are the lowest	40	-	
		T <sub>J</sub> = - 40 °C	value which will trigger all units 12 V anode to cathode applied	2.9	-	
DC gate voltage required to trigger	V <sub>GT</sub>	T <sub>J</sub> = 25 °C	12 V anode to cathode applied	1.8	3.0	V
		T <sub>J</sub> = 125 °C		1.2	-	
DC gate current not to trigger	I <sub>GD</sub>		Maximum gate current/voltage	10		mA
DC gate voltage not to trigger	V <sub>GD</sub>	$T_J = T_J maximum$	not to trigger is the maximum value which will not trigger any unit with rated V <sub>DRM</sub> anode to cathode applied	0.2	25	V

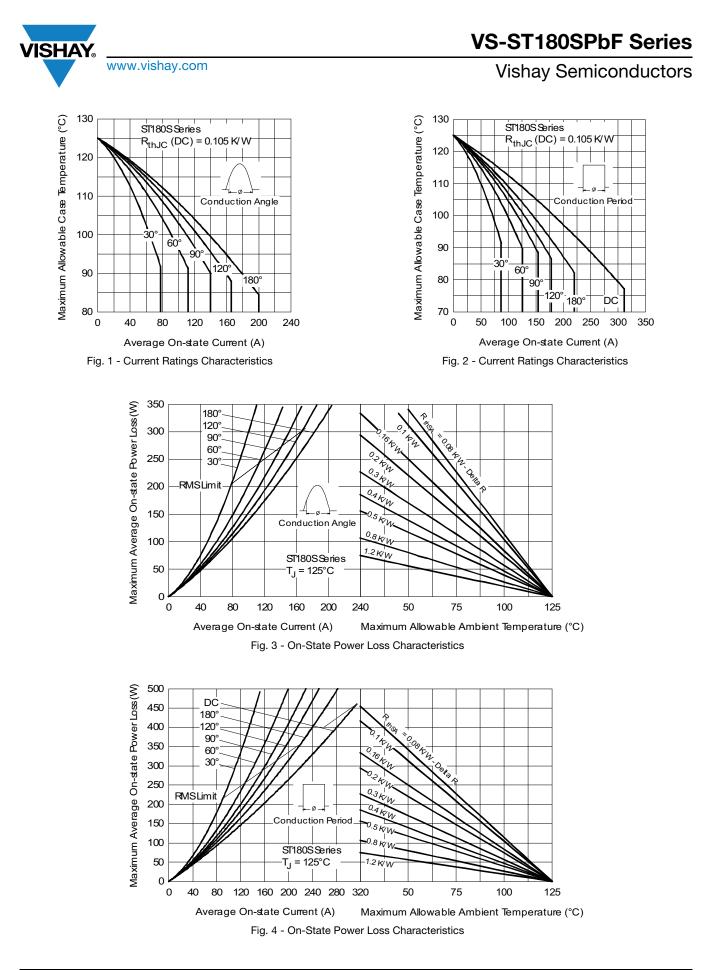
THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum operating junction temperature range	TJ		-40 to 125	°C			
Maximum storage temperature range	T <sub>Stg</sub>		-40 to 150				
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	0.105	K/W			
Maximum thermal resistance, case to heatsink	R <sub>thC-hs</sub>	Mounting surface, smooth, flat and greased	0.04	- r\/ vv			
Mounting torque + 10.0/		Non-lubricated threads	31 (275)	N·m			
Mounting torque, ± 10 %		Lubricated threads	24.5 (210)	(lbf · in)			
Approximate weight			280	g			
Case style		See dimensions - link at the end of datasheeet	TO-209AB (1	<sup>-</sup> O-93)			

$\Delta \mathbf{R}_{\text{thJC}}$ CONDUCTION				
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS
180°	0.015	0.012		
120°	0.019	0.020		
90°	0.025	0.027	$T_J = T_J maximum$	K/W
60°	0.036	0.037		
30°	0.060	0.060		

Note

• The table above shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC

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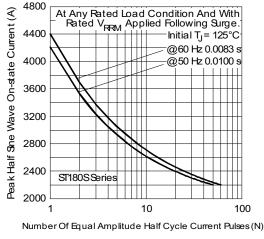
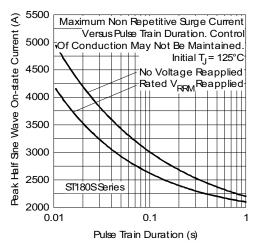
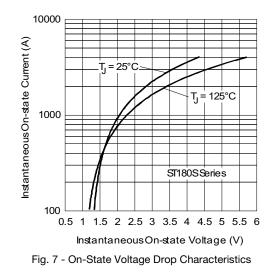
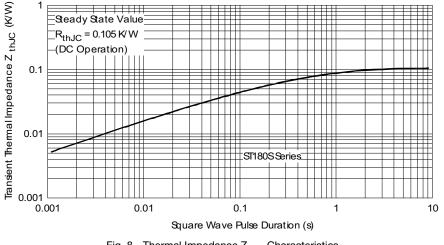


Fig. 5 - Maximum Non-Repetitive Surge Current



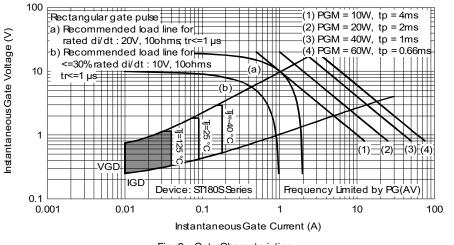








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#### Fig. 9 - Gate Characteristics

### **ORDERING INFORMATION TABLE**

SHA

Device code	VS-	ST	18	0	S	20	Р	0	-	PbF
	1	2	3	4	5	6	7	8	9	10
	1 - 2 - 3 - 4 - 5 -	- Thy - Ess - 0 =	ristor ential p Conver	niconduo art numt ter grado ession b	ber					
	6 - 7 - 8 -	- P=	Stud ba	de x 100 ase 3/4"- erminals	16UNF2	2A threa	ads	-		
	9 -	1 = - V =	Fast-on Glass-r	termina netal se ramic ho	als (gate al (only	and au up to 12	xiliary c 200 V)		-	
	10 - - Note: Fo	- PbF	= Lead	ndard p I (Pb)-fre M16 x 1	ee		ory			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95082			

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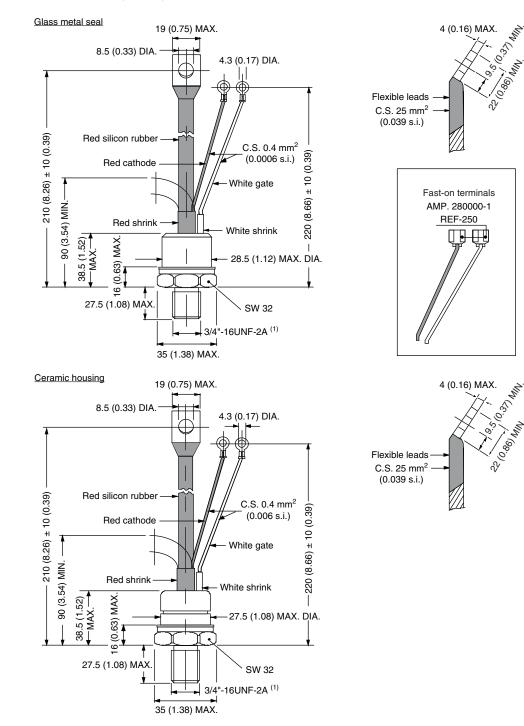
NI.

NIN,



#### **DIMENSIONS** in millimeters (inches)

www.vishay.com



#### Note

<sup>(1)</sup> For metric device: M16 x 1.5 - length 21 (0.83) maximum

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