imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





VS-ST180SPbF Series

Vishay Semiconductors

Phase Control Thyristors (Stud Version), 200 A



PRODUCT SUMMARY					
I _{T(AV)}	200 A				
V _{DRM} /V _{RRM}	400 V, 800 V, 1200 V, 1600 V, 2000 V				
V _{TM}	1.75 V				
I _{GT}	150 mA				
TJ	-40 °C to 125 °C				
Package	TO-209AB (TO-93)				
Diode variation	Single SCR				

FEATURES

- Center amplifying gate
- International standard case TO-209AB (TO-93)
- Hermetic metal case with ceramic insulator (Also available with glass-metal seal up to 1200 V)



COMPLIANT

- Compression bonded encapsulation for heavy duty operations such as severe thermal cycling
- Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- DC motor controls
- Controlled DC power supplies
- AC controllers

MAJOR RATINGS AND CHARACTERISTICS							
PARAMETER	TEST CONDITIONS	VALUES	UNITS				
1		200	А				
I _{T(AV)}	T _C	85	°C				
I _{T(RMS)}		314	A				
1	50 Hz	5000	•				
ITSM	60 Hz	5230	A				
l ² t	50 Hz	125	kA ² s				
1-1	60 Hz	114	KA-S				
V _{DRM} /V _{RRM}		400 to 2000	V				
t _q	Typical	100	μs				
TJ		-40 to 125	°C				

ELECTRICAL SPECIFICATIONS

VOLTAGE	RATINGS			
TYPE NUMBER	VOLTAGE CODE	V _{DRM} /V _{RRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK VOLTAGE V	$I_{DRM}/I_{RRM} MAXIMUM$ AT T _J = T _J MAXIMUM mA
	04	400	500	
	08	800	900	
VS-ST180S	12	1200	1300	30
	16	1600	1700	
	20	2000	2100	

Revision: 11-Mar-14 1 Document Number: 94397 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

VS-ST180SPbF Series



Vishay Semiconductors

ABSOLUTE MAXIMUM RATING	S						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS	
Maximum average on-state current	I _{T(AV)}	180° condu	ction, half sine v	wave	200	Α	
at case temperature	1(7,7)		,		85	°C	
Maximum RMS on-state current	I _{T(RMS)}	DC at 76 °C	case temperat	ure	314		
		t = 10 ms	No voltage		5000		
Maximum peak, one-cycle	L	t = 8.3 ms	reapplied		5230	A	
non-repetitive surge current	I _{TSM}	t = 10 ms	100 % V _{RRM}		4200		
		t = 8.3 ms	reapplied	Sinusoidal half wave,	4400		
		t = 10 ms	No voltage reapplied	initial $T_J = T_J$ maximum	125	kA ² s	
loving 12t for fusing		t = 8.3 ms			114		
Maximum I ² t for fusing		100 % V _{RRM}		88	KA-S		
		t = 8.3 ms	reapplied		81	1	
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10) ms, no voltage	reapplied	1250	kA²√s	
Low level value of threshold voltage	V _{T(TO)1}	(16.7 % x π	$x I_{T(AV)} < I < \pi x$	$I_{T(AV)}$), $T_J = T_J$ maximum	1.08	v	
High level value of threshold voltage	V _{T(TO)2}	$(I > \pi \times I_{T(AV)})$	$(I > \pi x I_{T(AV)}), T_J = T_J maximum$				
Low level value of on-state slope resistance	r _{t1}	(16.7 % x π x $I_{T(AV)}$ < I < π x $I_{T(AV)}$), T _J = T _J maximum			1.18	mΩ	
High level value of on-state slope resistance	r _{t2}	$(I > \pi \times I_{T(AV)})$	1.14	11152			
Maximum on-state voltage	V _{TM}	$I_{pk} = 570 \text{ A}, T_J = 125 \text{ °C}, t_p = 10 \text{ ms sine pulse}$			1.75	V	
Maximum holding current	Ι _Η	T _ T mov	imum anada a	upply 12 V resistive load	600	mA	
Maximum (typical) latching current	١L	ij=ijinax	anode st		1000 (300)		

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum non-repetitive rate of rise of turned-on current	dl/dt	Gate drive 20 V, 20 $\Omega,t_r \le 1~\mu s$ T_J = T_J maximum, anode voltage $\le 80~\%~V_{DRM}$	1000	A/µs
Typical delay time	t _d	Gate current 1 A, dl _g /dt = 1 A/ μ s V _d = 0.67 % V _{DRM} , T _J = 25 °C	1.0	
Typical turn-off time	tq	I_{TM} = 300 A, T_J = T_J maximum, dl/dt = 20 A/µs, V_R = 50 V, dV/dt = 20 V/µs, gate 0 V 100 $\Omega,$ t_p = 500 µs	100	μs

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum critical rate of rise of off-state voltage	dV/dt	$T_J = T_J$ maximum linear to 80 % rated V_{DRM}	500	V/µs
Maximum peak reverse and off-state leakage current	I _{RRM} , I _{DRM}	$T_J = T_J$ maximum, rated V_{DRM}/V_{RRM} applied	30	mA



Vishay Semiconductors

TRIGGERING						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS			VALUES	
PARAMETER	STMBUL			TYP.	MAX.	UNITS
Maximum peak gate power	P _{GM}	$T_J = T_J maximum,$, $t_p \le 5 \text{ ms}$	1	0	W
Maximum average gate power	P _{G(AV)}	$T_J = T_J maximum,$, f = 50 Hz, d% = 50	2.	.0	vv
Maximum peak positive gate current	I _{GM}	$T_J = T_J maximum,$, $t_p \le 5 \text{ ms}$	3.	.0	А
Maximum peak positive gate voltage	+ V _{GM}			20		V
Maximum peak negative gate voltage	- V _{GM}	$T_J = T_J$ maximum, $t_p \le 5$ ms		5.	.0	v
		T _J = - 40 °C		180	-	
DC gate current required to trigger	I _{GT}	T _J = 25 °C		90	150	mA
		T _J = 125 °C	Maximum required gate trigger/ current/voltage are the lowest	40	-	
		T _J = - 40 °C	value which will trigger all units 12 V anode to cathode applied	2.9	-	
DC gate voltage required to trigger	V _{GT}	T _J = 25 °C	12 V anode to cathode applied	1.8	3.0	V
		T _J = 125 °C		1.2	-	
DC gate current not to trigger	I _{GD}		Maximum gate current/voltage	10		mA
DC gate voltage not to trigger	V _{GD}	$T_J = T_J maximum$	not to trigger is the maximum value which will not trigger any unit with rated V _{DRM} anode to cathode applied	0.2	25	V

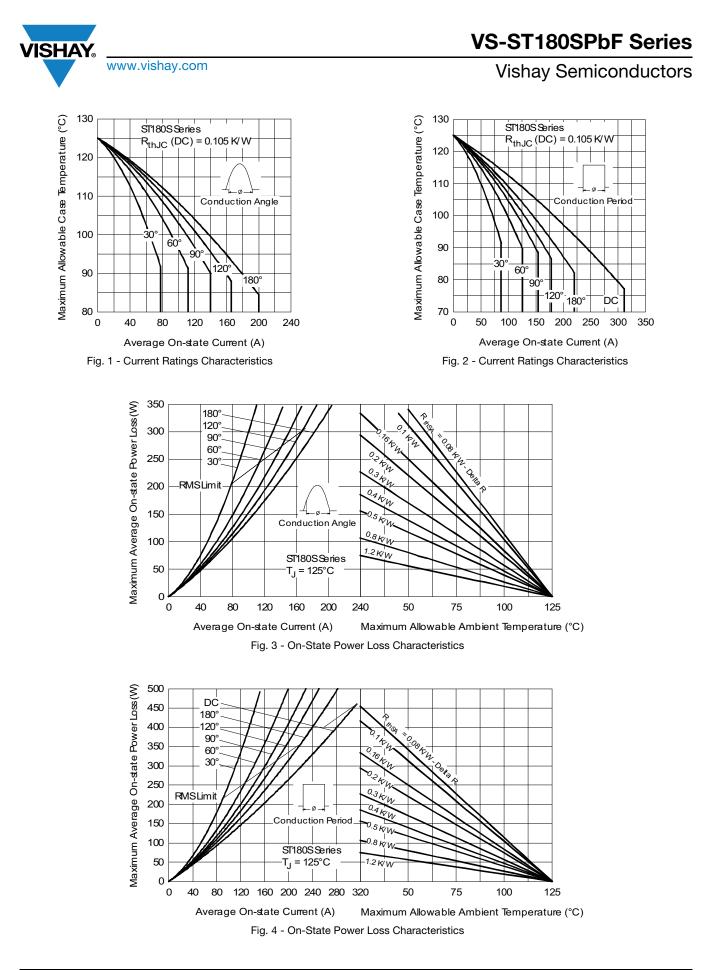
THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum operating junction temperature range	TJ		-40 to 125	°C			
Maximum storage temperature range	T _{Stg}		-40 to 150				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.105	K/W			
Maximum thermal resistance, case to heatsink	R _{thC-hs}	Mounting surface, smooth, flat and greased	0.04	- r\/ vv			
Mounting torque + 10.0/		Non-lubricated threads	31 (275)	N·m			
Mounting torque, ± 10 %		Lubricated threads	24.5 (210)	(lbf · in)			
Approximate weight			280	g			
Case style		See dimensions - link at the end of datasheeet	TO-209AB (1	⁻ O-93)			

$\Delta \mathbf{R}_{\text{thJC}}$ CONDUCTION				
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS
180°	0.015	0.012		
120°	0.019	0.020		
90°	0.025	0.027	$T_J = T_J maximum$	K/W
60°	0.036	0.037		
30°	0.060	0.060		

Note

• The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

Revision: 11-Mar-14 3 Document Number: 94397 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



Vishay Semiconductors



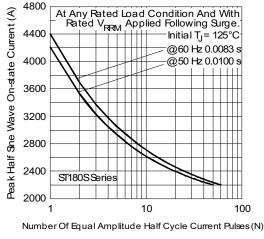
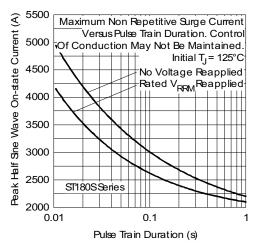
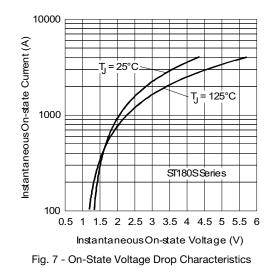
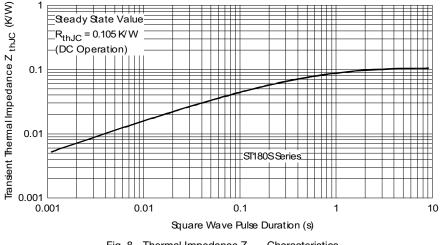


Fig. 5 - Maximum Non-Repetitive Surge Current











VS-ST180SPbF Series www.vishay.com **Vishay Semiconductors**

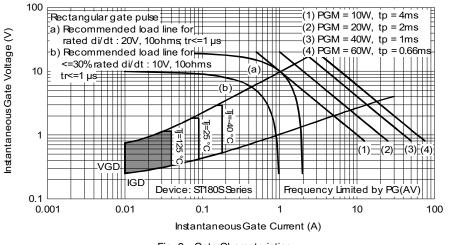


Fig. 9 - Gate Characteristics

ORDERING INFORMATION TABLE

SHA

Device code	VS-	ST	18	0	S	20	Р	0	-	PbF
	1	2	3	4	5	6	7	8	9	10
	1 - 2 - 3 - 4 - 5 -	- Thy - Ess - 0 =	ristor ential p Conver	niconduo art numt ter grado ession b	ber					
	6 - 7 - 8 -	- P=	Stud ba	de x 100 ase 3/4"- erminals	16UNF2	2A threa	ads	-		
	9 -	1 = - V =	Fast-on Glass-r	termina netal se ramic ho	als (gate al (only	and au up to 12	xiliary c 200 V)		-	
	10 - - Note: Fo	- PbF	= Lead	ndard p I (Pb)-fre M16 x 1	ee		ory			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95082			

Revision: 11-Mar-14 Document Number: 94397 For technical questions within your region: DiodesAmericas@vishav.com, DiodesAsia@vishav.com, DiodesEurope@vishav.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

Vishay Semiconductors

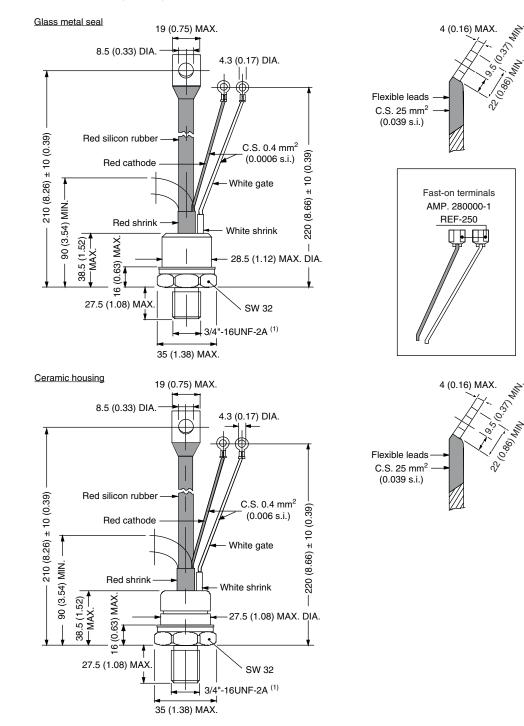
NI.

NIN,



DIMENSIONS in millimeters (inches)

www.vishay.com



Note

⁽¹⁾ For metric device: M16 x 1.5 - length 21 (0.83) maximum

Revision: 05-Mar-12

Document Number: 95082

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

1



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.