mail

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



BACKPLANE PRODUCTS

VSC7280

VITESSE

Dual XAUI – Dual XGMII Octal 0.95 Gbps to 3.25 Gbps Transceiver



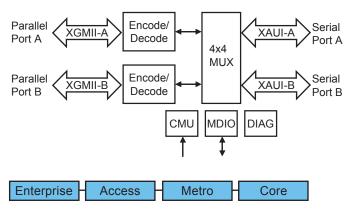
APPLICATIONS:

- 10 Gigabit Ethernet and Fibre Channel Links
 XENPAK, X2, XPAK Module Connections
- High Speed Serial Backplanes
 XAUI Channel
 - Independent Channel Control
- Box-to-Box Serial Communications
 Copper, Coax, and Fiber

SPECIFICATIONS:

- ▶ Packaged in a 448-pin, 23mm x 23mm TEPBGA
- \blacktriangleright Input Termination, Internal 100 Ω Differential
- Input Signal Detection and Signal Equalization
- ▶ Multiple Internal Clocking Modes

BLOCK DIAGRAM:



FEATURES:

- ▶ IEEE802.3ae 10 Gigabit Ethernet Compliant
- ANSI 10Gb Fibre Channel Compliant
- Dual XAUI Serial Bus
- Dual XGMII Parallel Bus
- ▶ 0.95 Gbps to 3.25 Gbps Octal SerDes/Transceiver
- > XAUI Compliant Retimer and Repeater Modes
- ▶ Simultaneous Operation at Multiple Frequencies
- Programmable Pre-Emphasis and Signal Equalization
- ▶ 8B/10B Encoder/Decoder with Optional Bypass
- Programmable Rate Matching Characters
- ► Elastic Buffer for Intra/Inter-Chip Deskewing and Channel-to-Channel Alignment
- ▶ Received Data Aligned to Local REFCLK or to Recovered Clock
- ▶ TX and RX Rate Matching for XAUI, 1GbE, and InfiniBand™
- Output Reduced Swing Level for Optics
- Serial and Parallel Loopback
- ▶ Fast Locking CRU
- ▶ Flexible CMU Operation
- ▶ IEEE802.3 MDIO Register Interface
- Multiple Built-In-Self Test Diagnostics and Counters
 PRBS, CRPAT, CJPAT Pattern Generation
- ▶ JTAG Boundary Scan
- HSTL1.5 and HSTL1.8 Parallel I/O

VSC7280

10000100

ζ

Dual XAUI – Dual XGMII Octal 0.95 Gbps to 3.25 Gbps Transceiver

GENERAL DESCRIPTION:



The VSC7280 is a Dual XAUI - Dual XGMII Transceivers tailored for 10 Gigabit Ethernet (10GbE) and 10 Gigabit Fibre Channel (10GFC) applications. The feature set and flexibility of these devices make them perfectly suited for Backplane and other

Serial Interconnect applications such as Serial Rapid I/O, PCI Express and XAUI.

With eight channels of serial transmitter/receiver pairs, these transceivers can deliver a duplex data rate of greater than 40 Gbps. These serial channels can be configured for independent operation, as two fully 10GbE/10GFC compliant XAUI (10 Gigabit Attachment Unit Interface) serial buses, or a combination thereof. The parallel bus is fully XGMII (10 Gigabit Media Independent Interface) compliant. When independent channel operation is required, the parallel bus can be configured for either 8- or 10-bit operation with the ability to enable or bypass the internal 8B/10B encoders/decoders.

No additional external logic is required for the basic operation of these devices. However, for flexibility and systems adaptability, an MDIO (Management Data Input/Output) interface is provided for internal register access. These registers are MDIO compliant and allow for monitoring and configuration of special modes for diagnostics, BIST (Built-In-Self Test), and various control of special channel features, such as pre-emphasis, cable equalization, reduced output swing, loopback, signal detection and XGMII/XAUI port selection.

The flexibility of this device is not only seen in the MDIO register programmable feature set, but also in a wide range of supported frequencies. With a serial data rate that can range from 0.95 Gbps to 3.25 Gbps, this device supports multiple communication applications such as Fibre Channel, InfiniBand, and Ethernet. The VSC7280 have the functionality to support these common rates simultaneous with a single external reference clock.

Whether the application is a high-speed backplane, chassis-tochassis communication or an optical link, these devices can provide the flexibility and reliability required in today's high-end Data Communication systems.

For more information on Vitesse Products visit the Vitesse web site

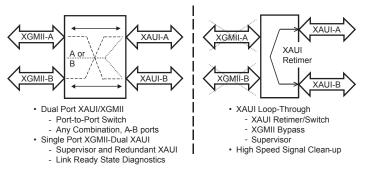
at www.vitesse.com or contact Vitesse Sales at (800) VITESSE

or sales@vitesse.com

Vitesse, ASIC-Friendly, FibreTimer, TimeStream and Snoop Loop are trademarks of Vitesse Semiconductor Corporation. All other trademarks or registered trademarks mentioned herein are the property of their respective holders. Vitesse Semiconductor Corporation ("Vitesse") retains the right to make changes to its products or specifications to improve performance, reliability or manufacturability. All information in this document, including descriptions of features. functions, performance, technical specifications and availability, is subject to change without notice at any time.

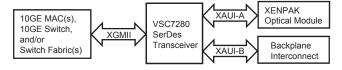
MODES OF OPERATION:

The VSC7280 allow for flexibility in multiple systems environments.

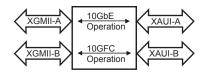


APPLICATIONS:

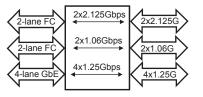
- **10 Gigabit Ethernet Port Application** · Driving out-of-box optics or backplane



10GbE and 10GFC Simultaneous Operation



2GFC, 1GFC, and 1GbE Simultaneous Operation



741 Calle Plano Camarillo, CA 93012, USA Tel: +1 805.388.3700 Fax: +1 805.987.5896 www.vitesse.com