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**DESCRIPTION** 

mounting (SMD).

## Vishay Semiconductors

# High Speed Infrared Emitting Diode, 890 nm, GaAlAs Double Hetero



VSMF3710 is an infrared, 890 nm emitting diode in GaAlAs

double hetero (DH) technology with high radiant power and

high speed, molded in a PLCC-2 package for surface

# FEATURES

Package type: surface mount

• Package form: PLCC-2

• Dimensions (L x W x H in mm): 3.5 x 2.8 x 1.75

Peak wavelength: λ<sub>p</sub> = 890 nm

High reliability

• High radiant power

· High radiant intensity

• Angle of half intensity:  $\varphi = \pm 60^{\circ}$ 

· Low forward voltage

· Suitable for high pulse current operation

• High modulation band width: f<sub>c</sub> = 12 MHz

· Good spectral matching with Si photodetectors

Floor life: 168 h, MSL 3, acc. J-STD-020

• Lead (Pb)-free reflow soldering

AEC-Q101 qualified

 Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

#### **APPLICATIONS**

- · High speed IR data transmission
- · High power emitter for low space applications
- High performance transmissive or reflective sensors

PRODUCT SUMMARY					
COMPONENT	I <sub>e</sub> (mW/sr)	φ (deg)	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)	
VSMF3710	10	± 60	890	30	

#### Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMF3710-GS08	Tape and reel	MOQ: 7500 pcs, 1500 pcs/reel	PLCC-2		
VSMF3710-GS18	Tape and reel	MOQ: 8000 pcs, 8000 pcs/reel	PLCC-2		

#### Note

• MOQ: minimum order quantity



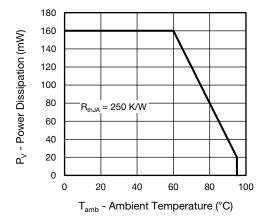
AUTOMOTIVE GRADE





# Vishay Semiconductors

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	5	V	
Forward current		I <sub>F</sub>	100	mA	
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I <sub>FM</sub>	200	mA	
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	1	Α	
Power dissipation		P <sub>V</sub>	160	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +95	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C	
Soldering temperature	Acc. figure 8, J-STD-020	T <sub>sd</sub>	260	°C	
Thermal resistance junction/ambient	J-STD-051, soldered on PCB	R <sub>thJA</sub>	250	K/W	



100 I<sub>F</sub> - Forward Current (mA) 80 60  $R_{thJA} = 250 \text{ K/W}$ 40 20 0 20 100 T<sub>amb</sub> - Ambient Temperature (°C)

Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

Fig. 2 - Forward Current Limit vs. Ambient Temperature

PARAMETER	(T <sub>amb</sub> = 25 °C, unless other	SYMBOL	MIN.	TYP.	MAX.	UNIT
FANAMETEN			IVIIIA.			
Forward voltage	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	V <sub>F</sub>	-	1.4	1.6	V
	$I_F = 1 \text{ A}, t_p = 100 \mu \text{s}$	$V_{F}$	-	2.3	-	V
Temperature coefficient of $V_F$	I <sub>F</sub> = 1 mA	TK <sub>VF</sub>	-	-1.8	-	mV/K
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μΑ
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz, E = 0	Cj	-	125	-	pF
Radiant intensity	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	l <sub>e</sub>	6	10	22	mW/sr
	$I_F = 1 \text{ A}, t_p = 100 \ \mu\text{s}$	I <sub>e</sub>	-	100	-	mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	фe	-	40	-	mW
Temperature coefficient of $\phi_e$	I <sub>F</sub> = 100 mA	TKφ <sub>e</sub>	-	-0.35	-	%/K
Angle of half intensity		φ	-	± 60	-	deg
Peak wavelength	I <sub>F</sub> = 100 mA	$\lambda_{p}$	-	890	-	nm
Spectral bandwidth	I <sub>F</sub> = 100 mA	Δλ	-	40	-	nm
Temperature coefficient of λ <sub>p</sub>	I <sub>F</sub> = 100 mA	TKλ <sub>p</sub>	-	0.25	-	nm/K
Rise time	I <sub>F</sub> = 100 mA	t <sub>r</sub>	=	30	-	ns
Fall time	I <sub>F</sub> = 100 mA	t <sub>f</sub>	-	30	-	ns
Cut-off frequency	I <sub>DC</sub> = 70 mA, I <sub>AC</sub> = 30 mA pp	f <sub>c</sub>	-	12	-	MHz
Virtual source diameter		d	-	0.44	_	mm



### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

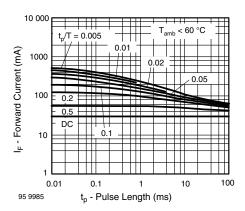


Fig. 3 - Pulse Forward Current vs. Pulse Duration

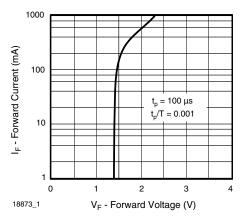


Fig. 4 - Forward Current vs. Forward Voltage

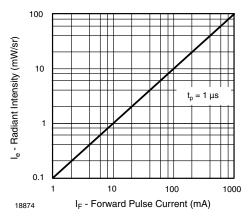


Fig. 5 - Radiant Intensity vs. Forward Current

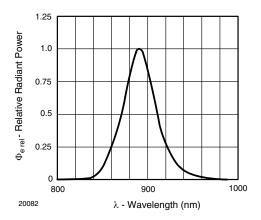


Fig. 6 - Relative Radiant Power vs. Wavelength

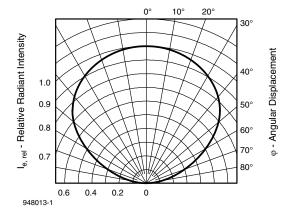
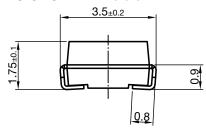
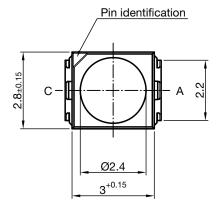


Fig. 7 - Relative Radiant Intensity vs. Angular Displacement



#### **PACKAGE DIMENSIONS** in millimeters



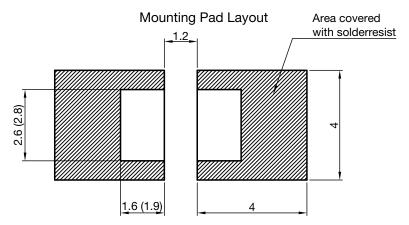




Dimensions in mm

Drawing-No.: 6.541-5067.01-4

Issue: 6; 23.09.13



Dimensions: Reflow and vapor phase (wave soldering)

#### **SOLDER PROFILE**

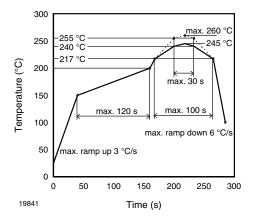


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

Moisture sensitivity level 3, acc. to J-STD-020.

#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40  $^{\circ}$ C (+ 5  $^{\circ}$ C), RH < 5  $^{\circ}$ M.

## Vishay Semiconductors

#### **TAPE AND REEL**

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.

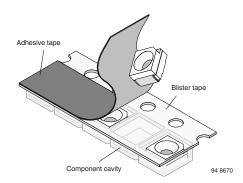


Fig. 9 - Blister Tape

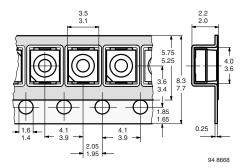


Fig. 10 - Tape Dimensions in mm for PLCC-2

#### **MISSING DEVICES**

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.

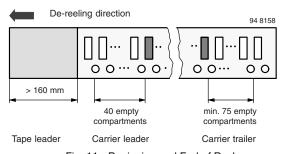


Fig. 11 - Beginning and End of Reel

The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartments and sealed with cover tape.

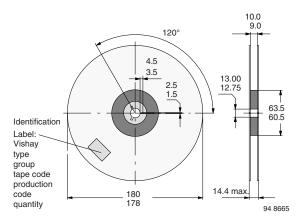


Fig. 12 - Dimensions of Reel-GS08

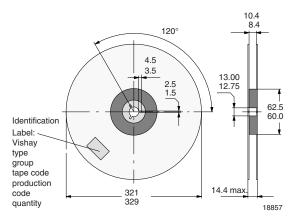


Fig. 13 - Dimensions of Reel-GS18

#### **COVER TAPE REMOVAL FORCE**

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.



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Vishay

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