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VSMY14940



Vishay Semiconductors

High Speed Infrared Emitting Diodes, 940 nm, Surface Emitter Technology



DESCRIPTION

As part of the <u>SurfLight</u>TM portfolio, the VSMY14940 is an infrared, 940 nm, side looking emitting diode based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted PCB based package (with lens) for surface mounting (SMD).

APPLICATIONS

- Emitter for remote control
- IR touch panels
- Photointerrupters
- Optical switch

FEATUF	RES
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- Package type: surface mount
- Package form: side view
- Dimensions (L x W x H in mm): 3.2 x 2.51 x 1.2
- Peak wavelength: $\lambda_p = 940 \text{ nm}$
- High reliability
- · High radiant power
- · Very high radiant intensity
- Angle of half intensity: $\phi = \pm 9^{\circ}$
- Suitable for high pulse current operation
- Floor life: 168 h, MSL 3, according to J-STD-020
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PRODUCT SUMMARY					
COMPONENT	l _e (mW/sr)	φ (deg)	λ _p (nm)	t _r (ns)	
VSMY14940	82	± 9	940	10	

Note

[•] Test condition see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE PACKAGING		REMARKS	PACKAGE FORM		
VSMY14940	Tape and reel	MOQ: 1500 pcs, 1500 pcs/reel	Side view		

Note

• MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	5	V	
Forward current		١ _F	70	mA	
Surge forward current	t _p = 100 μs	I _{FSM}	1	А	
Power dissipation		Pv	119	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T _{amb}	-40 to +85	°C	
Storage temperature range		T _{stg}	-40 to +100	°C	
Soldering temperature	According fig. 10, J-STD-020	T _{sd}	260	°C	
Thermal resistance junction / ambient	J-STD-051, soldered on PCB	R _{thJA}	390	K/W	

Rev. 1.4, 19-Nov-15

1 For technical questions, contact: <u>emittertechsupport@vishay.com</u> Document Number: 84209

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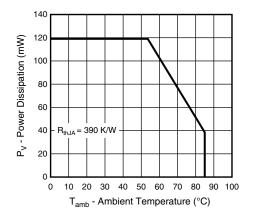


RoHS

COMPLIANT

<u>GREEN</u>

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Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

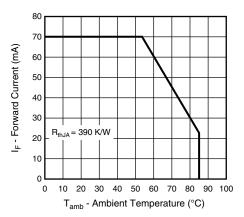
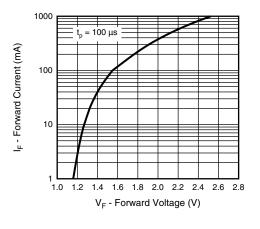


Fig. 2 - Forward Current Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	V _F	1.1	1.32	1.6	V
Forward voltage	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	V _F	-	1.48	1.7	V
	I _F = 1 A, t _p = 100 μs	V _F	-	2.5	-	V
Temperature coefficient of V _F	I _F = 20 mA	TK _{VF}	1.7 -		-	mV/K
Reverse current		I _R	not designed for reverse operation			μA
Junction capacitance	$V_{R} = 0 V, f = 1 MHz, E = 0 mW/cm^{2}$	CJ	-	5	-	pF
Radiant intensity	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	l _e	15	24	35	mW/sr
	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	l _e	50	82	120	mW/sr
	I _F = 1 A, t _p = 100 μs	l _e	-	660	-	mW/sr
Radiant power	I _F = 70 mA, t _p = 20 ms	фе	-	40	-	mW
Temperature coefficient of radiant power	I _F = 20 mA	$TK\phi_{e}$	-	-0.21	-	%/K
Angle of half intensity		φ	-	± 9	-	deg
Peak wavelength	I _F = 20 mA	λp	920	940	960	nm
Spectral bandwidth	I _F = 20 mA	Δλ	-	35	-	nm
Temperature coefficient of λ_p	I _F = 20 mA	ΤΚλ _ρ	-	0.25	-	nm/K
Rise time	I _F = 100 mA, 20 % to 80 %	tr	-	10	-	ns
Fall time	I _E = 100 mA, 20 % to 80 %	t _f	-	10	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)





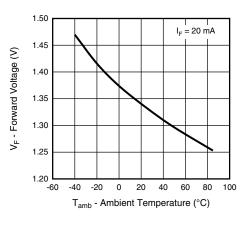


Fig. 4 - Forward Voltage vs. Ambient Temperature

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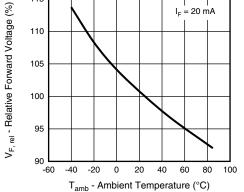


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

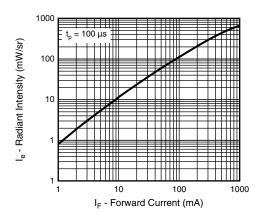


Fig. 6 - Radiant Intensity vs. Forward Current

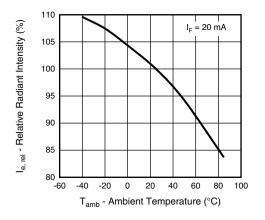


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature

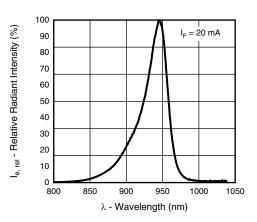


Fig. 8 - Relative Radiant Intensity vs. Wavelength

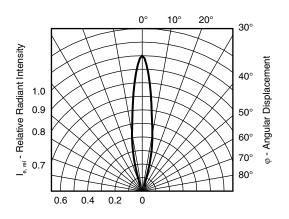
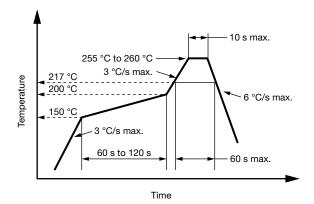


Fig. 9 - Relative Radiant Intensity vs. Angular Displacement

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SOLDER PROFILE

SHA



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Fig. 10 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

PACKAGE DIMENSIONS in millimeters: VSMY14940

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

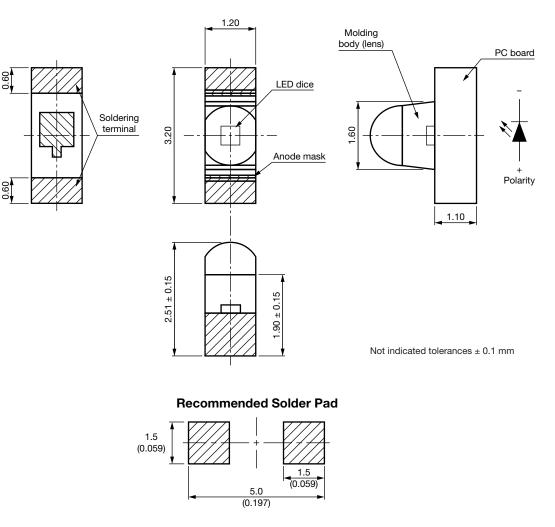
Floor life: 168 h

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 3, according to J-STD-020.

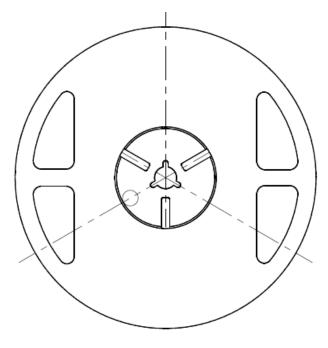
DRYING

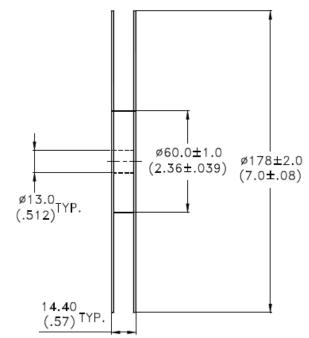
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

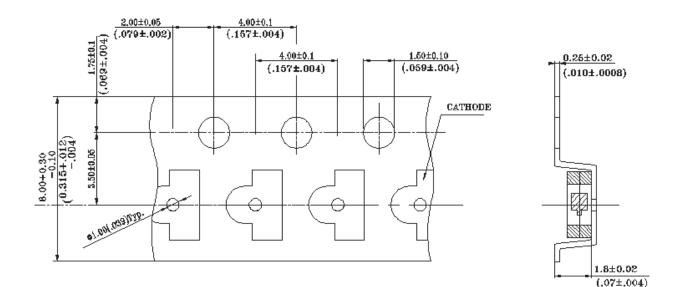




TAPING AND REEL DIMENSIONS in millimeters: VSMY14940









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