# mail

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### VSMY2853SL



**Vishay Semiconductors** 

### High Speed Infrared Emitting Diode, 850 nm, Surface Emitter Technology



### DESCRIPTION

As part of the <u>SurfLight<sup>TM</sup></u> portfolio, the reVSMY2853SL is an infrared, 850 nm, side looking emitting diode based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted plastic packages (with lens) for surface mounting (SMD).

### APPLICATIONS

- IrDA compatible data transmission
- Miniature light barrier
- Photointerrupters
- Optical switch
- Emitter source for proximity sensors
- IR touch panels
- IR illumination
- 3D TV

### FEATURES

- Package type: surface mount
- Package form: side view
- Dimensions (L x W x H in mm): 2.3 x 2.55 x 2.3
- Peak wavelength:  $\lambda_p = 850 \text{ nm}$
- High reliability
- High radiant power
- · Very high radiant intensity
- Angle of half intensity:  $\varphi = \pm 28^{\circ}$
- Suitable for high pulse current operation
- Package matches with detector VEMD2xx3SLX01 and VEMT2xx3SLX01 series
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PRODUCT SUMMARY						
COMPONENT	l <sub>e</sub> (mW/sr)	φ (deg)	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)		
VSMY2853SL	35	± 28	850	10		

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMY2853SL	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	Side view		

#### Note

• MOQ: minimum order quantity





### **VSMY2853SL**



### Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	5	V	
Forward current		١ <sub>F</sub>	100	mA	
Peak forward current	$t_p/T = 0.5, t_p = 100 \ \mu s$	I <sub>FM</sub>	200	mA	
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	1	А	
Power dissipation		Pv	190	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T <sub>amb</sub>	- 40 to + 85	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C	
Soldering temperature	acc. figure 7, J-STD-020	T <sub>sd</sub>	260	°C	
Thermal resistance junction/ambient	J-STD-051, soldered on PCB	R <sub>thJA</sub>	250	K/W	



Fig. 1 - Power Dissipation Limit vs. Ambient Temperature



Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	V <sub>F</sub>		1.65	1.9	V
	I <sub>F</sub> = 1 A, t <sub>p</sub> = 100 μs	V <sub>F</sub>		2.9		V
Temperature coefficient of V <sub>F</sub>	$I_F = 1 \text{ mA}$	TK <sub>VF</sub>		- 1.45		mV/K
	I <sub>F</sub> = 10 mA	TK <sub>VF</sub>		- 1.3		mV/K
Reverse current		I <sub>R</sub>	not designed for reverse operation		μA	
Junction capacitance	$V_{R} = 0 V, f = 1 MHz, E = 0 mW/cm^{2}$	CJ		125		pF
De dis el interneti	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	l <sub>e</sub>	20	35	50	mW/sr
Radiant Intensity	I <sub>F</sub> = 1 A, t <sub>p</sub> = 100 μs	l <sub>e</sub>		300		mW/sr
Radiant power	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	фе		55		mW
Temperature coefficient of radiant power	I <sub>F</sub> = 100 mA	ΤΚφ <sub>e</sub>		- 0.35		%/K
Angle of half intensity		φ		± 28		deg
Peak wavelength	I <sub>F</sub> = 100 mA	λp	840	850	870	nm
Spectral bandwidth	I <sub>F</sub> = 30 mA	Δλ		30		nm
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 30 mA	ΤΚλ <sub>p</sub>		0.25		nm/K
Rise time	$I_F = 100 \text{ mA}, 20 \% \text{ to } 80 \%$	t <sub>r</sub>		10		ns
Fall time	I <sub>F</sub> = 100 mA, 20 % to 80 %	t <sub>f</sub>		10		ns

For technical questions, contact: emittertechsupport@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000





**Vishay Semiconductors** 

### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)



Fig. 3 - Forward Current vs. Forward Voltage



Fig. 4 - Radiant Intensity vs. Forward Current

#### SOLDER PROFILE



Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020



Fig. 5 - Relative Radiant Power vs. Wavelength



Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label: Floor life: 4 weeks

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

Moisture sensitivity level 2a, acc. to J-STD-020.

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

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### PACKAGE DIMENSIONS in millimeters: VSMY2853SL







Not indicated tolerances ±0.2

within the tolerance of the leads

Dimensions in mm

0.9 Cathode 0.05±0.1 2.3 0.5

2.3

Anode



Drawing refers to following types: VSMY2853SL

Drawing-No.: 6.544-5410.03-4 Issue: prel. 03.08.12

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### TAPING AND REEL DIMENSIONS in millimeters: VSMY2853SL



Drawing refers to following types: see table Reel dimensions and tape Drawing-No.: 9.800-5123.01-4 Issue: prel; 01.02.13