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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







AUTOMOTIV

HALOGEN

FREE

GREEN



Vishay Semiconductors

High Power Infrared Emitting Diode, 850 nm, Surface Emitter Technology



DESCRIPTION

As part of the <u>SurfLightTM</u> portfolio, the VSMY7850X01 is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant power and high speed, molded in low thermal resistance Little Star package. A 42 mil chip provides outstanding low forward voltage and allows DC operation of the device up to 1 A.

FEATURES

- Package type: surface mount
- Package form: Little Star®
- Dimensions (L x W x H in mm): 6.0 x 7.0 x 1.5
- Peak wavelength: λ_p = 850 nm
- High reliability
- High radiant power
- · High radiant intensity
- Angle of half intensity: $\varphi = \pm 60^{\circ}$
- Low forward voltage
- Designed for high drive currents: Up to 1 A_{DC} and up to 5 A pulses
- Low thermal resistance: R_{thJP} = 10 K/W
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- · Lead (Pb)-free reflow soldering
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>



- Infrared illumination for CMOS cameras (CCTV)
- Driver assistance systems
- Machine vision IR data transmission
- 3D T\

PRODUCT SUMMARY					
COMPONENT	I _e (mW/sr)	φ (deg)	λ _p (nm)	t _r (ns)	
VSMY7850X01	170	± 60	850	15	

Note

• Test conditions see table "Basic Characteristics"

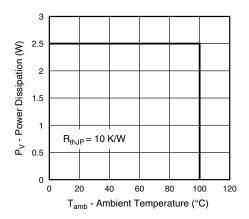
ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMY7850X01-GS08	Tape and reel	MOQ: 2000 pcs, 2000 pcs/reel	Little Star		

Note

MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	5	V	
Forward current		I _F	1	Α	
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I _{FM}	2	Α	
Surge forward current	t _p = 100 μs	I _{FSM}	5	Α	
Power dissipation		P_V	2.5	W	
Junction temperature		T _j	125	°C	
Operating temperature range		T _{amb}	- 40 to + 100	°C	
Storage temperature range		T _{stg}	- 40 to + 100	°C	
Soldering temperature	Acc. figure 7, J-STD-20	T _{sd}	260	°C	
Thermal resistance junction/pin	Acc. J-STD-051, soldered on PCB	R_{thJP}	10	K/W	







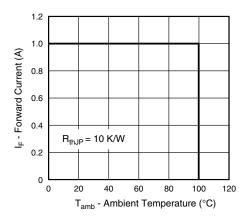


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	V _F		2.0	2.5	V
	$I_F = 5 \text{ A}, t_p = 100 \mu \text{s}$	V _F		3.5		V
Temperature coefficient of V _F	I _F = 1 A	TK _{VF}		- 0.2		mV/K
Reverse current	V _R = 5 V	I _R	not designed for reverse operation			μA
De Peril Salara S	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	l _e	130	170	390	mW/sr
Radiant intensity	$I_F = 5 \text{ A}, t_p = 100 \mu \text{s}$	I _e		780		mW/sr
Radiant power	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	φ _e		520		mW
Temperature coefficient of ϕ_{e}	I _F = 1 A	TKφ _e		- 0.5		%/K
Angle of half intensity		φ		± 60		deg
Peak wavelength	I _F = 1 A	λρ		850		nm
Spectral bandwidth	I _F = 1 A	Δλ		30		nm
Temperature coefficient of λ_p	I _F = 1 A	$TK\lambda_p$		0.2		nm/K
Rise time	I _F = 1 A	t _r		15		ns
Fall time	I _F = 1 A	t _f		18		ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

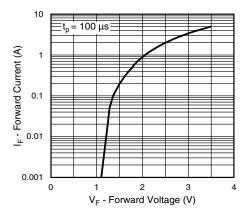


Fig. 3 - Forward Current vs. Forward Voltage

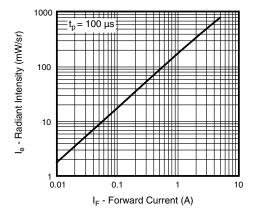


Fig. 4 - Radiant Intensity vs. Forward Current

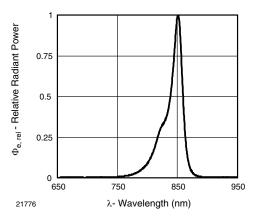


Fig. 5 - Relative Radiant Power vs. Wavelength

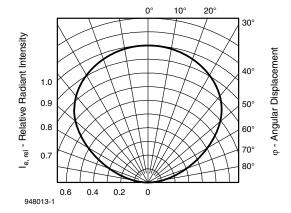
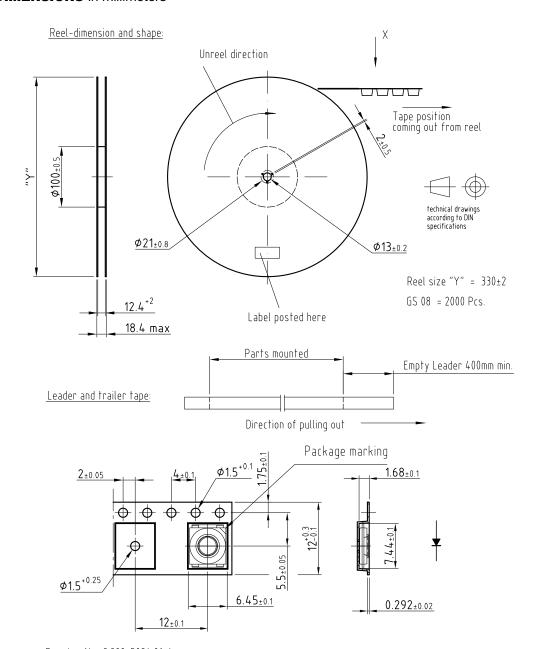


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

TAPING DIMENSIONS in millimeters

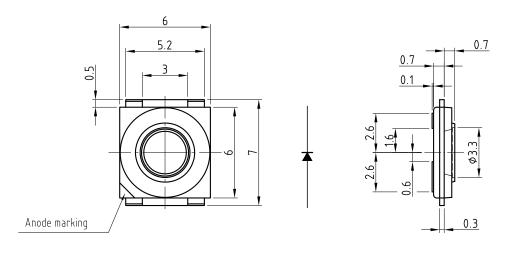


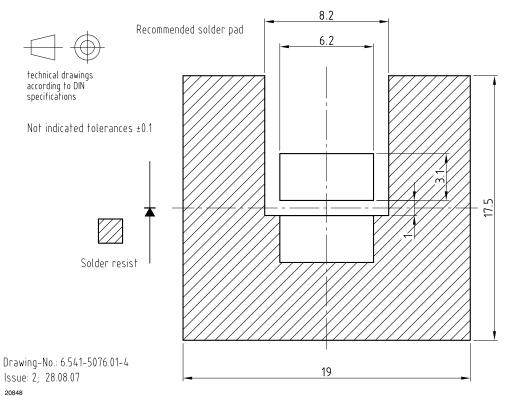
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PACKAGE DIMENSIONS in millimeters







SOLDER PROFILE

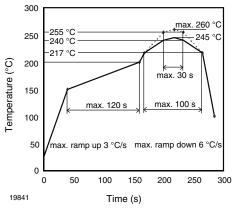


Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020 for Preconditioning acc. to JEDEC, Level 2a

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 2a, acc. to J-STD-020B

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.



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Vishay

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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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Revision: 02-Oct-12 Document Number: 91000