# imall

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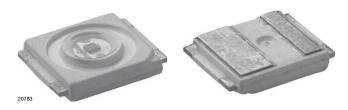


# VSMY7852X01

www.vishay.com

**Vishay Semiconductors** 

### High Power Infrared Emitting Diode, 850 nm, **Surface Emitter Technology**



### DESCRIPTION

As part of the SurfLight<sup>™</sup> portfolio, the VSMY7852X01 is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant power and high speed, molded in low thermal resistance Little Star package. A 20 mil chip provides outstanding low forward voltage and allows DC operation of the device up to 250 mA.

### APPLICATIONS

- Infrared illumination for CMOS cameras (CCTV)
- Machine vision IR data transmission

### **FEATURES**

- Package type: surface-mount
- Package form: Little Star<sup>®</sup>
- Dimensions (L x W x H in mm): 6.0 x 7.0 x 1.5
- Peak wavelength:  $\lambda_p = 850 \text{ nm}$
- High reliability
- High radiant power
- · High radiant intensity
- Angle of half intensity:  $\phi = \pm 60^{\circ}$
- · Low forward voltage
- · Designed for high drive currents: up to 250 mA DC and up to 1.5 A pulses
- Low thermal resistance: R<sub>thJP</sub> = 15 K/W
- Floor life: 1 year, MSL 2, according to J-STD-020
- · Lead (Pb)-free reflow soldering
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

PRODUCT SUMMARY					
COMPONENT	l <sub>e</sub> (mW/sr)	φ <b>(deg)</b>	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)	
VSMY7852X01	55	± 60	850	8	

#### Note

Test conditions see table "Basic Characteristics"

### ORDERING INFORMATION

ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY7852X01-GS08	Tape and reel	MOQ: 2000 pcs, 2000 pcs/reel	Little Star	

#### Note

MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	5	V	
Forward current		I <sub>F</sub>	250	mA	
Peak forward current	$t_p/T = 0.5, t_p = 100 \ \mu s$	I <sub>FM</sub>	500	mA	
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	1.5	А	
Power dissipation		Pv	500	mW	
Junction temperature		Тj	125	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +100	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C	
Soldering temperature	According to Fig. 7, J-STD-20	T <sub>sd</sub>	260	°C	
Thermal resistance junction-to-pin	According to J-STD-051, soldered on PCB	R <sub>thJP</sub>	15	K/W	

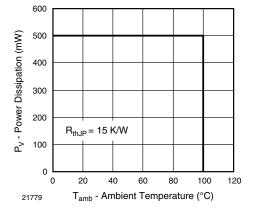
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COMPLIANT





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Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

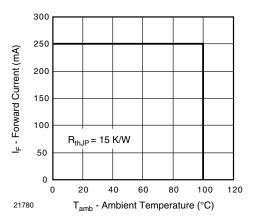


Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 250 mA, t <sub>p</sub> = 10 ms	V <sub>F</sub>	-	1.7	2.0	V
Temperature coefficient of $V_F$	I <sub>F</sub> = 1 mA	TK <sub>VF</sub>	-	-1.5	-	mV/K
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	Not designed for reverse operation			μA
Radiant intensity	I <sub>F</sub> = 250 mA, t <sub>p</sub> = 10 ms	l <sub>e</sub>	30	55	90	mW/sr
Radiant power	I <sub>F</sub> = 250 mA, t <sub>p</sub> = 20 ms	фе	-	130	-	mW
Temperature coefficient of $\phi_{e}$	I <sub>F</sub> = 1 A	ΤKφ <sub>e</sub>	-	-0.5	-	%/K
Angle of half intensity		φ	-	± 60	-	deg
Peak wavelength	I <sub>F</sub> = 250 mA	λ <sub>p</sub>	-	850	-	nm
Spectral bandwidth	I <sub>F</sub> = 250 mA	Δλ	-	30	-	nm
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 250 mA	ΤΚλ <sub>p</sub>	-	0.2	-	nm/K
Rise time	I <sub>F</sub> = 250 mA	t <sub>r</sub>	-	8	-	ns
Fall time	I <sub>F</sub> = 250 mA	t <sub>f</sub>	-	10	-	ns



### BASIC CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

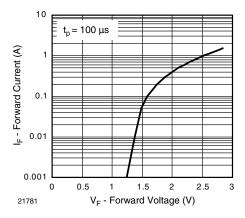


Fig. 3 - Forward Current vs. Forward Voltage

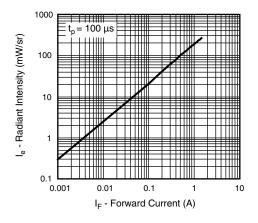


Fig. 4 - Radiant Intensity vs. Forward Current

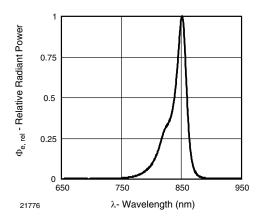


Fig. 5 - Relative Radiant Power vs. Wavelength

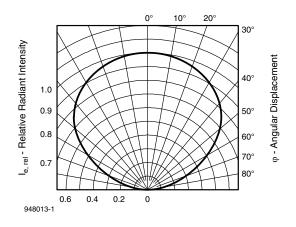
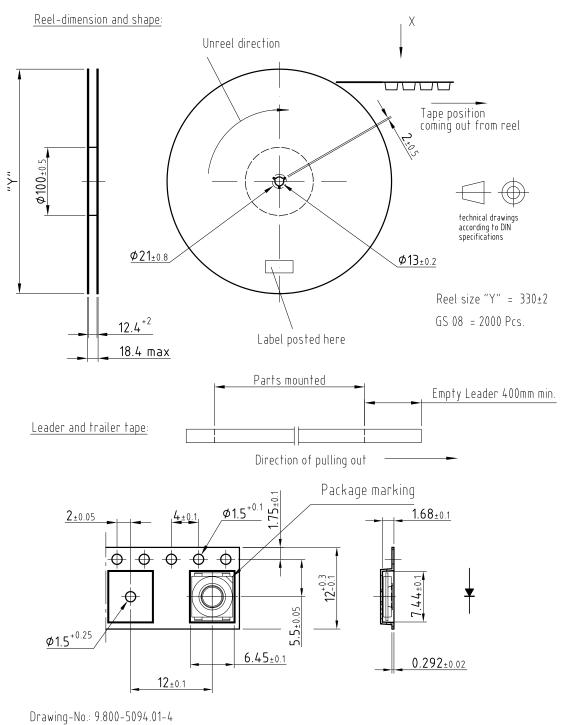


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

### **TAPING DIMENSIONS** in millimeters

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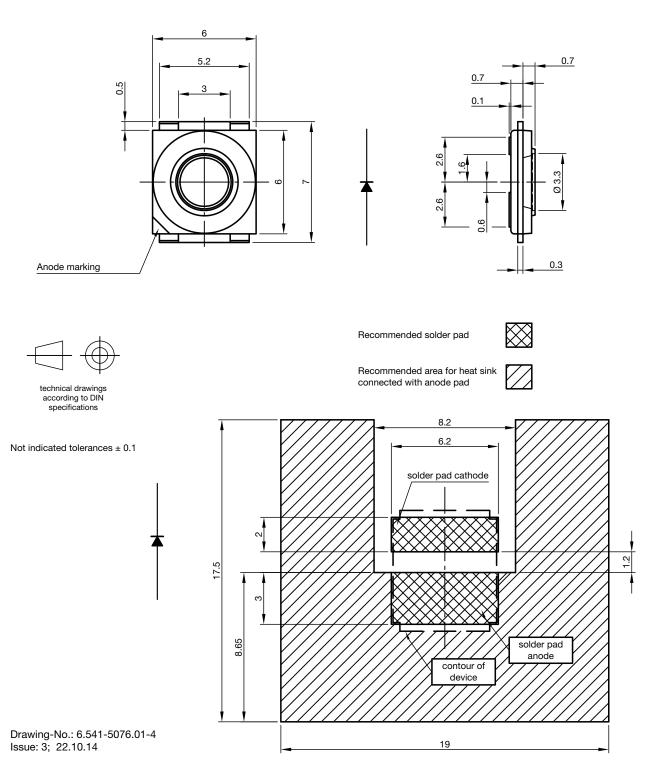


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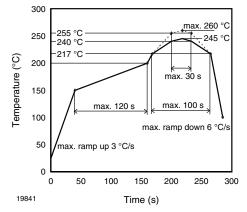


**PACKAGE DIMENSIONS** in millimeters



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### SOLDER PROFILE



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Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for Preconditioning According to JEDEC<sup>®</sup>, Level 2

### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 1 year

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

Moisture sensitivity level 2, according to J-STD-020B

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



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