

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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Standard Rectifier Module

R	3~ Rectifier		
V_{RRM}	=	1600 V	
I_{DAV}	=	150 A	
I _{FSM}	=	1800 A	

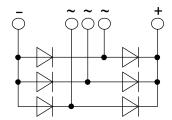
3~ Rectifier Bridge

Part number

VUO125-16NO7







Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For three phase bridge configurations Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: PWS-C

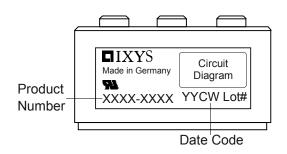
- Industry standard outline
- RoHS compliant
- Easy to mount with two screws
- Base plate: Copper internally DCB isolated
- Advanced power cycling



Rectifie	•				Ratings	3	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	king voltage	$T_{VJ} = 25^{\circ}C$			1700	V
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1600	V
I _R	reverse current	V _R = 1600 V	$T_{VJ} = 25^{\circ}C$			200	μΑ
		V _R = 1600 V	$T_{VJ} = 150^{\circ}C$			2	mΑ
V _F	forward voltage drop	I _F = 50 A	$T_{VJ} = 25^{\circ}C$			1.07	V
		I _F = 150 A				1.34	٧
		I _F = 50 A	T _{VJ} = 125 °C			0.97	V
		$I_F = 150 \text{ A}$				1.31	V
I _{DAV}	bridge output current	T _c = 110°C	T _{VJ} = 150°C			150	Α
		rectangular d = ⅓					i
V _{F0}	threshold voltage		T _{vJ} = 150°C			0.76	V
r _F	slope resistance \(\) for power	loss calculation only				3.6	mΩ
R _{thJC}	thermal resistance junction to ca	se				0.6	K/W
R _{thCH}	thermal resistance case to heats	sink			0.30		K/W
P _{tot}	total power dissipation		T _C = 25°C			205	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			1.80	kA
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.95	kA
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			1.53	kA
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.65	kA
I²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			16.2	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			15.7	kA²s
		t = 10 ms; (50 Hz), sine	T _{VJ} = 150°C			11.7	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			11.3	kA²s
CJ	junction capacitance	$V_R = 400 \text{ V}; f = 1 \text{ MHz}$	T _{VJ} = 25°C		58		pF



Package PWS-C			Ratings				
Symbol	Definition	Conditions		min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal				150	Α
T _{stg}	storage temperature			-40		125	°C
T _{VJ}	virtual junction temperature			-40		150	°C
Weight					250		g
M _D	mounting torque			4.25		5.75	Nm
M _T	terminal torque			4.25		5.75	Nm
d Spp/App	creepage distance on surface striking distance through air terminal to backside		26.0			mm	
d Spb/Apb			terminal to backside	14.0			mm
V _{ISOL}	1301ation voitage	t = 1 second	50/60 Hz RMS: IIsoi ≤ 1 mA	3000			V
.002		t = 1 minute		2500			٧

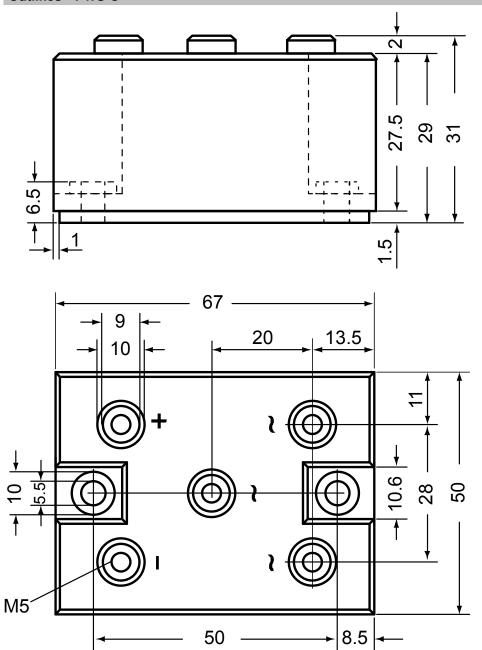


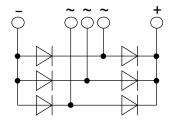
Ordering	Part Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	VUO125-16NO7	VUO125-16NO7	Box	10	456780

Equivalent Circuits for Simulation			* on die level	T _{VJ} = 150 °C
$I \rightarrow V_0$	R_0	Rectifier		
V _{0 max}	threshold voltage	0.76		V
R_{0max}	slope resistance *	2.4		$m\Omega$



Outlines PWS-C







Rectifier

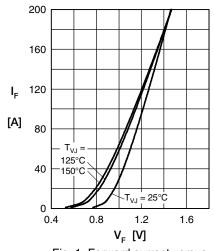


Fig. 1 Forward current versus voltage drop per diode

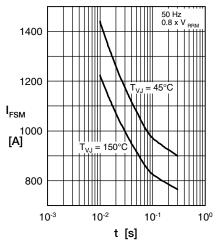


Fig. 2 Surge overload current vs. time per diode

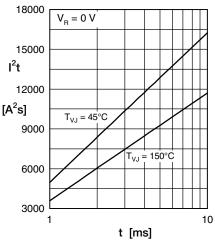


Fig. 3 I2t versus time per diode

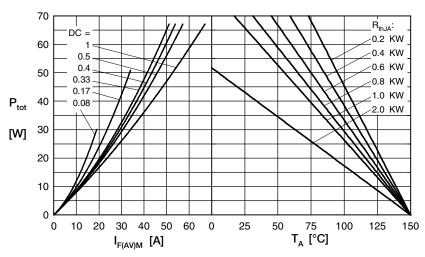


Fig. 4 Power dissipation vs. forward current and ambient temperature per diode

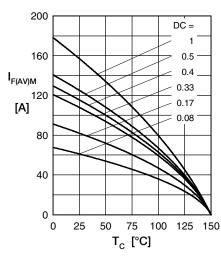


Fig. 5 Max. forward current vs. case temperature per diode

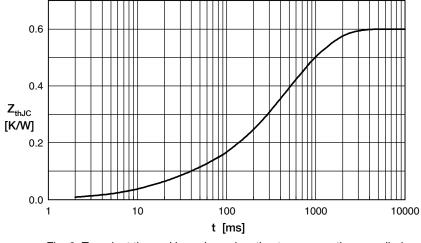


Fig. 6 Transient thermal impedance junction to case vs. time per diode

Constants for \boldsymbol{Z}_{thJC} calculation:

İ	R_{th} (K/W)	t _i (s)
1	0.060	0.020
2	0.003	0.010
3	0.150	0.225
4	0.243	0.800
5	0.144	0.580