

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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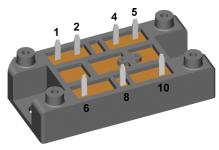
Standard Rectifier Module

3~ Rectifier			
V_{RRM}	=	1600	
I_{DAV}	=	80	
I _{FSM}	=	600	

3~ Rectifier Bridge

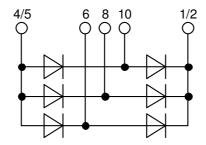
Part number

VUO80-16NO1



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: V1-A-Pack

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Height: 17 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you.

Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

- to perform joint risk and quality assessments; the conclusion of quality agreements;
- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

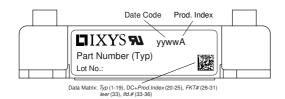
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Rectifier				1	Ratings	S	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			1700	V
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1600	V
I _R	reverse current	V _R = 1600 V	$T_{VJ} = 25^{\circ}C$			40	μΑ
		$V_R = 1600 \text{ V}$	$T_{VJ} = 150$ °C			1,5	mΑ
V _F	forward voltage drop	I _F = 30 A	$T_{VJ} = 25^{\circ}C$			1,14	V
		$I_F = 90 A$				1,48	٧
		$I_F = 30 \text{ A}$	T _{VJ} = 125 °C			1,06	V
		$I_F = 90 A$				1,51	٧
I DAV	bridge output current	T _C = 110°C	T _{VJ} = 150°C			80	Α
		rectangular d = ⅓					
V _{F0}	threshold voltage		T _{VJ} = 150°C			0,81	V
\mathbf{r}_{F}	slope resistance \(\) for power	loss calculation only				7,8	mΩ
R _{thJC}	thermal resistance junction to ca	ase				1,1	K/W
R _{thCH}	thermal resistance case to heats	sink			0,3		K/W
P _{tot}	total power dissipation		$T_{C} = 25^{\circ}C$			110	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			600	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			650	Α
		t = 10 ms; (50 Hz), sine	T _{VJ} = 150°C			510	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			550	Α
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			1,80	kA2s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1,76	kA2s
		t = 10 ms; (50 Hz), sine	T _{VJ} = 150°C			1,30	kA2s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1,26	kA2s
CJ	junction capacitance	$V_{R} = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		18		pF
-				+	-		



Package V1-A-Pack				Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal				100	Α
T _{VJ}	virtual junction temperature			-40		150	°C
T _{op}	operation temperature		-40		125	°C	
T _{stg}	storage temperature		-40		125	°C	
Weight					37		g
M _D	mounting torque			2		2,5	Nm
d _{Spp/App}	araanaga diatanaa an aurfa	creepage distance on surface striking distance through air		6,0			mm
d _{Spb/Apb}	creepage distance on suna	ce striking distance through an	terminal to backside	12,0			mm
V _{ISOL}	isolation voltage	t = 1 second	50/00 II 5140 I	3600			٧
.002		t = 1 minute	50/60 Hz, RMS; IsoL ≤ 1 mA				٧

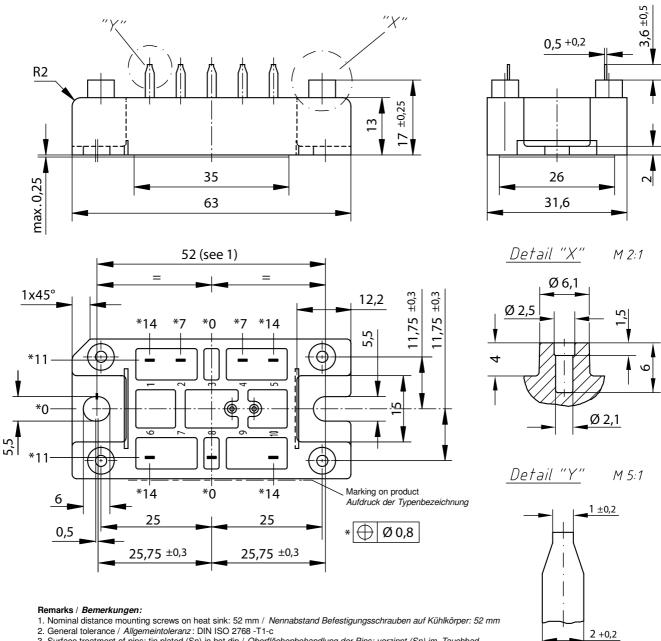


Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	VUO80-16NO1	VUO80-16NO1	Blister	24	469173

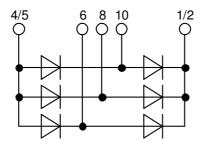
Equiva	alent Circuits for	Simulation	* on die level	$T_{VJ} = 150 ^{\circ}\text{C}$
$I \rightarrow V_0$	R_0	Rectifier		
V _{0 max}	threshold voltage	0,81		V
R _{0 max}	slope resistance *	6,6		mΩ



Outlines V1-A-Pack

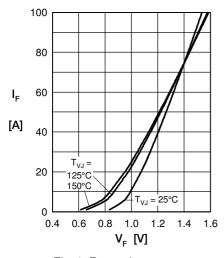


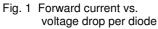
- General tolerance / Allgemeintoleranz: DIN ISO 2768 -T1-c
 Surface treatment of pins: tin plated (Sn) in hot dip / Oberflächenbehandlung der Pins: verzinnt (Sn) im Tauchbad





Rectifier





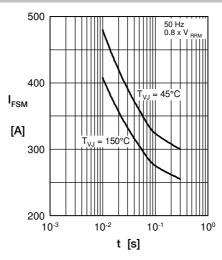


Fig. 2 Surge overload current vs. time per diode

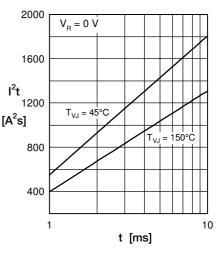


Fig. 3 I²t vs. time per diode

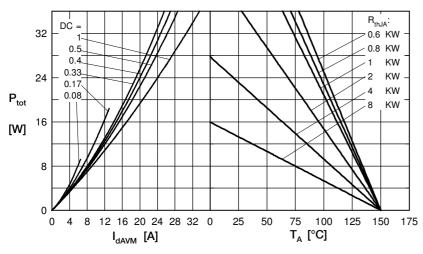


Fig. 4 Power dissipation vs. forward current and ambient temperature per diode

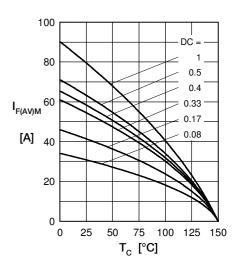


Fig. 5 Max. forward current vs. case temperature per diode

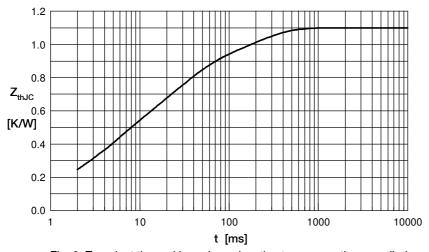


Fig. 6 Transient thermal impedance junction to case vs. time per diode

Constants for $Z_{th,IC}$ calculation:

i	R_{th} (K/W)	t _i (s)
1	0.0607	0.0004
2	0.1230	0.00256
3	0.2305	0.0045
4	0.4230	0.0242
5	0.2628	0.1800