# imall

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## SERIES: VYB15W | DESCRIPTION: DC-DC CONVERTER

#### FEATURES

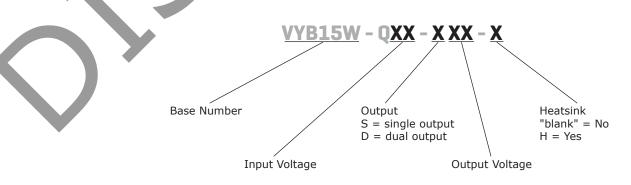
- up to 15W output
- industry standard pinout
- 4:1 input range (9 ~ 36 V, 18 ~ 75 V)
- single and dual outputs
- 1,500 V isolation
- short circuit, over current, and over voltage protections
- wide temperature operation (-40 ~ 85°C)
- efficiency up to 87%



MODEL	input	output	out	put	output	ripple <sup>1</sup>	noise <sup>1</sup>	efficiency
MODEL	voltage range (Vdc)	voltage (Vdc)	curr min (mA)		power max (W)	max (mVp-p)	max (mVp-p)	typ (%)
VYB15W-Q24-S3	9 ~ 36	3.3	400	4,000	13.2	150	150	80
VYB15W-Q24-S5	9 ~ 36	5	300	3,000	15	150	150	82
VYB15W-Q24-S12	9 ~ 36	12	125	1,250	15	150	150	85
VYB15W-Q24-S15	9 ~ 36	15	100	1,000	15	150	150	85
VYB15W-Q24-D5	9 ~ 36	±5	±150	±1,500	15	50	100	86
VYB15W-Q24-D12	9 ~ 36	±12	±63	±625	15	50	100	87
VYB15W-Q24-D15	9 ~ 36	±15	±50	±500	15	50	100	87
VYB15W-Q48-S3	18 ~ 75	3.3	400	4,000	13.2	150	150	81
VYB15W-Q48-S5	18 ~ 75	5	300	3,000	15	150	150	83
VYB15W-Q48-S12	18 ~ 75	12	125	1,250	15	150	150	85
VYB15W-Q48-S15	18 ~ 75	15	100	1,000	15	150	150	85
VYB15W-Q48-D5	18 ~ 75	±5	±150	±1,500	15	50	100	84
VYB15W-Q48-D12	18 ~ 75	±12	±63	±625	15	50	100	87
VYB15W-Q48-D15	18 ~ 75	±15	±50	±500	15	50	100	87

Notes: 1. Ripple and noise are measured at 20 MHz BW with 10µF tantalum capacitor and 1µF ceramic capacitor across output

## PART NUMBER KEY



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#### **INPUT**

parameter	conditions/descript	ion	min	typ	max	units
operating input voltage			9 18	24 48	36 75	Vdc Vdc
start-up time				10		ms
	dual output models	power up 24 V input power up 48 V input			9.0	Vdc Vdc
under voltage lockout	dual output models	power down 24 V input power down 48 V input	7.8 16.0		17.8	Vdc Vdc
Remote on/off <sup>1</sup>	all models single output models dual output models	module off module on (or open circuit) module on (or open circuit)	0 3.5 3.5		1.2 40 12	Vdc Vdc Vdc
ïlter	single output models, dual output models, P					
Notes: 1. The on/off pin voltage i	, ,					

#### OUTPUT

conditions/description	min typ	max	units
measured from low line to high line	±0.2	±0.5	%
measured from 10% to full load	±0.5	±1	%
refer to recommended circuit	±1	±3	%
25% step load charge	200	500	μs
single output models dual output models 25% rated load	±2 ±3	±5 ±5	% %
main output 55%, dual output models supplemental output from 10~100% load		±5	%
single output models	±10%		Vdc
100% load, input voltage range single output models dual output models	300 400		kHz kHz
	±0.02		%/°C
	measured from low line to high line   measured from 10% to full load   refer to recommended circuit   25% step load charge   single output models   dual output models   dual output models   ual output models   supplemental output from   10~100% load   single output models   single output models	measured from low line to high line ±0.2   measured from 10% to full load ±0.5   refer to recommended circuit ±1   25% step load charge 200   single output models ±2   dual output models ±3   main output 55%, supplemental output from   100% load, input voltage range 300   dual output models 400	measured from low line to high line±0.2±0.5measured from 10% to full load±0.5±1refer to recommended circuit±1±325% step load charge200500single output models dual output models±2±5dual output models n0~100% load±10%±5100% load, input voltage range single output models±10%100% load, input voltage range single output models300 400

#### **PROTECTIONS**

parameter	conditions/descript	conditions/description		typ	max	units
short circuit protection	hiccups, continuous, a	automatic recovery				
over current protection	single output models dual output models	input voltage range input voltage range	120 120	130 140	150 150	% %
over voltage protection	single output models dual output models	3.3 V 5 V 12 V 15 V ±5 V ±12 V ±15 V		3.9 6.2 15 18 $\pm 6.1$ $\pm 15$ $\pm 18$		Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc

### **SAFETY AND COMPLIANCE**

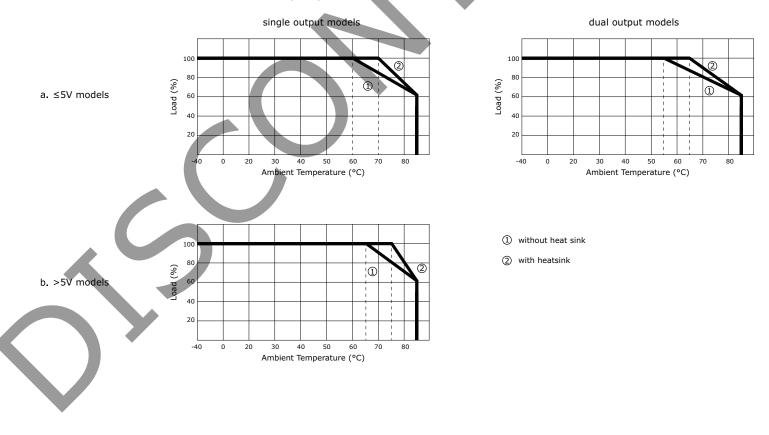
parameter	conditions/description	min	typ	max	units
isolation voltage	tested for 1 minute at 1 mA max.	1,500			Vdc
isolation resistance	at 500 Vdc	1,000			MΩ
isolation capacitance	input to output, 100 kHz / 0.1 V single output models dual output models		1,000 2,000		pF pF
RoHS compliant	yes				
MTBF	M1L-HDBK-217F	1,000,000			hours
ENVIRONMENTAL					

parameter	conditions/description	min	typ	max	units
case operating temperature		-40		85	°C
maximum case temperature	during operation			105	°C
storage temperature		-55		125	°C
storage humidity	non-condensing	5		95	%
temperature rise	100% load		40		°C
lead temperature	1.5 mm from the case for 10 seconds			300	°C

## **DERATING CURVES**

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output power vs. ambient temperature

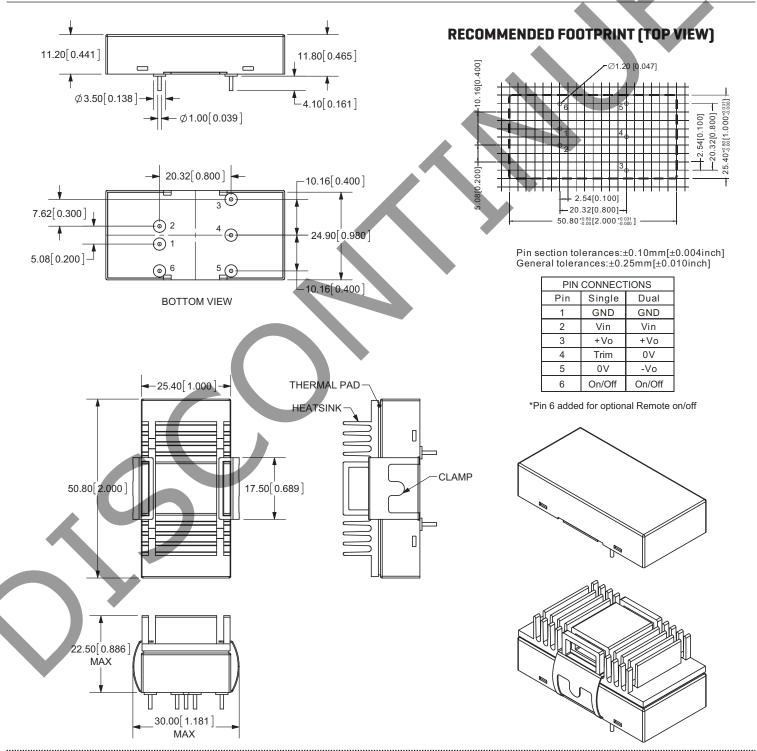


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#### **MECHANICAL**

parameter	conditions/description	min	typ	max	units
dimensions	2.00 x 1.00 x 0.44 inch (50.8 x 25.4 x 11.2mm)				
case material	nickel-coated copper (six-sided)				
weight	with heat sink		40 55		g g

#### **MECHANICAL DRAWING**



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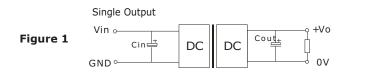
#### **APPLICATION NOTES**

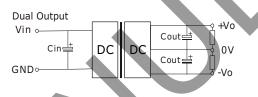
#### 1. Requirement on Output Load

In order to ensure the product operates efficiently and reliably, make sure the specified range of input voltage is not exceeded and the minimum output load is not less than 10% load. If the actual load is less than the specified minimum load, the output ripple may increase sharply while its efficiency and reliability will reduce greatly. If the actual output power is very small, please add an appropriate resistor as extra loading.

#### 2. Recommended Circuit

The VYB15W series has been tested according to the following recommended testing circuit. This series should be tested under load. (see Figure 1)





If you want to further decrease the input/output ripple, you can increase capacitance properly or choose capacitors with low ESR. If the capacitance is too big, a startup problem might arise. The maximum allowable capacitance to ensure safe and reliable operation is listed in Table 1.

	Single Vout (Vdc)	Cout (µF)	Cin (µF)	Dual Vout (Vdc)	Cout (µF)	Cin (µF)
	3.3	470	100			100
Table 1	5	470	100	±5	±220	100
	12	220	100	±12	±100	100
	15	220	100	±15	±100	100

#### 3. Trim Application and Trim Resistance (Single Output Models)

Formula for trim resistance

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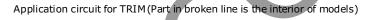
up: 
$$R = \frac{aR_2}{R_2 - a} - R_3$$
  $a = \frac{Vref}{Vo' - Vref} \cdot R_1$   
wn:  $R_T = \frac{aR_1}{R_1 - a} - R_3$   $a = \frac{Vo' - Vref}{Vref} \cdot R_2$ 

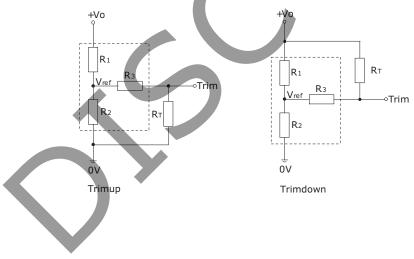
Note: Value for R1, R2, R3, and Vref refer to the following table.  $R_{T}{:}\ Trim \ resistance$ 

a: User-defined parameter, no actual meaning.

Vo': Trim up/down voltage.

Vo Resistance	3.3 (Vdc)	5 (Vdc)	12 (Vdc)	15 (Vdc)
R1 (KΩ)	4.801	2.883	10.971	14.497
R2 (KΩ)	2.863	2.864	2.864	2.864
R3 (KΩ)	15	10	17.8	17.8
Vref (V)	1.24	2.5	2.5	2.5





#### **REVISION HISTORY**

rev.	description	date	
1.0	initial release	08/08/2011	
1.01	V-Infinity branding removed	08/29/2012	

The revision history provided is for informational purposes only and is believed to be accurate



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