



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Part Number: WP63ID

High Efficiency Red

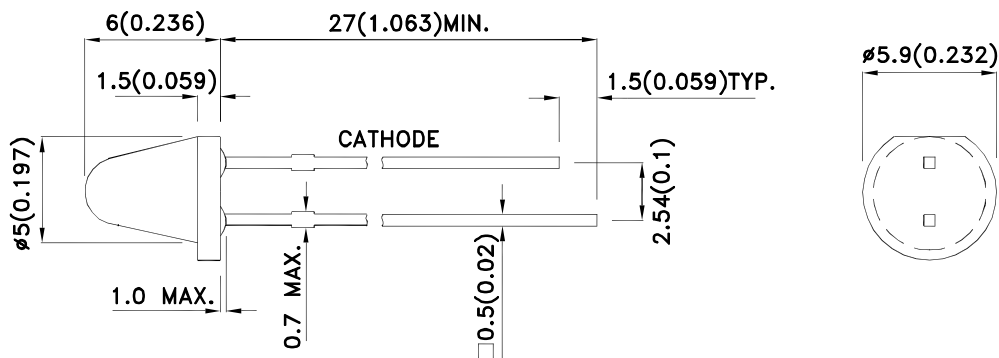
### Features

- Low profile.
- Low power consumption.
- Long life - solid state reliability.
- Reliable and rugged.
- RoHS compliant.

### Description

The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode.

### Package Dimensions



#### Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is  $\pm 0.25(0.01)$  unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.
4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.



## Selection Guide

Part No.	Dice	Lens Type	Iv (mcd) [2] @ 10mA		Viewing Angle [1]
			Min.	Typ.	2θ1/2
WP63ID	High Efficiency Red (GaAsP/GaP)	Red Diffused	15	25	60°

Notes:

1. θ1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
2. Luminous intensity/ luminous Flux: +/-15%.

## Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Typ.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	High Efficiency Red	627		nm	IF=20mA
λD [1]	Dominant Wavelength	High Efficiency Red	625		nm	IF=20mA
Δλ1/2	Spectral Line Half-width	High Efficiency Red	45		nm	IF=20mA
C	Capacitance	High Efficiency Red	15		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	High Efficiency Red	2	2.5	V	IF=20mA
IR	Reverse Current	High Efficiency Red		10	uA	VR = 5V

Notes:

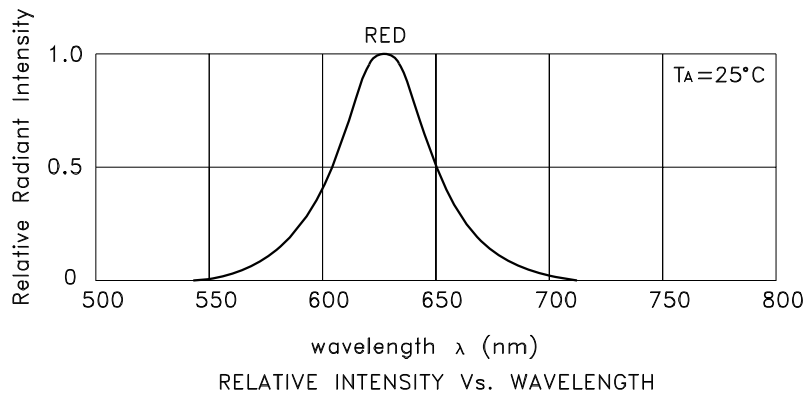
1. Wavelength: +/-1nm.
2. Forward Voltage: +/-0.1V.

## Absolute Maximum Ratings at TA=25°C

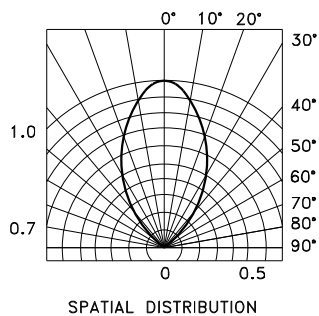
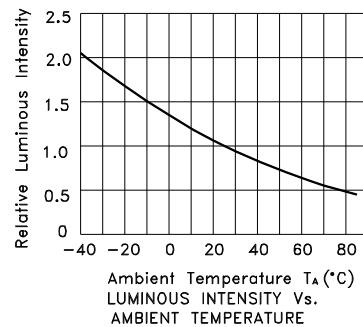
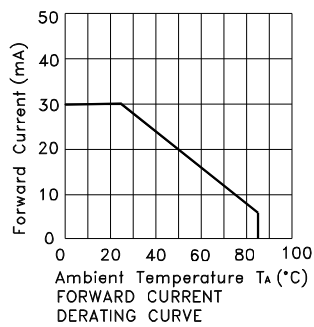
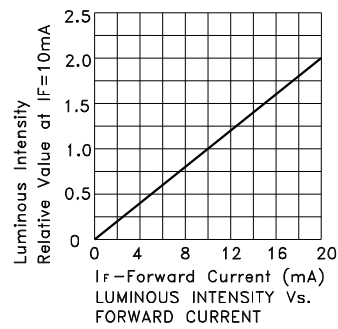
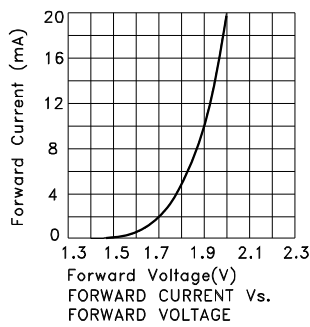
Parameter	High Efficiency Red	Units
Power dissipation	75	mW
DC Forward Current	30	mA
Peak Forward Current [1]	160	mA
Reverse Voltage	5	V
Operating/Storage Temperature	-40°C To +85°C	
Lead Solder Temperature [2]	260°C For 3 Seconds	
Lead Solder Temperature [3]	260°C For 5 Seconds	

Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. 2mm below package base.
3. 5mm below package base.

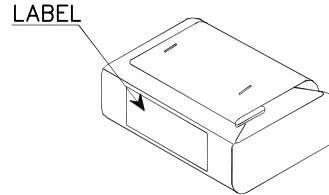
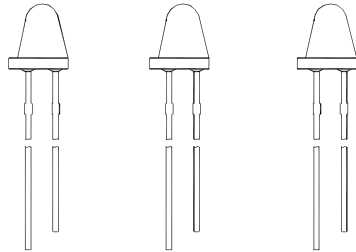


## High Efficiency Red WP63ID



**PACKING & LABEL SPECIFICATIONS**

**WP63ID**

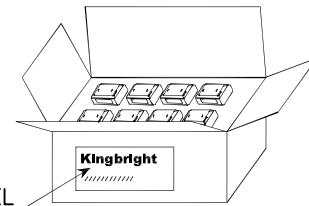


1,000PCS / BAG




OUTSIDE LABEL

40K / 9# BOX



OUTSIDE LABEL

20K / 5# BOX

<b>Kingbright</b>	
P/NO: WP63xxx	
QTY: 1000 pcs	Q.C. <span style="border: 1px solid black; border-radius: 50%; padding: 2px;">Q C XX-XX-XXXX PASSED</span>
S/N: XXXX	
CODE: XXX	
LOT NO:	
 xx	
RoHS Compliant	

## PRECAUTIONS

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)



Fig.1

”○” Correct mounting method ”×” Incorrect mounting method

2. When soldering wire to the LED, use individual heat-shrink tubing to insulate the exposed leads to prevent accidental contact short-circuit. (Fig.2)

3. Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.

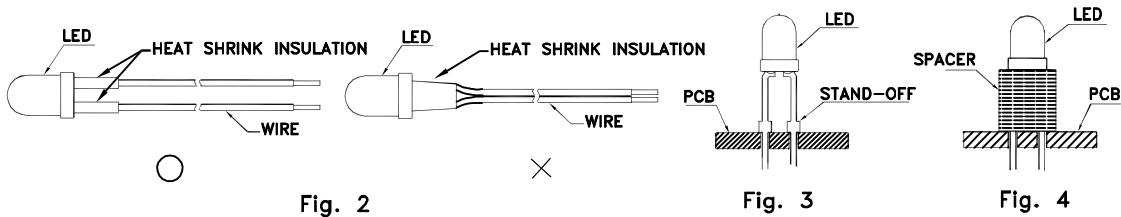


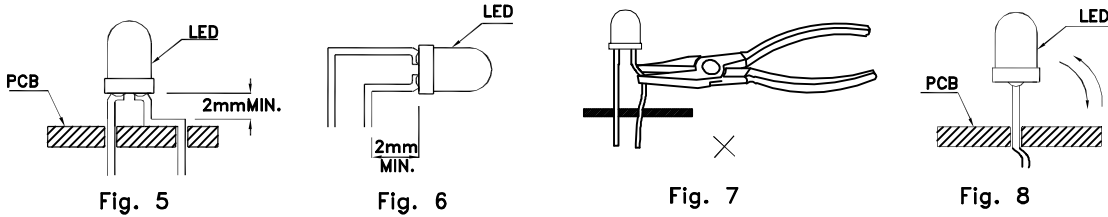
Fig. 2

Fig. 3

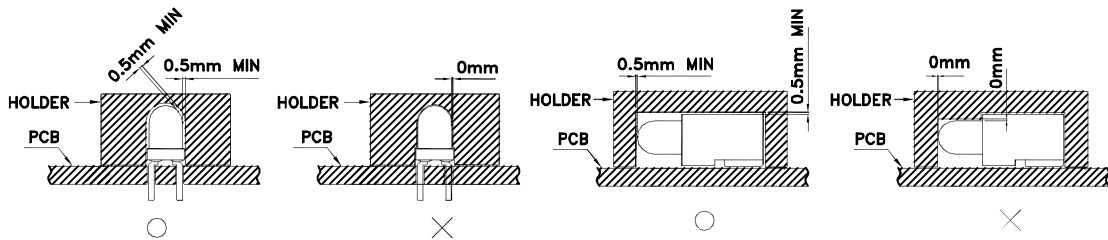
Fig. 4

4. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)
5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

6. Do not bend the leads more than twice. (Fig. 8)



7. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.

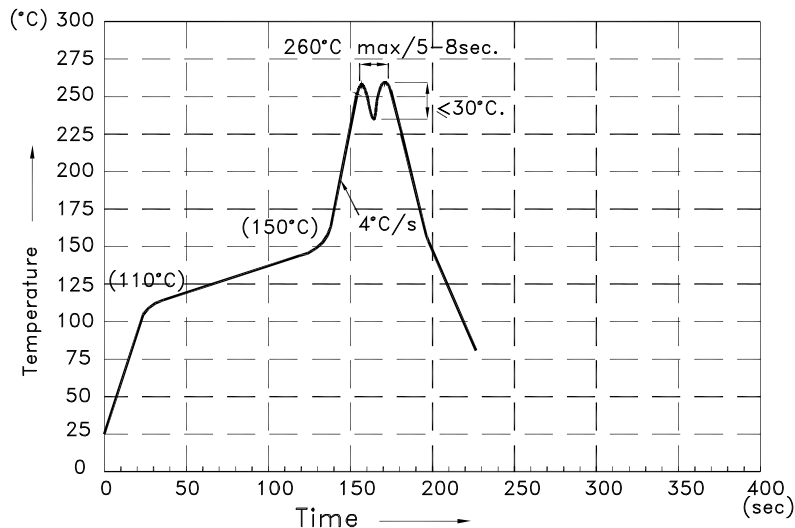


8. The tip of the soldering iron should never touch the lens epoxy.

9. Through-hole LEDs are incompatible with reflow soldering.

10. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.

11. Recommended Wave Soldering Profile for Kingbright Thru-Hole Products



**NOTES:**

1. Recommend the wave temperature 245°C~260°C. The maximum soldering temperature should be less than 260°C.
2. Do not apply stress on epoxy resins when temperature is over 85°C.
3. The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
4. During wave soldering, the PCB top-surface temperature should be kept below 105°C.
5. No more than once.