

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









## T-1 (3mm) SOLID STATE LAMP

Part Number: WP7104LYD

Yellow

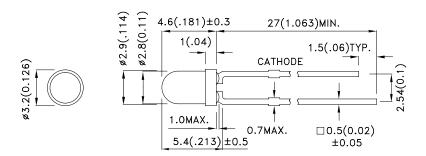
### **Features**

- LOW POWER CONSUMPTION.
- POPULAR T-1 DIAMETER PACKAGE.
- GENERAL PURPOSE LEADS.
- RELIABLE AND RUGGED.
- LONG LIFE SOLID STATE RELIABILITY.
- AVAILABLE ON TAPE AND REEL.
- LOW CURRENT IF=2mA OPERATING.
- RoHS COMPLIANT.

### Description

The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode.

## **Package Dimensions**



- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is  $\pm 0.25(0.01")$  unless otherwise noted.
- Lead spacing is measured where the leads emerge from the package.
   Specifications are subject to change without notice.





PAGE: 1 OF 6 SPEC NO: DSAF2311 **REV NO: V.3 DATE: MAY/18/2007 APPROVED: WYNEC CHECKED: Allen Liu** DRAWN: Y.L.LI ERP: 1101008235

### **Selection Guide**

Part No.	Dice Lens Type		lv (mcd) [2] @ 2mA		Viewing Angle [1]
		, , , , , , , , , , , , , , , , , , ,	Min.	Тур.	201/2
WP7104LYD	Yellow (GaAsP/GaP)	YELLOW DIFFUSED	0.7	1.5	40°

- 1.  $\theta$ 1/2 is the angle from optical centerline where the luminous intensity is 1/2 the optical centerline value. 2. Luminous intensity/ luminous Flux: +/-15%.

## Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Yellow	590		nm	IF=2mA
λD [1]	Dominant Wavelength	Yellow	588		nm	IF=2mA
Δλ1/2	Spectral Line Half-width	Yellow	35		nm	IF=2mA
С	Capacitance	Yellow	20		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	Yellow	1.85	2.5	V	IF=2mA
lR	Reverse Current	Yellow		10	uA	VR = 5V

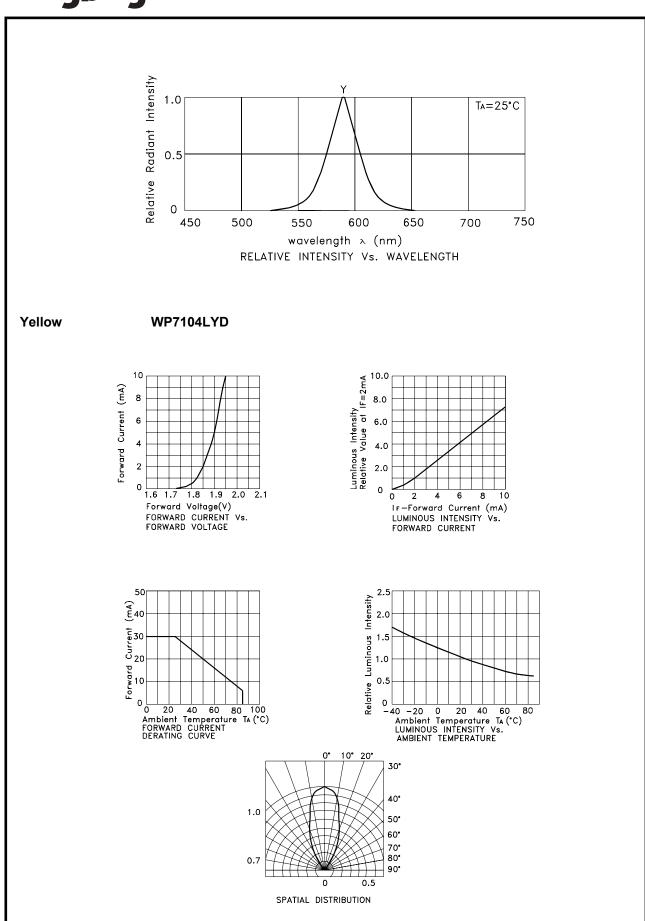
- 1.Wavelength: +/-1nm.
- 2. Forward Voltage: +/-0.1V.

## Absolute Maximum Ratings at TA=25°C

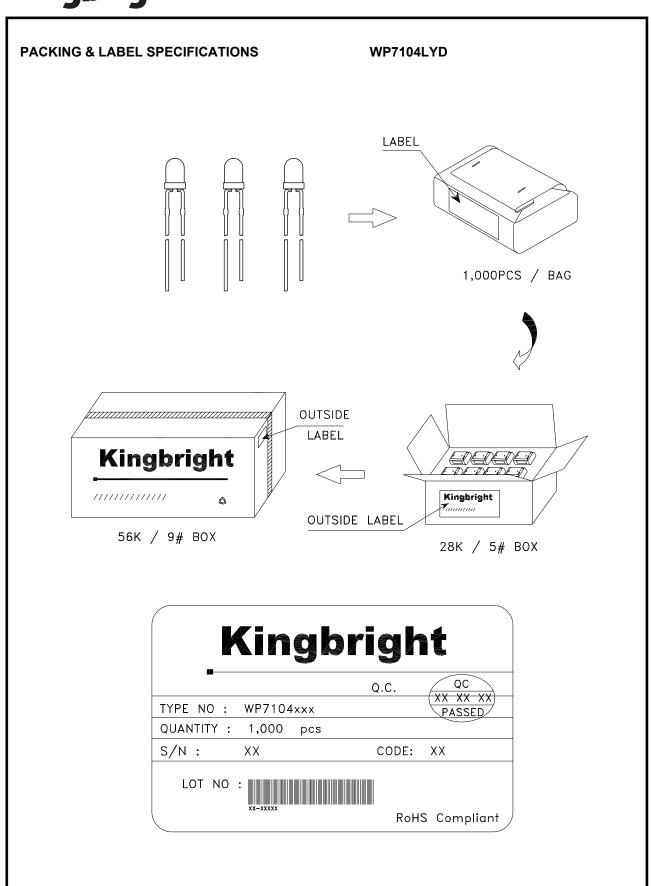
Parameter	Yellow	Units	
Power dissipation	75	mW	
DC Forward Current	30	mA	
Peak Forward Current [1]	140	mA	
Reverse Voltage	5	V	
Operating/Storage Temperature	-40°C To +85°C		
Lead Solder Temperature [2]	260°C For 3 Seconds		
Lead Solder Temperature [3]	260°C For 5 Seconds		

- 1. 1/10 Duty Cycle, 0.1ms Pulse Width.
  2. 2mm below package base.
  3. 5mm below package base.

SPEC NO: DSAF2311 **REV NO: V.3** DATE: MAY/18/2007 PAGE: 2 OF 6 APPROVED: WYNEC **CHECKED: Allen Liu** DRAWN: Y.L.LI ERP: 1101008235



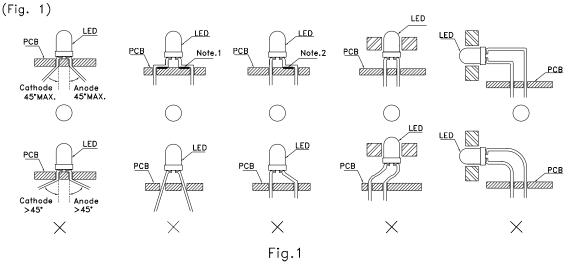
SPEC NO: DSAF2311 REV NO: V.3 DATE: MAY/18/2007 PAGE: 3 OF 6
APPROVED: WYNEC CHECKED: Allen Liu DRAWN: Y.L.LI ERP: 1101008235



SPEC NO: DSAF2311 APPROVED: WYNEC REV NO: V.3 CHECKED: Allen Liu DATE: MAY/18/2007 DRAWN: Y.L.LI PAGE: 4 OF 6 ERP: 1101008235

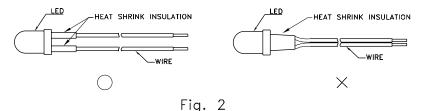
### LED MOUNTING METHOD

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead—forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.

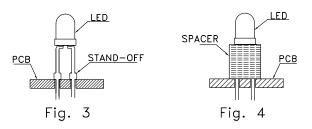


" $\bigcirc$ " Correct mounting method " $\times$ " Incorrect mounting method Note 1-2: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

2. When soldering wire to the LED, use individual heat—shrink tubing to insulate the exposed leads to prevent accidental contact short—circuit. (Fig. 2)



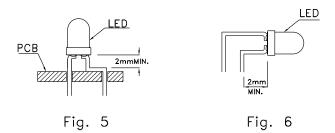
3. Use stand—offs (Fig. 3) or spacers (Fig. 4) to securely position the LED above the PCB.



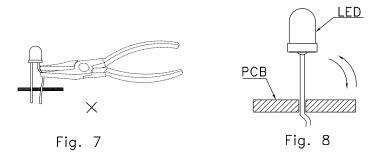
SPEC NO: DSAF2311 APPROVED: WYNEC REV NO: V.3 CHECKED: Allen Liu DATE: MAY/18/2007 DRAWN: Y.L.LI PAGE: 5 OF 6 ERP: 1101008235

### LEAD FORMING PROCEDURES

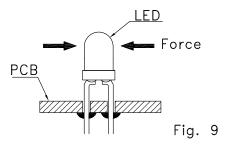
1. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)



- 2. Lead forming or bending must be performed before soldering, never during or after Soldering.
- 3. Do not stress the LED lens during lead—forming in order to fractures in the lens epoxy and damage the internal structures.
- 4. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)
- 5. Do not bend the leads more than twice. (Fig. 8)



6. After soldering or other high—temperature assembly, allow the LED to cool down to 50°C before applying outside force (Fig. 9). In general, avoid placing excess force on the LED to avoid damage. For any questions please consult with Kingbright representative for proper handling procedures.



SPEC NO: DSAF2311 REV NO: V.3 DATE: M.
APPROVED: WYNEC CHECKED: Allen Liu DRAWN:

DATE: MAY/18/2007 PAGE: 6 OF 6
DRAWN: Y.L.LI ERP: 1101008235