

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









T-1 (3mm) SOLID STATE LAMP

Part Number: WP710A10WG1C

Green



ATTENTION OBSERVE PRECAUTIONS FOR HANDLING ELECTROSTATIC DISCHARGE SENSITIVE **DEVICES**

Features

- Low power consumption.
- Popular T-1 diameter package.
- General purpose leads.
- Reliable and rugged.
- Long life solid state reliability.
- Available on tape and reel.
- RoHS compliant.

Description

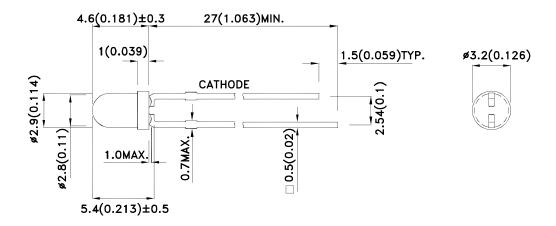
The Green source color devices are made with InGaN Light Emitting Diode.

Static electricity and surge damage the LEDS.

It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs.

All devices, equipment and machinery must be electrically grounded.

Package Dimensions



- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is $\pm 0.25(0.01")$ unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.
 4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SPEC NO: DSAL4967 **REV NO: V.1 DATE: DEC/22/2010** PAGE: 1 OF 6 APPROVED: WYNEC **CHECKED: Allen Liu** DRAWN: Y.H.Wu ERP: 1101030219

Selection Guide

Part No.	Dice	lv (mcd) [2] Dice Lens Type @ 20mA		,	Viewing Angle [1]
			Min.	Тур.	201/2
WP710A10WG1C	Green (InGaN)	Water Clear	4000	5800	34°

- 1. 01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
 2. Luminous intensity/ luminous Flux: +/-15%.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Green	500		nm	IF=20mA
λD [1]	Dominant Wavelength	Green	505		nm	IF=20mA
Δλ1/2	Spectral Line Half-width	Green	28		nm	IF=20mA
С	Capacitance	Green	45		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	Green	3.2	3.8	V	IF=20mA
lR	Reverse Current	Green		10	uA	VR = 5V

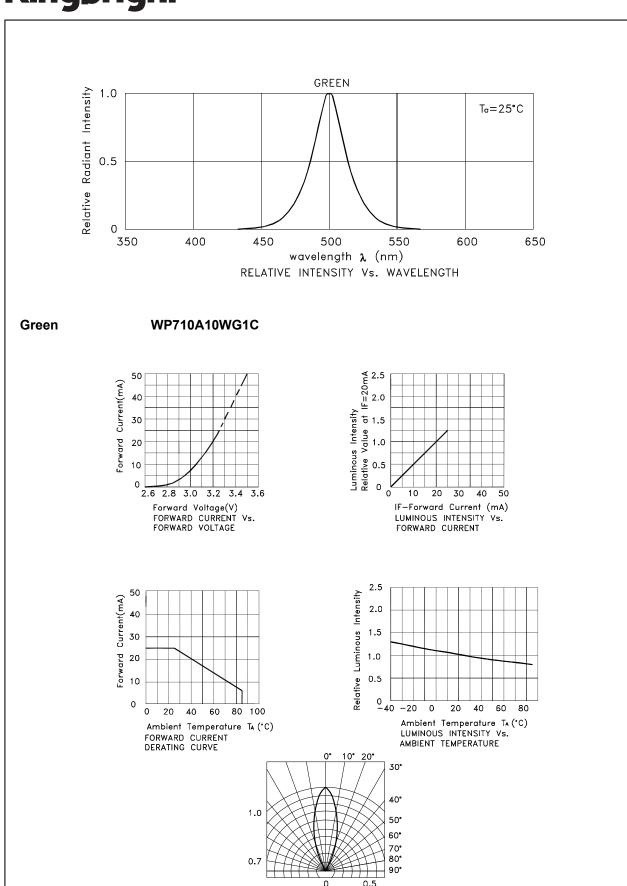
- Notes: 1.Wavelength: +/-1nm. 2. Forward Voltage: +/-0.1V.

Absolute Maximum Ratings at TA=25°C

Parameter	Green	Units	
Power dissipation	95	mW	
DC Forward Current	25	mA	
Peak Forward Current [1]	150	mA	
Reverse Voltage	5	V	
Operating/Storage Temperature	-40°C To +85°C		
Lead Solder Temperature [2]	260°C For 3 Seconds		
Lead Solder Temperature [3]	260°C For 5 Seconds		

- 1. 1/10 Duty Cycle, 0.1ms Pulse Width.
 2. 2mm below package base.
 3. 5mm below package base.

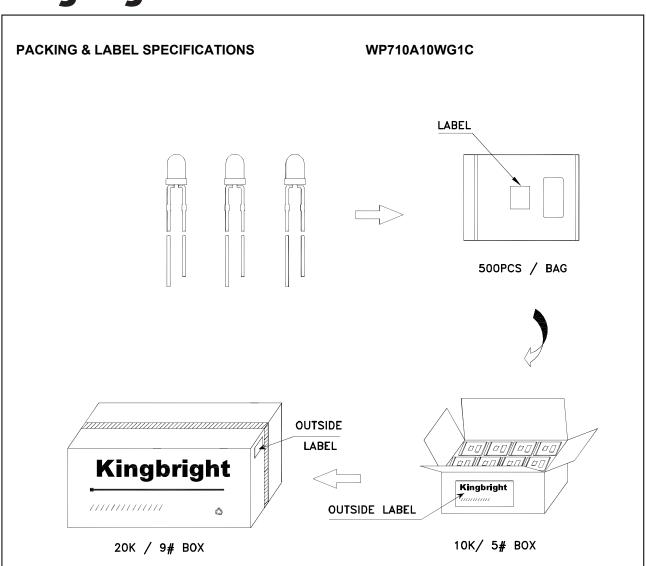
SPEC NO: DSAL4967 **REV NO: V.1 DATE: DEC/22/2010** PAGE: 2 OF 6 APPROVED: WYNEC **CHECKED: Allen Liu** DRAWN: Y.H.Wu ERP: 1101030219



 SPEC NO: DSAL4967
 REV NO: V.1
 DATE: DEC/22/2010
 PAGE: 3 OF 6

 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Y.H.Wu
 ERP: 1101030219

SPATIAL DISTRIBUTION

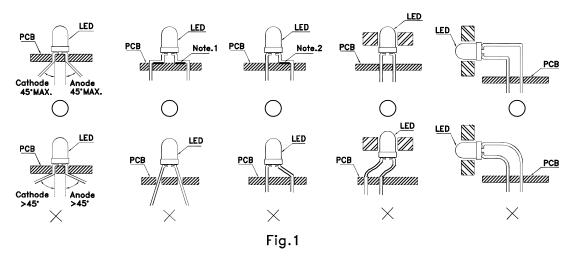




SPEC NO: DSAL4967 APPROVED: WYNEC REV NO: V.1 CHECKED: Allen Liu DATE: DEC/22/2010 DRAWN: Y.H.Wu PAGE: 4 OF 6 ERP: 1101030219

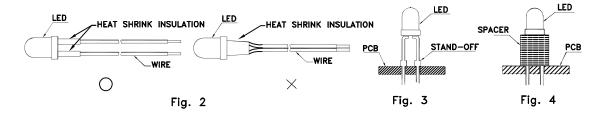
PRECAUTIONS

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead—forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)



 $"\bigcirc$ " Correct mounting method "imes" Incorrect mounting method

- 2. When soldering wire to the LED, use individual heat—shrink tubing to insulate the exposed leads to prevent accidental contact short—circuit. (Fig.2)
- 3.Use stand—offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.



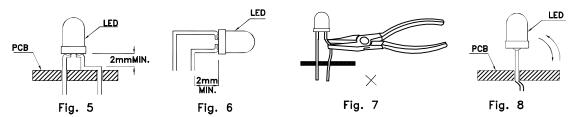
- 4. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)
- 5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

SPEC NO: DSAL4967 REV NO: V.1

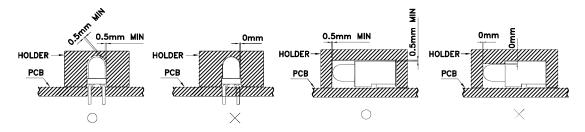
APPROVED: WYNEC CHECKED: Allen Liu

DATE: DEC/22/2010 DRAWN: Y.H.Wu PAGE: 5 OF 6 ERP: 1101030219

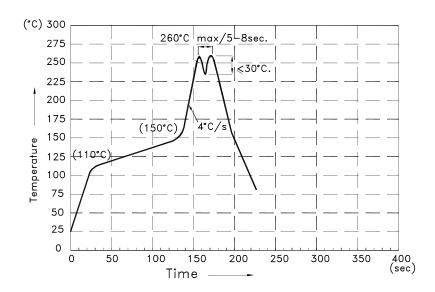
6. Do not bend the leads more than twice. (Fig. 8)



7. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.



- 8. The tip of the soldering iron should never touch the lens epoxy.
- 9. Through—hole LEDs are incompatible with reflow soldering.
- 10. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.
- 11. Recommended Wave Soldering Profile for Kingbright Thru-Hole Products



NOTES:

- 1.Recommend the wave temperature 245°C \sim 260°C.The maximum soldering temperature should be less than 260°C.
- $2.\mbox{Do}$ not apply stress on epoxy resins when temperature is over $85\mbox{^{\circ}C}.$
- 3. The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
- 4.During wave soldering, the PCB top-surface temperature should be kept below 105°C.
- 5.No more than once.

 SPEC NO: DSAL4967
 REV NO: V.1
 DATE: DEC/22/2010
 PAGE: 6 OF 6

 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Y.H.Wu
 ERP: 1101030219