



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Part Number: WP7113F3C

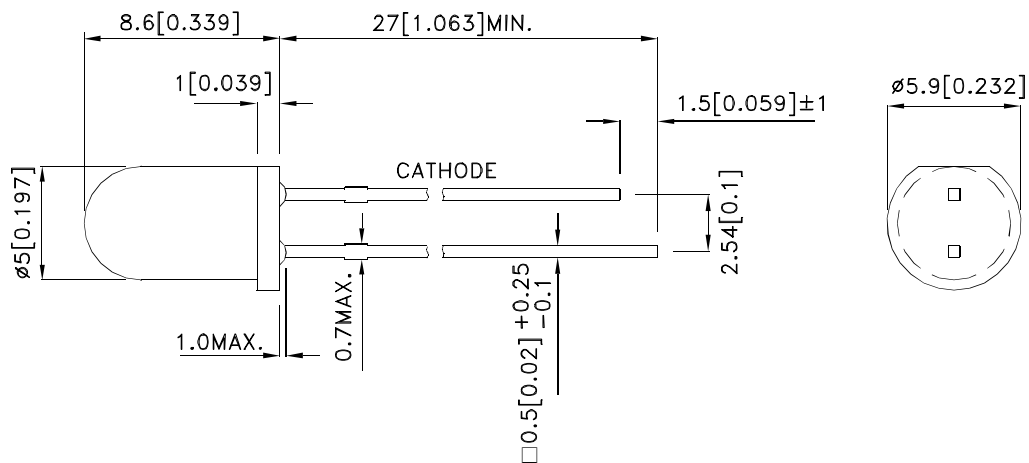
### Features

- Mechanically and spectrally matched to the phototransistor.
- RoHS compliant.

### Description

F3 Made with Gallium Arsenide Infrared Emitting diodes.

### Package Dimensions



#### Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is  $\pm 0.25(0.01)$  unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.
4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.



## Selection Guide

Part No.	Dice	Lens Type	Po (mW/sr) [2] @ 20mA		Po (mW/sr) [2] @ 50mA		Viewing Angle [1]
			Min.	Typ.	Min.	Typ.	θ1/2
WP7113F3C	F3 (GaAs)	Water Clear	15	30	55	100	20°
			*8	*20	*25	*50	

Notes:

1. θ1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
  2. Radiant Intensity/ luminous flux: +/-15%.
- \*Radiant Intensity value is traceable to the CIE127-2007 compliant national standards.

## Electrical / Optical Characteristics at TA=25°C

Parameter	P/N	Symbol	Typ.	Max.	Units	Test Conditions
Forward Voltage [1]	F3	V <sub>F</sub>	1.2	1.6	V	I <sub>F</sub> =20mA
Reverse Current	F3	I <sub>R</sub>		10	uA	V <sub>R</sub> = 5V
Capacitance	F3	C	90		pF	V <sub>F</sub> =0V;f=1MHz
Peak Spectral Wavelength	F3	λ <sub>P</sub>	940		nm	I <sub>F</sub> =20mA
Spectral Bandwidth	F3	Δλ1/2	50		nm	I <sub>F</sub> =20mA

Notes:

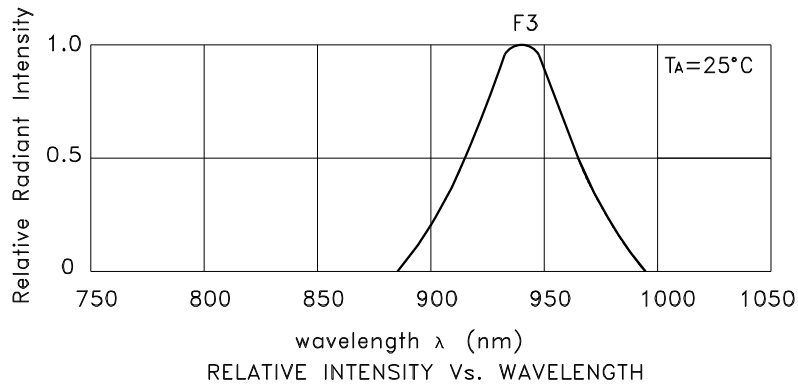
1. Forward Voltage: +/-0.1V.
2. Wavelength value is traceable to the CIE127-2007 compliant national standards.

## Absolute Maximum Ratings at TA=25°C

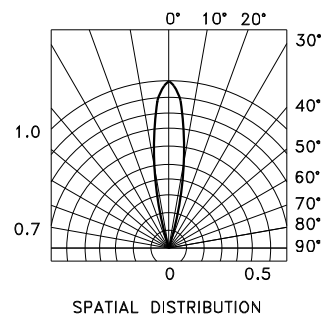
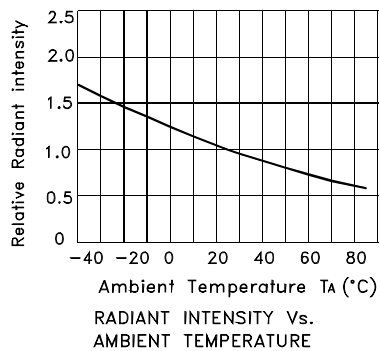
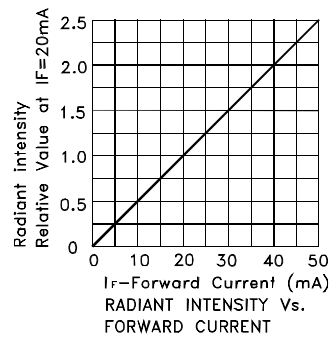
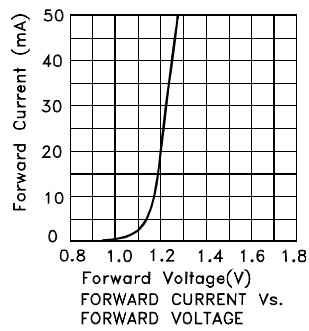
Parameter	Symbol	F3	Units
Power dissipation	P <sub>D</sub>	80	mW
DC Forward Current	I <sub>F</sub>	50	mA
Peak Forward Current [1]	i <sub>FS</sub>	1.2	A
Reverse Voltage	V <sub>R</sub>	5	V
Operating Temperature	T <sub>A</sub>	-40 To +85	°C
Storage Temperature	T <sub>STG</sub>	-40 To +85	°C
Lead Solder Temperature [2]		260°C For 3 Seconds	
Lead Solder Temperature [3]		260°C For 5 Seconds	

Notes:

1. 1/100 Duty Cycle, 10μs Pulse Width.
2. 2mm below package base.
3. 5mm below package base.



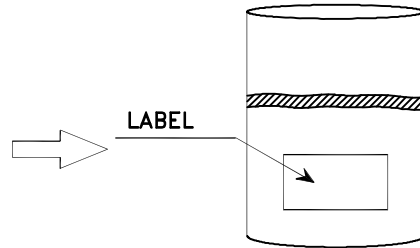
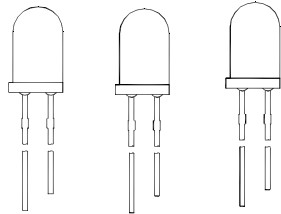
## WP7113F3C



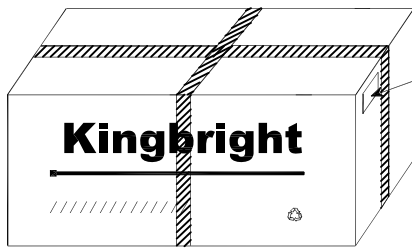
# Kingbright

## PACKING & LABEL SPECIFICATIONS

WP7113F3C

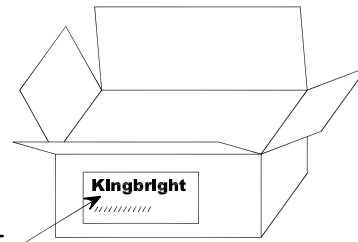
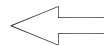


1000PCS / BAG




36K / 9# BOX

OUTSIDE LABEL



OUTSIDE LABEL

18K / 5# BOX

<h1>Kingbright</h1>					
P/NO: WP7113xxx					
QTY: 1000 pcs	Q.C.	<table border="1"> <tr> <td>Q C</td> </tr> <tr> <td>xx xx xxxx</td> </tr> <tr> <td>PASSED</td> </tr> </table>	Q C	xx xx xxxx	PASSED
Q C					
xx xx xxxx					
PASSED					
S/N: XXXX					
CODE: XXX					
LOT NO:					
 xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx					
RoHS Compliant					

## PRECAUTIONS

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)

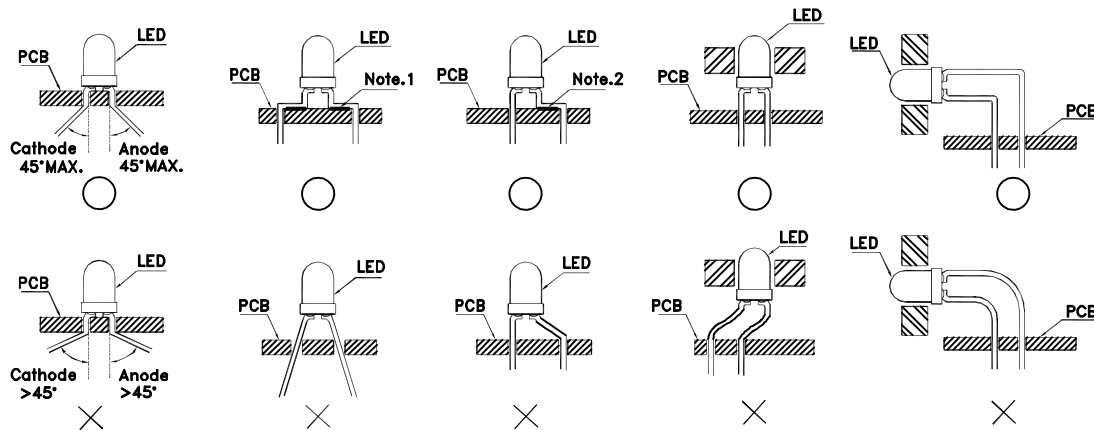


Fig.1

”○” Correct mounting method ”×” Incorrect mounting method

2. When soldering wire to the LED, use individual heat-shrink tubing to insulate the exposed leads to prevent accidental contact short-circuit. (Fig.2)
3. Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.

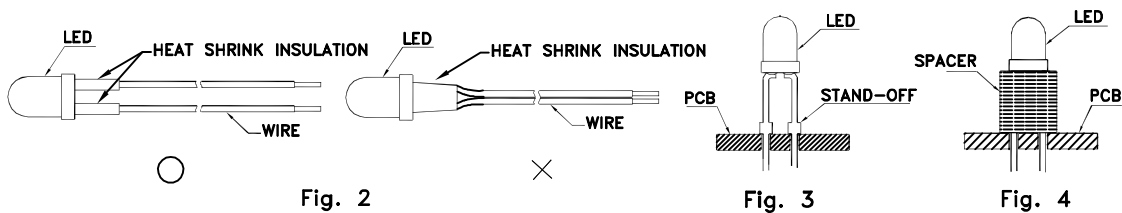


Fig. 2

Fig. 3

Fig. 4

4. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)
5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

6. Do not bend the leads more than twice. (Fig. 8)

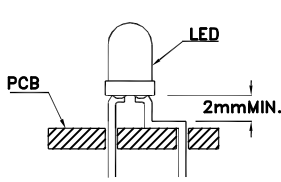


Fig. 5

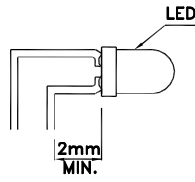


Fig. 6

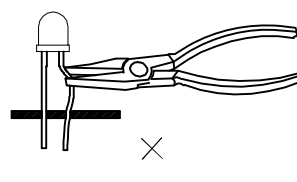


Fig. 7

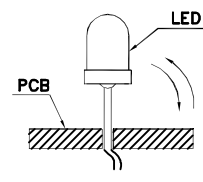
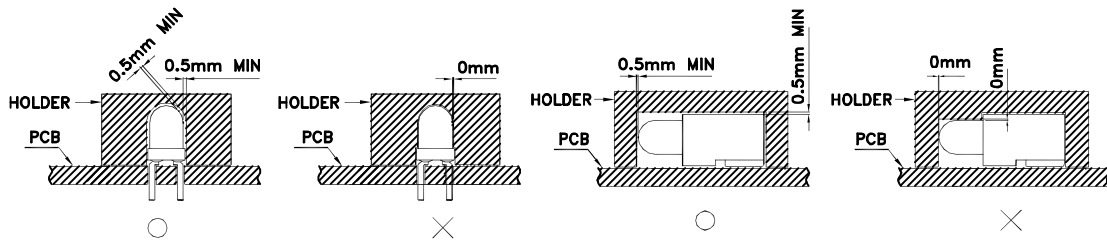


Fig. 8

7. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.

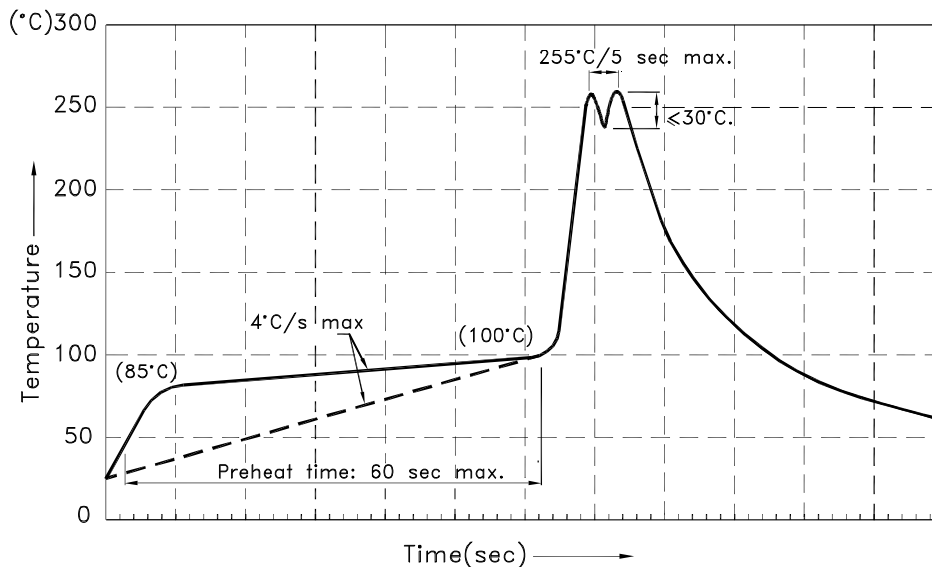


8. The tip of the soldering iron should never touch the lens epoxy.

9. Through-hole LEDs are incompatible with reflow soldering.

10. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.

11. Recommended Wave Soldering Profiles:



Notes:

1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
4. Fixtures should not incur stress on the component when mounting and during soldering process.
5. SAC 305 solder alloy is recommended.
6. No more than one wave soldering pass.

All design applications should refer to Kingbright application notes available at <http://www.KingbrightUSA.com/ApplicationNotes>