imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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WP7113VRCBC/A

T-1 3/4 (5mm) Solid State Lamp



DESCRIPTIONS

- The source color devices are made with InGaN Light **Emitting Diode**
- · Electrostatic discharge and power surge could damage the LEDs
- · It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs
- · All devices, equipments and machineries must be electrically grounded

FEATURES

- Low power consumption
- Popular T-1 3/4 diameter package
- · General purpose leads
- Reliable and rugged
- · Long life solid state reliability
- · Available on tape and reel
- RoHS compliant

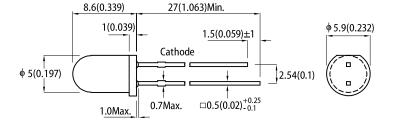
APPLICATIONS

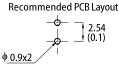
- Status indicator
- Illuminator
- Signage applications
- · Decorative and entertainment lighting
- · Commercial and residential architectural lighting

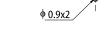
ATTENTION

Observe precautions for handling electrostatic discharge sensitive devices









Notes 1 All dimensions are in millimeters (inches)

PACKAGE DIMENSIONS

All differisions are in minimiters (incres).
 Tolerance is ±0.25(0.01") unless otherwise noted.
 Lead spacing is measured where the leads emerge from the package.
 The specifications, characteristics and technical data described in the datasheet are subject to change

without prior notice

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	lv (mcd) @ 20mA ^[2]		Viewing Angle ^[1]
Fait Number			Min.	Тур.	201/2
WP7113VRCBC/A	Cyan (InGaN)	Water Clear	3800	7900	20°

Notes

- 41/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
 2. Luminous intensity / luminous flux: +/-15%.
 3. Luminous intensity value is traceable to CIE127-2007 standards.

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Emitting Color	Value		Unit	
Parameter	Symbol	Emitting Color	Тур.	Max.	Unit	
Chromaticity Coordinates x I _F = 20mA	x ^[1]	Cyan	0.19	-	-	
Chromaticity Coordinates y I _F = 20mA	y ^[1]	Cyan	0.37	-	-	
Capacitance	С	Cyan	100	-	pF	
Forward Voltage $I_F = 20 \text{mA}$	V _F ^[2]	Cyan	3.3	4.0	V	
Reverse Current (V _R = 5V)	I _R	Cyan	-	50	uA	

Notes:

Measurement tolerance of the chromaticity coordinates is ± 0.02.
 Forward voltage: ±0.1V.
 Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

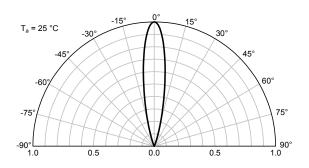
Parameter	Symbol	Value	Unit	
Power Dissipation	P _D	120	mW	
Reverse Voltage	V _R	5	V	
Junction Temperature	Tj	100	°C	
Operating Temperature	T _{op}	-40 to +85	°C	
Storage Temperature	T _{stg}	-40 to +85	°C	
DC Forward Current	I _F	30	mA	
Peak Forward Current	I _{FM} ^[1]	100	mA	
Electrostatic Discharge Threshold (HBM)	-	250	V	
Lead Solder Temperature ^[2]		260°C For 3 Seconds		
Lead Solder Temperature [3]		260°C For 5 Seconds		

Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. 2mm below package base. 3. 5mm below package base. 4. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

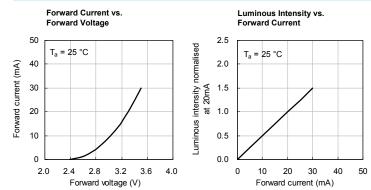
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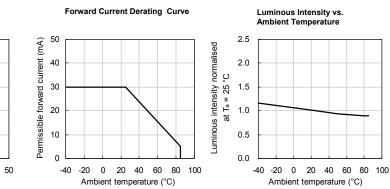
TECHNICAL DATA

SPATIAL DISTRIBUTION

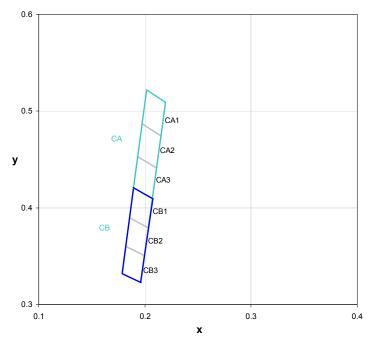


CYAN





CIE CHROMATICITY DIAGRAM



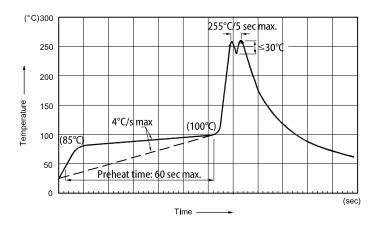
	Х	У		х	у
	0.2016 0.5221		0.1894	0.4206	
CA1	0.1973	0.4868	CB1	0.1856	0.3897
CAT	0.2152	0.4744		0.2032	0.3794
	0.2195	0.5089		0.2070	0.4097
	0.1973	0.4868	CB2	0.1856	0.3897
CA2	0.1933	0.4530		0.1821	0.3601
	0.2110	0.4413		0.1996	0.3505
	0.2152	0.4744		0.2032	0.3794
	0.1933	0.4530		0.1821	0.3601
CA3	0.1894	0.4206	CB3	0.1786	0.3318
	0.2070	0.4097		0.1961	0.3228
	0.2110	0.4413		0.1996	0.3505

Notes:

Shipment may contain more than one chromaticity regions. Orders for single chromaticity region are generally not accepted. Measurement tolerance of the chromaticity coordinates is ±0.02.

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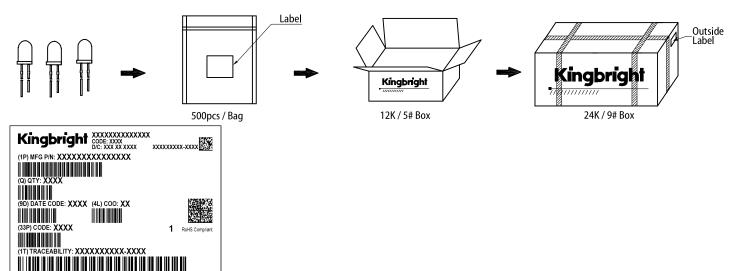
RECOMMENDED WAVE SOLDERING PROFILE



Notes:

- Notes: 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C temperature of 260°C 2. Peak wave soldering temperature between 245°C ~ 255°Cfor 3 sec (5 sec max). 3. Do not apply stress to the epoxy resin while the temperature is above 85°C. 4. Fixtures should not incur stress on the component when mounting and during soldering process. 5. SAC 305 solder alloy is recommended. 6. No more than one wave soldering pass.

PACKING & LABEL SPECIFICATIONS



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PRECAUTIONS

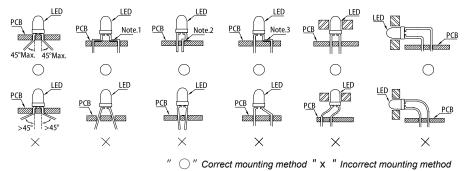
Storage conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature \leq 30°C and relative humidity < 60%.
- Product in the original sealed package is recommended to be assembled within 72 hours of opening.
 Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

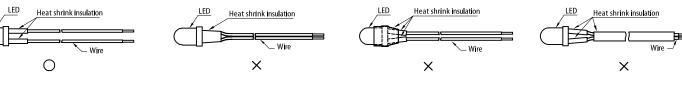
LED Mounting Method

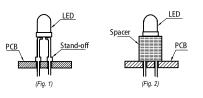
 The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. Note 1-3: Do not route PCB trace in the contact area between the

Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.



2. When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure.

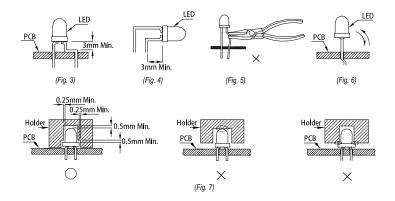




- 3. Use stand-offs (Fig.1) or spacers (Fig.2) to securely position the LED above the PCB.
- 4. Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend (*Fig. 3 , Fig. 4*).
- 5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (*Fig. 5*)

Lead Forming Procedures

- 1. Do not bend the leads more than twice. (Fig. 6)
- 2. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering. (*Fig.* 7)
- 3. The tip of the soldering iron should never touch the lens epoxy.
- 4. Through-hole LEDs are incompatible with reflow soldering.
- 5. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



PRECAUTIONARY NOTES

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- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to
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