



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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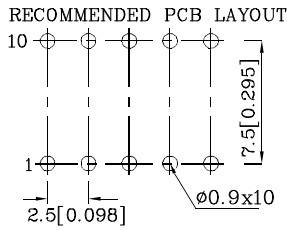
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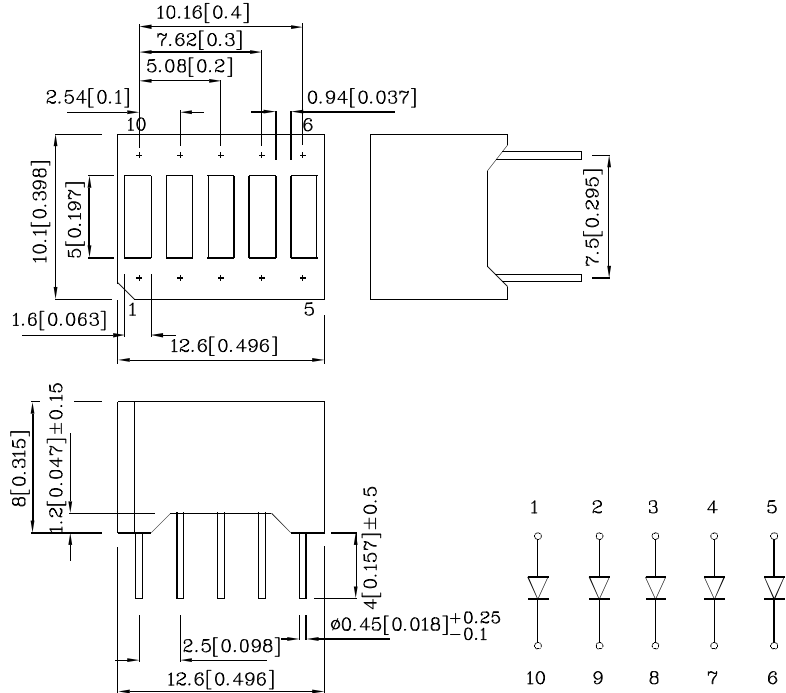


Features

- Robust package
- Uniform light disbursement
- Ideal for backlighting logos or icons
- Excellent for flush mounting
- Standard configuration: Gray face w/ white segments
- RoHS compliant



Package Schematics



Notes:

1. All dimensions are in millimeters (inches), Tolerance is $\pm 0.25(0.01)$ unless otherwise noted.
2. Specifications are subject to change without notice.

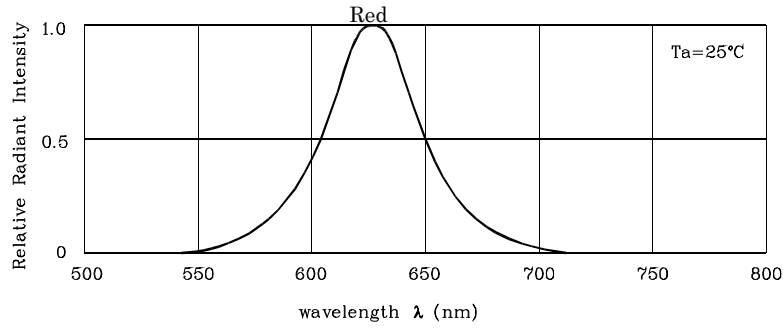
Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)		Red (GaAsP/GaP)	Unit
Reverse Voltage	V_R	5	V
Forward Current	I_F	30	mA
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width	i_{FS}	160	mA
Power Dissipation	P_D	75	mW
Operating Temperature	T_A	-40 ~ +85	°C
Storage Temperature	T_{stg}	-40 ~ +85	
Lead Solder Temperature [2mm Below Package Base]	260°C For 3-5 Seconds		

A Relative Humidity between 40% and 60% is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

Operating Characteristics ($T_A=25^\circ\text{C}$)		Red (GaAsP/GaP)	Unit
Forward Voltage (Typ.) ($I_F=10\text{mA}$)	V_F	1.9	V
Forward Voltage (Max.) ($I_F=10\text{mA}$)	V_F	2.5	V
Reverse Current (Max.) ($V_R=5\text{V}$)	I_R	10	μA
Wavelength of Peak Emission CIE127-2007* (Typ.) ($I_F=10\text{mA}$)	λ_P	627*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) ($I_F=10\text{mA}$)	λ_D	617*	nm
Spectral Line Full Width At Half-Maximum (Typ.) ($I_F=10\text{mA}$)	$\Delta\lambda$	45	nm
Capacitance (Typ.) ($V_F=0\text{V}$, $f=1\text{MHz}$)	C	15	pF

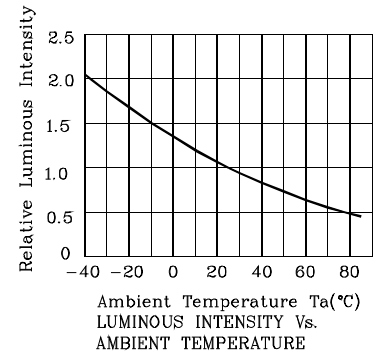
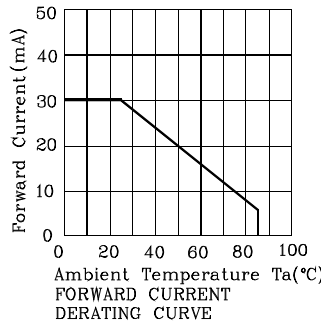
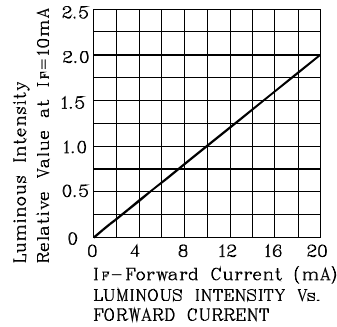
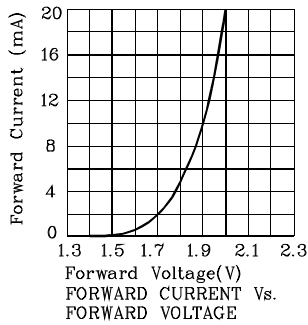
Part Number	Emitting Color	Emitting Material	Luminous Intensity CIE127-2007* ($I_F=10\text{mA}$) ucd		Wavelength CIE127-2007* nm λ_P	Description
			min.	typ.		
XGURX5D	Red	GaAsP/GaP	3600 900*	8990 1990*	627*	5 Segment Bar graph-Display

*Luminous intensity value and wavelength are in accordance with CIE127-2007 standards.

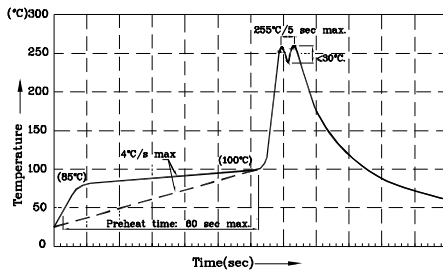


RELATIVE INTENSITY Vs. CIE WAVELENGTH

❖ Red



Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)



Notes:

1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C during soldering process.
2. Peak wave soldering temperature between $245^\circ\text{C} \sim 255^\circ\text{C}$ for 3 sec (5 sec max).
3. Do not apply stress to the epoxy resin while the temperature is above 85°C .
4. Fixtures should not incur stress on the component when mounting and during soldering process.
5. SAC 305 solder alloy is recommended.
6. No more than one wave soldering pass.
7. During wave soldering, the PCB top-surface temperature should be kept below 105°C .

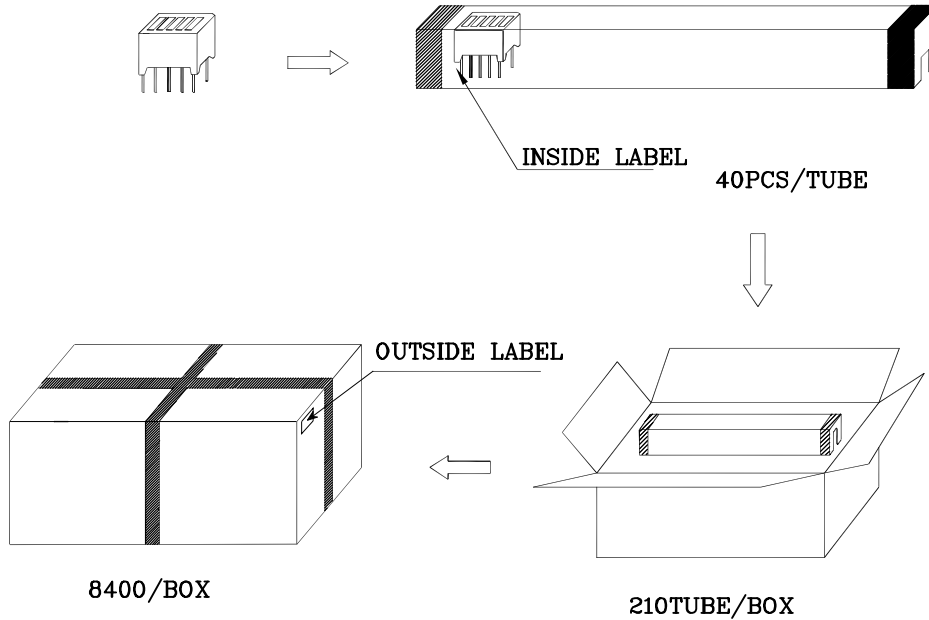
Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity / luminous flux, or wavelength), the typical accuracy of the sorting process is as follows:

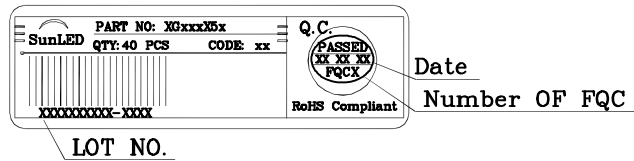
1. Wavelength: $\pm 1\text{nm}$
2. Luminous Intensity / Luminous Flux: $\pm 15\%$
3. Forward Voltage: $\pm 0.1\text{V}$

Note: Accuracy may depend on the sorting parameters.

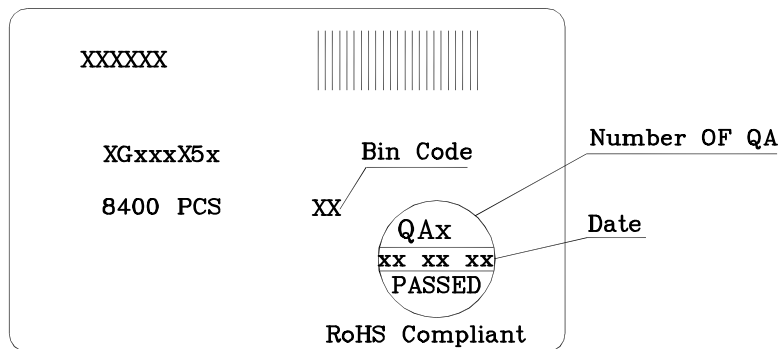
PACKING & LABEL SPECIFICATIONS



Inside Label On IC-tube



Outside Label On Box



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