

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









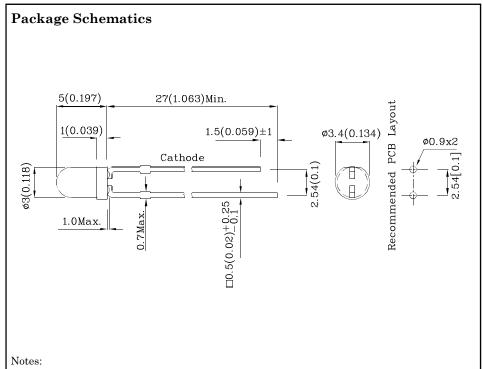


Features

- Radial / Through hole package
- \bullet Reliable & robust
- Low power consumption
- Available on tape and reel
- RoHS Compliant







- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is $\pm 0.25(0.01")$ unless otherwise noted.
- 3. Specifications are subject to change without notice.

Absolute Maximum Ratings (T _A =25°C)	TNI (GaAs)	Unit			
Reverse Voltage	$V_{\rm R}$	5	V		
Forward Current	I_{F}	50	mA		
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width	iFS	1200	mA		
Power Dissipation	P_{D}	80	mW		
Operating Temperature	T _A -40 ~ +85		°C		
Storage Temperature	Tstg	-40 ~ +85	30		
Lead Solder Temperature [2mm Below Package Base]	260°C For 3 Seconds				
Lead Solder Temperature [5mm Below Package Base]	260°C For 5 Seconds				

A Relative Humidity between 40% and 60% is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

Operating Characteristics (T _A =25°C)	TNI (GaAs)	Unit	
Forward Voltage (Typ.) (I _F =20mA)	$ m V_F$	1.2	V
Forward Voltage (Max.) (I _F =20mA)	$ m V_{F}$	1.6	V
Reverse Current (Max.) $(V_R=5V)$	${ m I}_{ m R}$	10	uA
Wavelength of Peak Emission CIE127-2007* (Typ.) (I _F =20mA)	λΡ	940*	nm
Spectral Line Full Width At Half-Maximum (Typ.) (I _F =20mA)	$\triangle \lambda$	50	nm
Capacitance (Typ.) (V _F =0V, f=1MHz)	С	90	pF

Part Number	Emitting Material	Lens-color	Radiant Intensity CIE127-2007* (Po=mW/sr) @20mA		CIE127-2007* CIE127-2 (Po=mW/sr) (Po=mW		Wavelength CIE127-2007* nm λP	Viewing Angle 20 1/2
			min.	typ.	min.	typ.		
XTNI30W GaAs	Water Clear -	8	11	18	31	940*	50°	
		3*	7*	8*	14*			

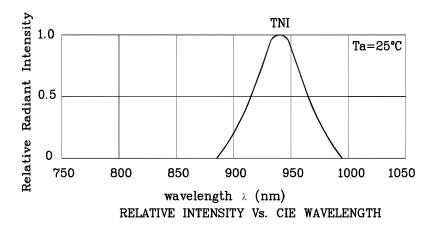
^{*}Radiant intensity value and wavelength are in accordance with CIE127-2007 standards.

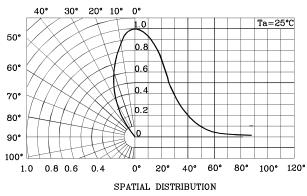
Oct 11,2016

XDSA2672 V9-X Layout: Maggie L.

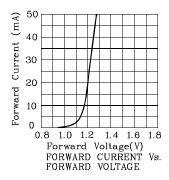


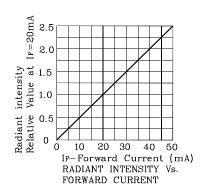


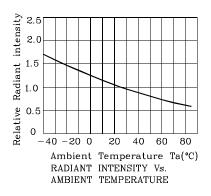




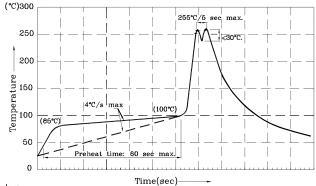
❖ TNI







Wave Soldering Profile For Thru-Hole Products (Pb-Free Components)



Notes: Notes. I. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of $260^{\circ}C$ 2. Peak wave soldering temperature between $245^{\circ}C \sim 255^{\circ}C$ for 3 sec

(5 sec max).

 $3.\mathrm{Do}$ not apply stress to the epoxy resin while the temperature is above $85^{\circ}\mathrm{C}$. $4.\mathrm{Fixtures}$ should not incur stress on the component when mounting and

during soldering process. 5.SAC 305 solder alloy is recommended.

6. No more than one wave soldering pass.

Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity / luminous flux),

the typical accuracy of the sorting process is as follows:

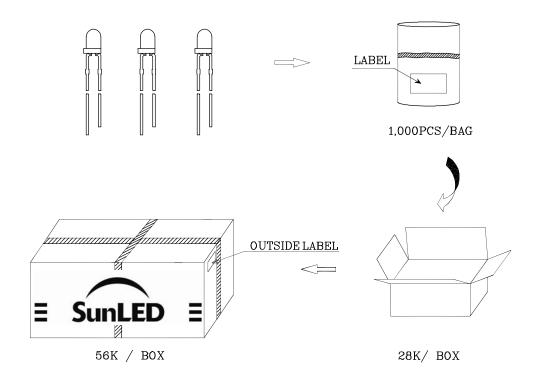
1. Radiant Intensity / Luminous Flux: +/-15%

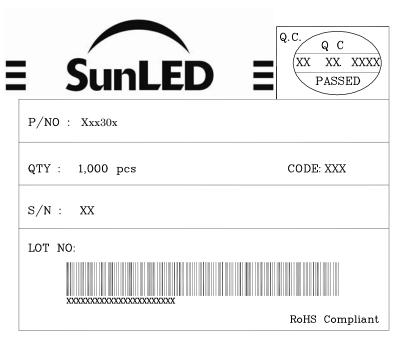
2. Forward Voltage: +/-0.1V

Note: Accuracy may depend on the sorting parameters.



PACKING & LABEL SPECIFICATIONS





TERMS OF USE

- 1. Data presented in this document reflect statistical figures and should be treated as technical reference only.
- 2. Contents within this document are subject to improvement and enhancement changes without notice.
- 3. The product(s) in this document are designed to be operated within the electrical and environmental specifications indicated on the datasheet. User accepts full risk and responsibility when operating the product(s) beyond their intended specifications.
- 4. The product(s) described in this document are intended for electronic applications in which a person's life is not reliant upon the LED. Please consult with a SunLED representative for special applications where the LED may have a direct impact on a person's life.
- 5. The contents within this document may not be altered without prior consent by SunLED.
- 6. Additional technical notes are available at http://www.SunLEDusa.com/TechnicalNotes.asp

XDSA2672 V9-X Layout: Maggie L.