# imall

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#### PolyZen Polymer Enhanced Zener Diode Micro-Assemblies

#### PRODUCT: ZEN056V075A48LS

DOCUMENT: SCD27365 REV LETTER: E REV DATE: JUNE 22, 2011 PAGE NO : 1 OF 8

### **Specification Status: Released**



#### **GENERAL DESCRIPTION**

TE PolyZen devices are polymer enhanced, precision Zener diode micro-assemblies. They offer resettable protection against multi-Watt fault events without the need for multi-Watt heat sinks.

The Zener diode used for voltage clamping in a PolyZen micro-assembly was selected due to its relatively flat voltage vs current

response. This helps improve output voltage clamping, even when input voltage is high and diode currents are large.

An advanced feature of the PolyZen micro-assembly is that the Zener diode is thermally coupled to a resistively non-linear, polymer PTC (positive temperature coefficient) layer. This PTC layer is fully integrated into the device, and is electrically in series between  $V_{IN}$  and the diode clamped  $V_{OUT}$ .

This advanced PTC layer responds to either extended diode heating or overcurrent events by transitioning from a low to high resistance state, also known as "tripping". A tripped PTC will limit current and generate voltage drop. It helps to protect both the Zener diode and the follow on electronics and effectively increases the diode's power handling capability.

The polymer enhanced Zener diode helps protect sensitive portable electronics from damage caused by inductive voltage spikes, voltage transients, incorrect power supplies and reverse bias. These devices are particularly suitable for portable electronics and other low-power DC devices.

#### BENEFITS

- Stable Zener diode helps shield downstream electronics from overvoltage and reverse bias
- Trip events shut out overvoltage and reverse bias sources
- Analog nature of trip events minimizes upstream inductive spikes
- Minimal power dissipation requirements
- Single component placement

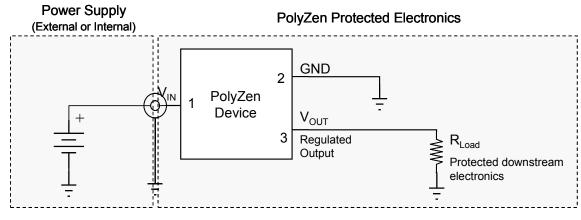
#### **FEATURES**

- Overvoltage transient suppression
- Stable V<sub>z</sub> vs fault current
- Time delayed, overvoltage trip
- Time delayed, reverse bias trip
- Multi-Watt power handling capability
- Integrated device construction
- RoHS Compliant

#### TARGET APPLICATIONS

- DC power port protection in portable electronics
- DC power port protection for systems using barrel jacks for power input
- Internal overvoltage & transient suppression
- DC output voltage regulation

#### **TYPICAL APPLICATION BLOCK DIAGRAM**





**PolyZen** Polymer Enhanced Zener Diode Micro-Assemblies

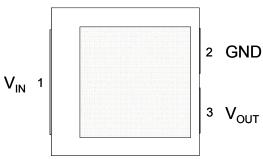
**Pad Dimensions** 

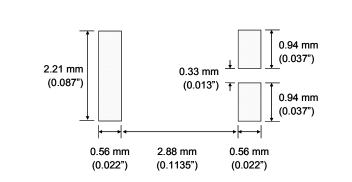
#### PRODUCT: ZEN056V075A48LS

DOCUMENT: SCD27365 REV LETTER: E REV DATE: JUNE 22, 2011 PAGE NO : 2 OF 8

#### **CONFIGURATION INFORMATION**

#### Pin Configuration (Top View)

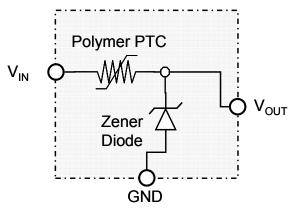




#### **PIN DESCRIPTION**

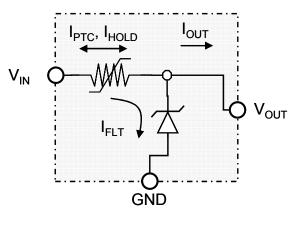
Pin Number	Pin Name	Pin Function			
1	V <sub>IN</sub>	V <sub>IN</sub> . Protected input to Zener diode.			
2	GND	GND			
3	V <sub>OUT</sub>	Vour. Zener regulated voltage output			

#### **BLOCK DIAGRAM**



#### **DEFINITION of TERMS**

I <sub>PTC</sub>	Current flowing through the PTC portion of the
	circuit
I <sub>FLT</sub>	RMS fault current flowing through the diode
I <sub>OUT</sub>	Current flowing out the V <sub>OUT</sub> pin of the device
Trip Event	A condition where the PTC transitions to a high
-	resistance state, thereby significantly limiting IPTC
	and related currents.
Trip	Time the PTC portion of the device remains in a
Endurance	high resistance state.
Endurance	high resistance state.





#### PolyZen Polymer Enhanced Zener Diode **Micro-Assemblies**

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DOCUMENT: SCD27365 **REV LETTER: E REV DATE: JUNE 22, 2011** PAGE NO: 3 OF 8

#### **GENERAL SPECIFICATIONS**

Operating Temperature Storage Temperature

-40° to +85°C -40° to +85°C

#### ELECTRICAL CHARACTERISTICS<sup>1-3, 11</sup> (Typical unless otherwise specified)

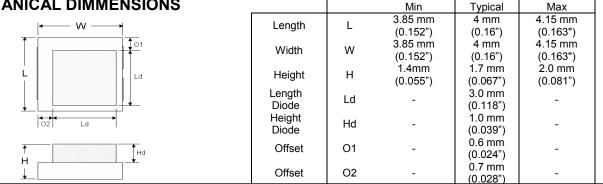
	Vz <sup>4</sup> (V)		l <sub>zt</sub> <sup>4</sup>	I <sub>но∟D</sub> ⁵ <sup>@</sup> 20⁰С	Leakage Current		R Typ <sup>6</sup>	R <sub>1Max</sub> <sup>7</sup>	V <sub>int</sub> Max <sup>8</sup> (V)		I <sub>FLT</sub> Max <sup>9</sup>		Tripped Power Dissipation <sup>10</sup> Max	
Min	Тур	Max	(A)	(A)	Test Voltage	Max Current (mA)	(Ohms) lax rrent	(Ohms)	V <sub>INT</sub> Max (V)	Test Current (A)	I <sub>FLT</sub> Max (A)	Test Voltage (V)	Value (W)	Test Voltage (V)
5.45	5.6	5.75	0.1	0.75	5.25	10	0.28	0.45	48V	ЗA	+10 -40	+48 -16V	0.8	48

Note 1: Electrical characteristics determined at 25°C unless otherwise specified.

This device is intended for limited fault protection. Repeated trip events or extended trip endurance can degrade the device and Note 2: may affect performance to specifications. Performance impact will depend on multiple factors including, but not limited to, voltage, trip current, trip duration, trip cycles, and circuit design. For details or ratings specific to your application contact TE Connectivity Circuit Protection directly.

- Specifications developed using 1.0 ounce 0.045" wide copper traces on dedicated FR4 test boards. Performance in your Note 3: application may vary.
- $I_{zt}$  is the current at which  $V_z$  is measured ( $V_z = V_{OUT}$ ). Additional  $V_z$  values are available on request. Note 4:
- Note 5: IHOLD: Maximum steady state IPTC (current entering or exiting the VIN pin of the device) that will not generate a trip event at the specified temperature. Specification assumes IFLT (current flowing through the Zener diode) is sufficiently low so as to prevent the diode from acting as a heat source. Testing is conducted with an "open" Zener.
- Note 6: R Typ: Resistance between  $V_{IN}$  and  $V_{OUT}$  pins during normal operation at room temperature.
- R<sub>1Max</sub>: The maximum resistance between V<sub>IN</sub> and V<sub>OUT</sub> pins at room temperature, one hour after 1<sup>st</sup> trip or after reflow Note 7: soldering.
- VINT Max: VINT Max relates to the voltage across the PPTC portion of the PolyZen device (VIN-VOUT). VINT Max is defined as the Note 8: voltage (VIN-VOUT) at which typical qualification devices (98% devices, 95% confidence) survived at least 100 trip cycles and 24 hours trip endurance at the specified voltage (V<sub>IN</sub>-V<sub>OUT</sub>) and current (I<sub>PTC</sub>). V<sub>INT</sub> Max testing is conducted using a "shorted" load (V<sub>OUT</sub> = 0 V). V<sub>INT</sub> Max is a survivability rating, not a performance rating.
- Note 9: IFLT Max: IFLT Max relates to the stead state current flowing through the diode portion of the PolyZen device in a fault condition, prior to a trip event. IFLT Max is defined as the current at which typical qualification devices (12 parts per lot from 3 lots) survived 100 test cycles. RMS fault currents above IFLT Max may permanently damage the diode portion of the PolyZen device. Testing is conducted with NO load connected to V<sub>OUT</sub>, such that I<sub>OUT</sub> = 0. "Test voltage" is defined as the voltage between V<sub>IN</sub> to GND and includes the PolyZen Diode drop. Specification is dependent on the direction of current flow through the diode. IFLT Max is a survivability rating, not a performance rating.
- Note 10: The power dissipated by the device when in the "tripped" state, as measured on TE test boards (see note 3).
- Note 11: Specifications based on limited qualification data and subject to change.

#### **MECHANICAL DIMMENSIONS**



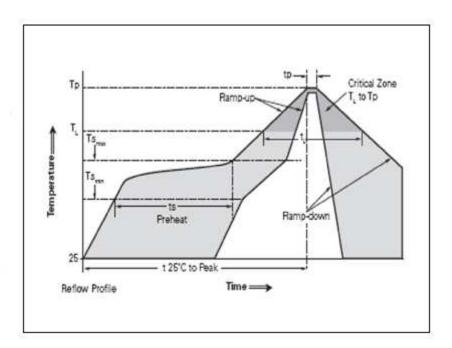


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#### PRODUCT: ZEN056V075A48LS

DOCUMENT: SCD27365 REV LETTER: E REV DATE: JUNE 22, 2011 PAGE NO : 4 OF 8

SOLDER REFLOW RECOMMENDATIONS:							
Classification Reflow Profiles							
Profile Feature	Pb-Free Assembly						
Average Ramp-Up Rate (Tsmax to Tp)	3° C/second max.						
Preheat							
<ul> <li>Temperature Min (Tsmin)</li> </ul>	150 °C						
<ul> <li>Temperature Max (Tsmax)</li> </ul>	200 °C						
<ul> <li>Time (tsmin to tsmax)</li> </ul>	60-180 seconds						
Time maintained above:							
Temperature (TL)	217 °C						
• Time (tL)	60-150 seconds						
Peak/Classification Temperature							
(Тр)	260 °C						
Time within 5 °C of actual Peak							
Temperature (tp)	20-40 seconds						
Ramp-Down Rate	6 °C/second max.						
Time 25 °C to Peak Temperature	8 minutes max.						



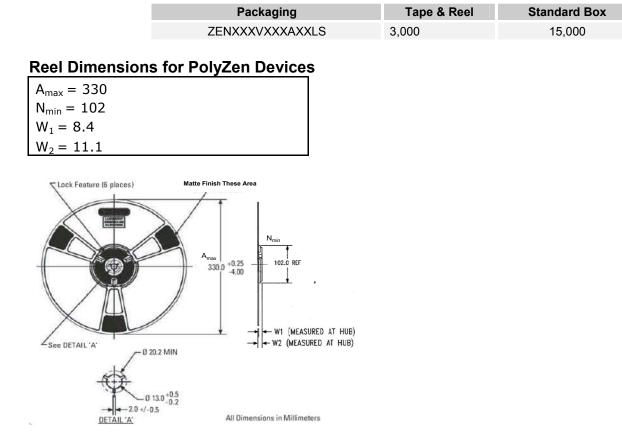


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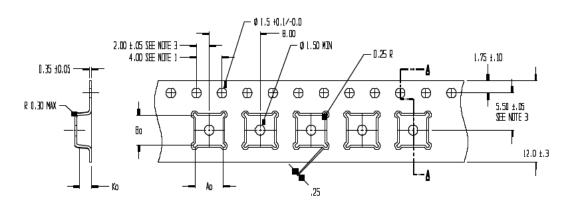
#### PRODUCT: ZEN056V075A48LS

DOCUMENT: SCD27365 REV LETTER: E REV DATE: JUNE 22, 2011 PAGE NO : 5 OF 8

#### PACKAGING



#### Taped Component Dimensions for PolyZen Devices



 Aa = 4.35
 NOTES:

 Bo = 4.35
 I. JO SPROCKET HOLE PITCH CLMULATIVE TOLERANCE ±0.2

 Ka = 2.30
 2. CAMBER IN COMPLIANCE VITH EIA 481

 3. POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED

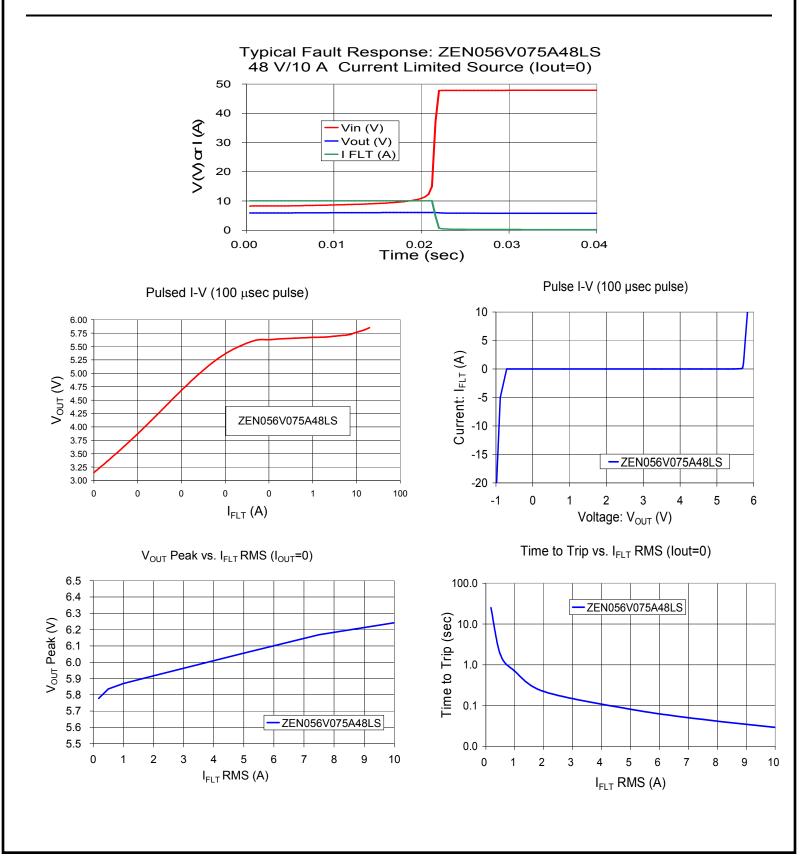
 AS TRLE POSITION OF POCKET, NOT POCKET HOLE



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DOCUMENT: SCD27365 REV LETTER: E REV DATE: JUNE 22, 2011 PAGE NO : 6 OF 8

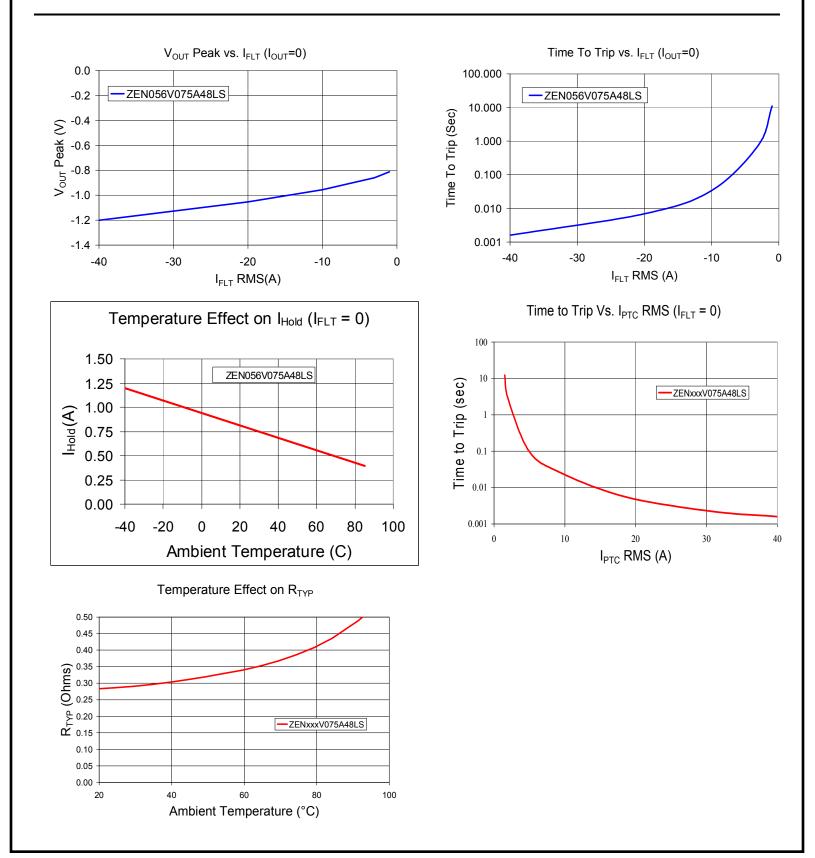




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#### PRODUCT: ZEN056V075A48LS

DOCUMENT: SCD27365 REV LETTER: E REV DATE: JUNE 22, 2011 PAGE NO : 7 OF 8





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#### PRODUCT: ZEN056V075A48LS

DOCUMENT: SCD27365 REV LETTER: E REV DATE: JUNE 22, 2011 PAGE NO : 8 OF 8

#### **Materials Information**

ROHS Compliant Directive 2002/95/EC Compliant





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