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ZXCT1050 Precision wide input range current monitor

Description

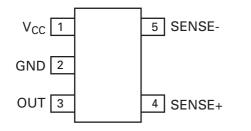
The ZXCT1050 is a wide input range current monitor, which operates over a range of input voltages from ground up to V_{CC} -2V. As a result the ZXCT1050 can be used on the high or low side of the load.

The ZXCT1050 provides variable gain by using two external resistors. The first of which sets the transconductance and the second setting the overall gain.

Features

- · Accurate down to end current sensing
- Output voltage scaling x10
- 0 to V_{CC}-2V sense input range
- 2.7 to 20V supply range
- 50µA quiescent current
- SOT23-5 package

Pin connections

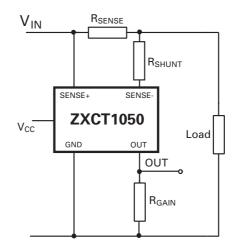


The very low offset voltage enables a typical accuracy of 1% for sense voltages of only 30mV, giving better tolerances for small sense resistors necessary at higher currents.

Applications

- Power supply
- DC motor and solenoid control
- Battery management
- Over current monitor
- Power management
- Short circuit detection

Typical application circuit



Ordering information

Order code	Pack	Part mark	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXCT1050E5TA	SOT23-5	1050	7	8	3000

Absolute maximum ratings

20V
-0.6 to V _{CC}
-0.6V and V _{CC} +0.6V
500mV
-40 to 125°C
-55 to 150°C
150°C
300mW^* at T _A = 25°C (De-rate to zero for T _J = 150°C)

Operation above the absolute maximum rating may cause device failure. Operation at the absolute maximum ratings, for extended periods, may reduce device reliability.

Recommended operating conditions

Parameter			Max.	Units
V _{SENSE+}	Common-mode sense input range		V _{CC} -2	V
V _{CC}	Supply voltage range	2.7	20	V
V _{SENSE}	Differential sense input voltage range	10	300	mV
V _{OUT}	Output voltage range	0	V _{CC} -2	V
T _A	Ambient temperature range	-40	125	°C

Recommended resistor gain setting combinations

Gain	R _{SH}	R _G
10	7.5kΩ	$3.75 \mathrm{k}\Omega$
20	7.5kΩ	7.5k Ω
50	7.5kΩ	18.7kΩ
100	7.5kΩ	37.5 kΩ
20	3.75k Ω	$3.75 k\Omega$
50	1.5kΩ	3.75k Ω
100	750Ω	$3.75 \mathrm{k}\Omega$

Pin function table

PIN	Name	Description
1	V _{CC}	This is the analog supply and provides power to internal circuitry.
2	GND	Ground pin.
3	OUT	Output pin. A resistor, R _{GAIN} , connected from this pin pin down to ground develops an output voltage.
4	SENSE+	This is the positive input of the current monitor and has an input range from 0V up to V_{CC} – 2V.
5	SENSE-	This is the negative input of the current monitor and has an input range from 0V up to V_{CC} – 2V. The current through this pin varies with differential sense voltage. A resistor, R_{SHUNT} , from this pin to the rail being sensed set the transconductance of the current monitor.

Electrical characteristics

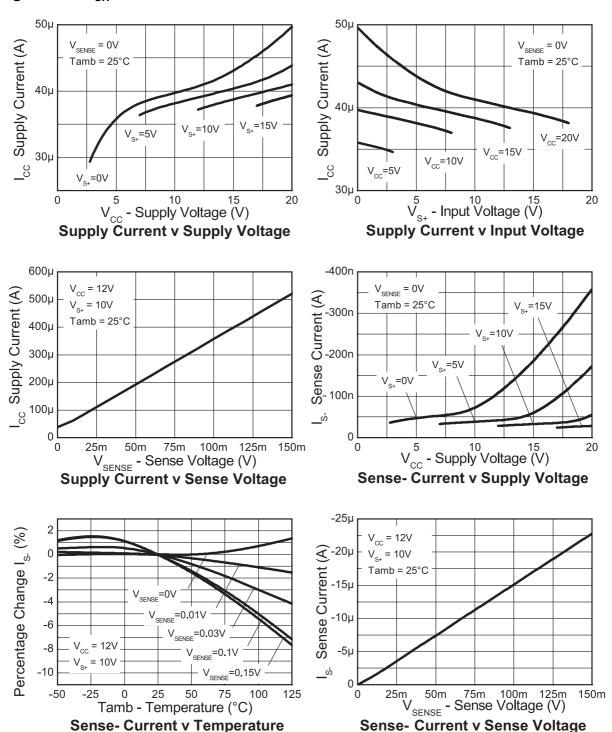
$\label{eq:test} \mbox{Test conditions } T_{A} = 25^{\circ}\mbox{C}, \ V_{SENSE+} = 10\mbox{V}, \ V_{CC} = 12\mbox{V}, \ V_{SENSE} = 100\mbox{mV}, \ R_{SH} = 7.5\mbox{k}\Omega, \ R_{G} = 3.75\mbox{k}\Omega.$

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Units
Ι _Q	V _{CC} pin current	V _{SENSE} = 0V			45	70	μA
V _{OUT}	Output voltage	V _{SENSE} = 0V =30mV =100mV =150mV		0 285 0.97 1.45	3 300 1.00 1.50	15 315 1.03 1.55	mV mV V V
I _{SENSE+}	V _{SENSE+} input current	V _{SENSE} = 0V	,		60	150	nA
I _{SENSE-}	V _{SENSE-} input current	V _{SENSE} = 0V			15	150	nA
V _{OUT} TC	V _{OUT} variation with temperature	See note ^(*)				300	ppm/°C
Gain	V _{OUT} /V _{SENSE}				10		
Accuracy	Total output error			-3		3	%
BW	Bandwidth	$V_{SENSE(DC)}$ $V_{SENSE(AC)}$ = 10mV = 10mV _{PP}			300		kHz
		V _{SENSE(DC)} = 100mV	CL = 5pF,		0.8		MHz
PSRR	Power supply rejection ratio	V _{CC} = 2.7V to 20V V _{SENSE+} = 0.7V			60		dB
CMRR	Common mode rejection ratio	V _{CC} = 20V V _{SENSE+} = 0 to 18V			70		dB

NOTES:

(*) Temperature dependent measurements are extracted from characterisation and simulation results.

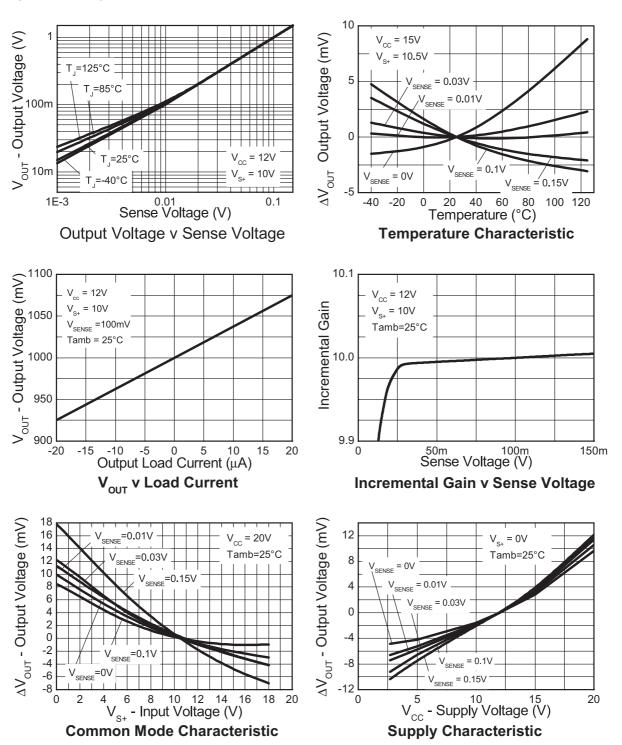
Typical characteristics



 R_G = 3.75k Ω , R_{SH} = 7.5k Ω unless otherwise stated.

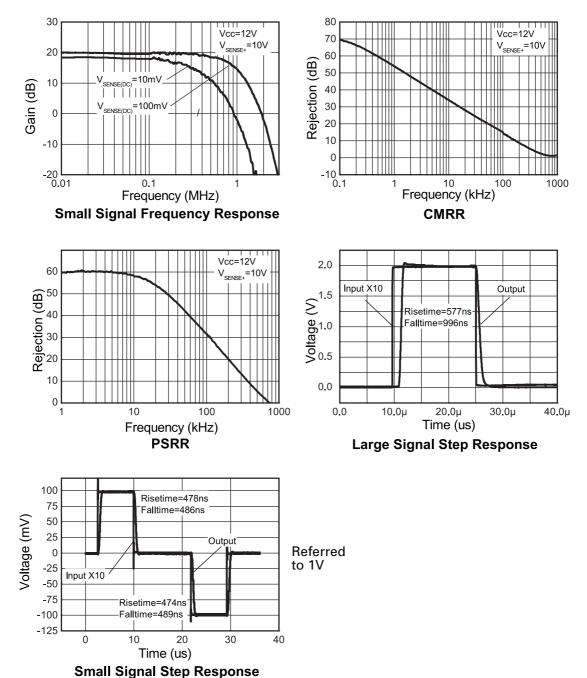
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Typical characteristics



 $\textbf{R}_{\textbf{G}}$ = 3.75k Ω , $\textbf{R}_{\textbf{SH}}$ = 7.5k Ω unless otherwise stated.

Typical characteristics



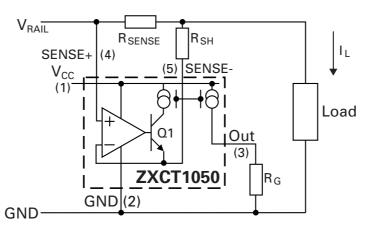
 $\textbf{R}_{\textbf{G}}$ = 3.75k $\Omega_{\textbf{r}}$, $\textbf{R}_{\textbf{SH}}$ = 7.5k Ω unless otherwise stated.

Applications information

The ZXCT1050 is a current output version of the ZXCT1051 and as such uses a separate power supply pin. All biasing for the internal amplifiers comes from its separate V_{CC} input and is not 'line powered', unlike the ZXCT1021.

This means that at very small sense voltages the ZXCT1050 draws very little current (<1 μ A) from the lines being sensed.

The separate V_{CC} pin enables the ZXCT1050 to be operated at sense line voltages down to 0V, where the ZXCT1021 would switch off. This feature enables the ZXCT1050 to be used to sense the currents flowing through lines that have been shorted to ground.



Basic operation

Load current, I_L, from V_{RAIL} is drawn through R_{SENSE} developing a voltage V_{SENSE} across the sense inputs of the ZXCT1050.

The internal amplifier forces V_{SENSE} across external resistance R_{SH} (internal on the ZXCT1051) causing a current to flow through transistor Q1 and out of the output pin, OUT. This current is then converted to a voltage by a resistor, R_G , between OUT and GND.

The overall gain of the ZXCT1050 is determined by the following expression:

$$GAIN = 20 \times \frac{R_G}{R_{SH}}$$

A ratio of 1:2 between R_{SH} and R_G creates the fixed gain of 10 with an output impedance equal to RG (see electrical characteristics for output current-voltage characteristics).

The ZXCT1050 has both $\rm R_G$ and $\rm R_{SH}$ external. This allows $\rm R_G$ and $\rm R_{SH}$ to be varied so that the required gain can be achieved at the required output impedance.

For low power applications both $\rm R_G$ and $\rm R_{SH}$ can be increased whereas for driving low impedance $\rm R_G$ and $\rm R_{SH}$ can be decreased.

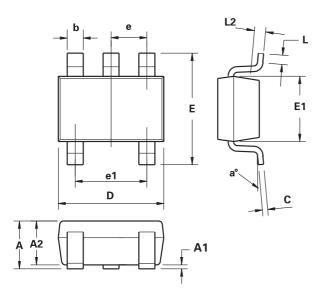
The maximum recommended value for R_G is $40 k\Omega$ and the maximum recommended value for RSH is $10 k\Omega$. Large values of R_{SH} start increasing the effective input offset error, while large values of R_G can created load errors and reduce bandwidths.

The maximum differential input voltage, V_{SENSE}, is 150mV (I_L * R_{SENSE}); however voltages up to 500mV will not damage it. This can be increased further by the inclusion of a resistor, R_{LIM}, between the SENSE+ pin and the rail being sensed, V_{RAIL}.

For best performance R_{SENSE} should be connected as close to the SENSE+ and SENSE- pins thus minimizing any series resistance with $R_{SENSE}.$

Intentionally left blank

Package outline - SOT23-5



DIM	Millimeters Min. Max.		Inc	hes		
			Min.	Max.		
A	-	1.00	-	0.0393		
A1	0.01	0.10	0.0003	0.0039		
A2	0.84	0.90	0.0330	0.0354		
b	0.30	0.45	0.0118	0.0177		
С	0.12	0.20	0.0047	0.0078		
D	2.90 BSC		0.114 BSC			
E	2.80	2.80 BSC		0.110 BSC		
E1	1.60 BSC		0.062 BSC			
e	0.95 BSC		0.0374 BSC			
e1	1.90 BSC		0.0748 BSC			
L	0.30	0.50	0.0118	0.0196		
L2	0.25 BSC		0.010	BSC		
a°	4°	12°	4°	12°		

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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Zetex sales offices

Europe	Americas	Asia Pacific	Corporate Headquarters
Zetex GmbH Kustermann-park Balanstraße 59 D-81541 München Germany	Zetex Inc 700 Veterans Memorial Highway Hauppauge, NY 11788 USA	Zetex (Asia Ltd) 3701-04 Metroplaza Tower 1 Hing Fong Road, Kwai Fong Hong Kong	Zetex Semiconductors plc Zetex Technology Park, Chadderton Oldham, OL9 9LL United Kingdom
Telefon: (49) 89 45 49 49 0 Fax: (49) 89 45 49 49 49 europe.sales@zetex.com	Telephone: (1) 631 360 2222 Fax: (1) 631 360 8222 usa.sales@zetex.com	Telephone: (852) 26100 611 Fax: (852) 24250 494 asia.sales@zetex.com	Telephone: (44) 161 622 4444 Fax: (44) 161 622 4446 hq@zetex.com

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Issue 1 - June 2007

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