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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China











COMPLEMENTARY 60V ENHANCEMENT MODE MOSFET

Product Summary

Device	V _{(BR)DSS}	R _{DS(on) max}	I _D T _A = +25°C
Q2	60V	$55m\Omega$ @ $V_{GS} = 10V$	4.7A
Q1	-60V	105mΩ @ V _{GS} = -10V	-3.9A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Backlighting

Features and Benefits

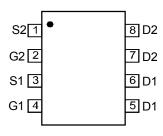
- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

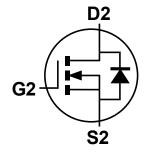
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish annealed over Copper leadframe Solderable per MIL-STD-202, Method 208
- Weight: 0.074 grams (approximate)



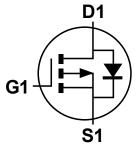




TOP VIEW Internal Schematic







P-Channel MOSFET

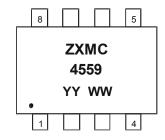
Ordering Information (Note 4)

I	Part Number	Compliance	Case	Packaging
	ZXMC4559DN8TA	Standard	SO-8	500/Tape & Reel
	ZXMC4559DN8TC	Standard	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



ZXMC4559 = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 14 = 2014) WW = Week (01 - 53)



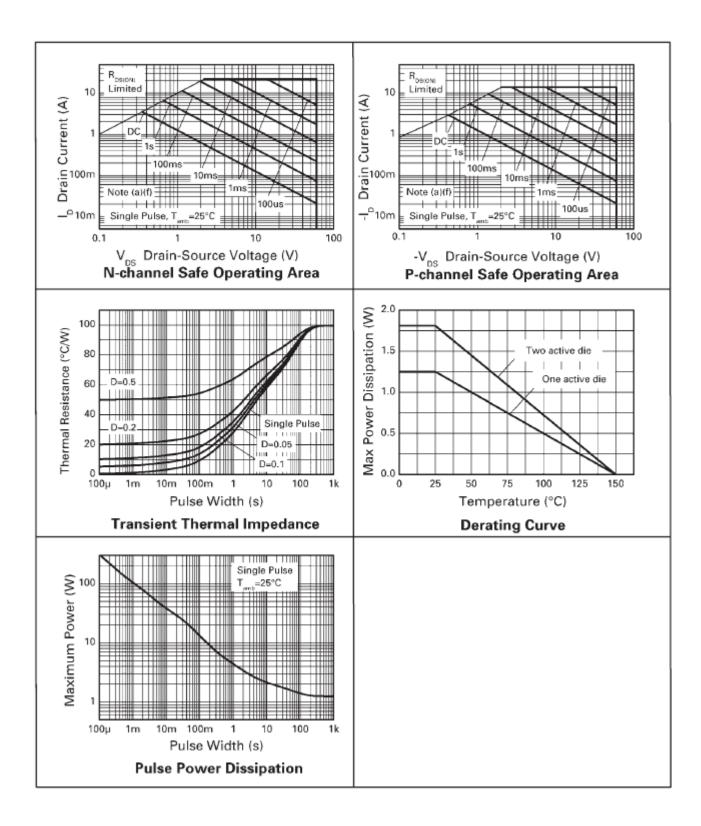
Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value_Q2	Value_Q1	Units	
Drain-Source Voltage	V _{DSS}	60	-60	V	
Gate-Source Voltage	V _{GSS}	±20	±20	V	
Continuous Prain Current V = 10V	SteadyState (Note 5)	I _D	3.6	-2.6	Α
Continuous Drain Current V _{GS} = 10V	t<10s (Note 6)	I _D	4.7	-3.9	Α
Maximum Body Diode Forward Current at t<10s (Note 6)	I _S	3.4	-3.2	Α	
Pulsed Drain Current (300µs pulse, duty cycle = 2%)	I _{DM}	22.2	-18.3	Α	
Pulsed Source Current (Body Diode) (300µs pulse, duty cycl	I _{SM}	22.2	-18.3	Α	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Power Dissipation Linear Derating Factor (Note 5)	P _D	1.25 10	W mW/°C
Power Dissipation	P _D	2.1	W
Linear Derating Factor (Note 6) Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	17 100	mW/°C
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	58	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C





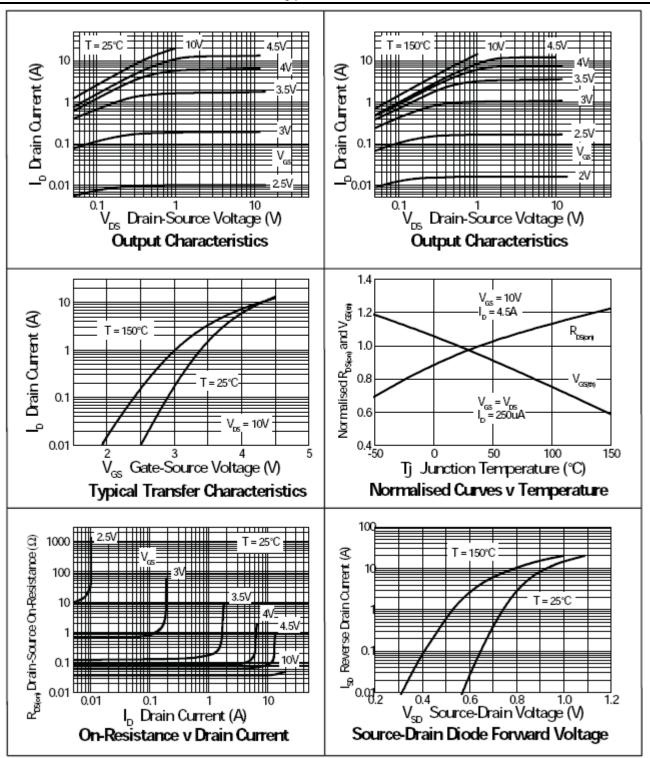


Electrical Characteristics N-Channel Q2 (@T_A = +25°C, unless otherwise specified.)

0 1			_			T (0 10)
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	1		1	1	1	
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	—	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		_	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	1.0	_	_	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	0	_	_	55	mΩ	V _{GS} = 10V, I _D = 4.5A
Static Diain-Source On-Resistance	R _{DS(ON)}	_	_	75	11122	V _{GS} = 4.5V, I _D = 4.0A
Diode Forward Voltage	V _{SD}	_	0.85	1.2	V	V _{GS} = 0V, I _S = 5.5A
Forward Transconductance	9fs	_	10.2	_	S	V _{DS} =15V,I _D =4.5A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}		1063	_		V _{DS} = 30V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	Coss	_	104	_	pF	
Reverse Transfer Capacitance	C _{rss}		64	_		
Total Gate Charge (V _{GS} = 5.0V)	Q_g		11	_		V _{DS} = 30V, I _D = 4.5A
Total Gate Charge (V _{GS} = 10V)	Q_g		20.4	_	nC	
Gate-Source Charge	Q _{gs}		4.1	_	IIC	
Gate-Drain Charge	Q_{gd}		5.1	_		
Turn-On Delay Time	t _{D(on)}		3.5	_		
Turn-On Rise Time	t _r	_	4.1	_	nS	$V_{DD} = 30V, I_D = 1.0A$
Turn-Off Delay Time	t _{D(off)}		26.2		113	V_{GS} = 10V, R_G = 6.0 Ω
Turn-Off Fall Time	t _f		10.6			
Body Diode Reverse Recovery Time	t _{rr}		22		nS	I _F = 2.2A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Qrr		21.4		nC	I _F = 2.2A, di/dt = 100A/μs

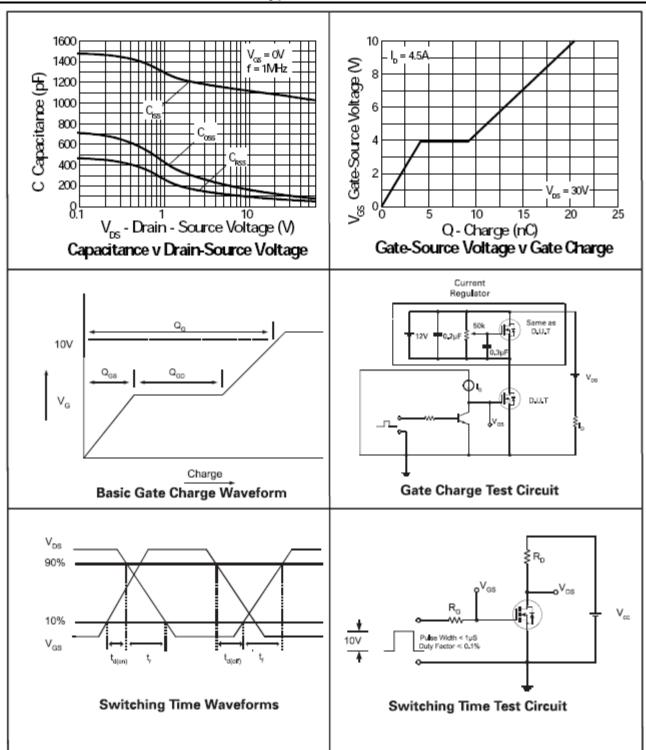


N-Channel Typical Characteristics





N-Channel Typical Characteristics (cont.)





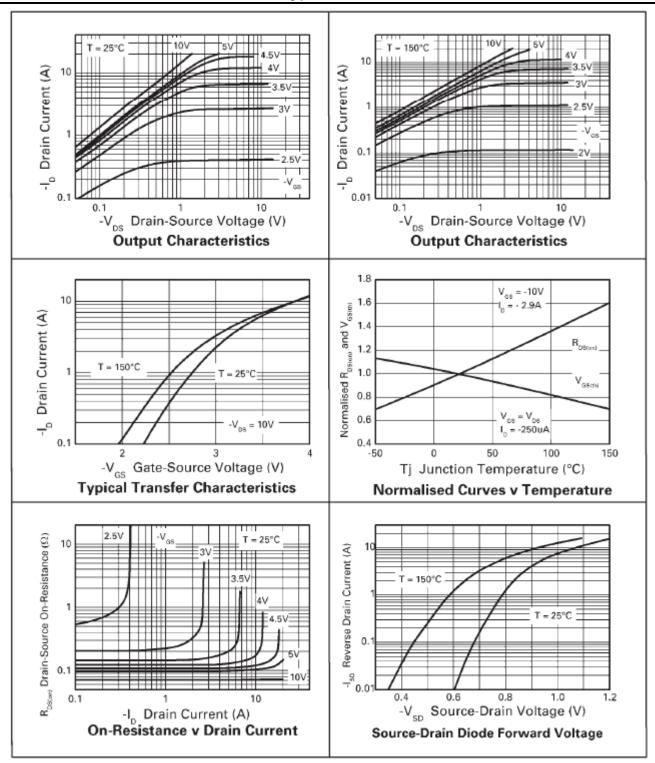
Electrical Characteristics P-Channel Q1 (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-60	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1.0	μA	V _{DS} = -60V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	-1.0	_	_	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	ь	_	_	85	mΩ	$V_{GS} = -10V, I_D = -2.9A$	
Static Dialii-Source Oil-Resistance	R _{DS(ON)}	_	_	125	11122	$V_{GS} = -4.5V$, $I_D = -2.4A$	
Diode Forward Voltage	V_{SD}	_	-0.85	-0.95	V	$V_{GS} = 0V, I_S = -3.4A$	
Forward Transconductance	g _{fs}	_	7.2	_	S	V _{DS} =-15V,I _D =-2.9A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	1021	_		V _{DS} = -30V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	83.1	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	56.4	_			
Total Gate Charge (V _{GS} = -5.0V)	Q_g	_	12.1	_		VDS = -30V, ID = -2.9A	
Total Gate Charge (V _{GS} = -10V)	Q_g	_	24.2	_	nC		
Gate-Source Charge	Qgs	_	2.5	_	IIC		
Gate-Drain Charge	Q_{gd}	_	3.7	_			
Turn-On Delay Time	t _{D(on)}	_	3.5	_			
Turn-On Rise Time	t _r	_	4.1	_	0	V_{DD} = -30V, I_{D} = -1.0A V_{GS} = -10V, R_{G} = 6.0 Ω	
Turn-Off Delay Time	t _{D(off)}	_	35	_	nS		
Turn-Off Fall Time	t _f	_	10	_			
Body Diode Reverse Recovery Time	t _{rr}	_	29.2	_	nS	I _S = -2.0A, dI/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Qrr	_	39.6	_	nC	$I_S = -2.0A$, $dI/dt = 100A/\mu s$	

- Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

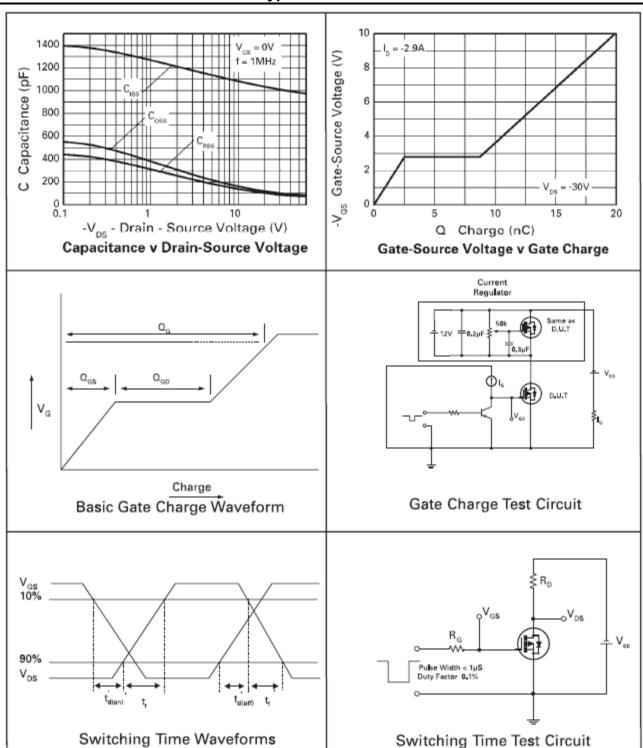


P-Channel Typical Characteristics





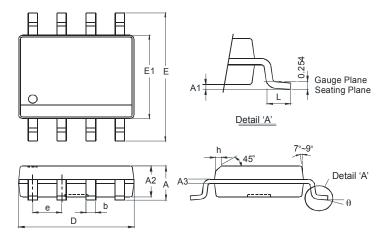
P-Channel Typical Characteristics (cont.)





Package Outline Dimensions

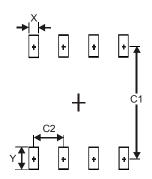
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SO-8						
Dim	Min	Max				
Α	ı	1.75				
A1	0.10	0.20				
A2	1.30	1.50				
A3	0.15	0.25				
b	0.3	0.5				
D	4.85	4.95				
Е	5.90	6.10				
E1	3.85	3.95				
е	1.27 Typ					
h	-	0.35				
L	0.62	0.82				
θ	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27



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