# imall

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## **ZXMN3A02N8**

### **30V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### SUMMARY

 $V_{(BR)DSS} = 30V; R_{DS(ON)} = 0.025\Omega I_D = 9.0A$ 

#### DESCRIPTION

This new generation of TRENCH MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

#### **FEATURES**

- Low on-resistance
- Fast switching speed ٠
- Low threshold ٠
- Low gate drive ٠
- Low profile SOIC package

#### **APPLICATIONS**

- Disconnect switches
- Motor control

#### **ORDERING INFORMATION**

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMN3A02N8TA	7"	12m m	500 units
ZXMN3A02N8TC	13"	12m m	2500 units

#### **DEVICE MARKING**

ZXMN 3A02

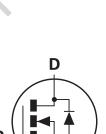
PINOUT

s s	⊖ Single	D D
s⊡	Device	D
G		⊨ D

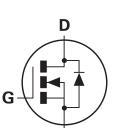
Top View



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## **ZXMN3A02N8**

#### ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	30	V
Gate Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current $V_{GS}$ =-10V; $T_A$ =25°C (b) $V_{GS}$ =-10V; $T_A$ =70°C (b) $V_{GS}$ =-10V; $T_A$ =25°C (a)	ID	9.0 7.2 7.3	A
Pulsed Drain Current (c)	I <sub>DM</sub>	44	А
Continuous Source Current (Body Diode) (b)	I <sub>S</sub>	3.2	A
Pulsed Source Current (Body Diode) (c)	I <sub>SM</sub>	44	A
Power Dissipation at T <sub>A</sub> =25°C (a) Linear Derating Factor	P <sub>D</sub>	1.56 12.5	W mW/°C
Power Dissipation at T <sub>A</sub> =25°C (b) Linear Derating Factor	PD	2.5 20	W mW/°C
Operating and Storage Temperature Range	T <sub>j</sub> :T <sub>stg</sub>	-55 to +150	°C

#### THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)	R <sub>θJA</sub>	80	°C/W
Junction to Ambient (b)	R <sub>0JA</sub>	50	°C/W

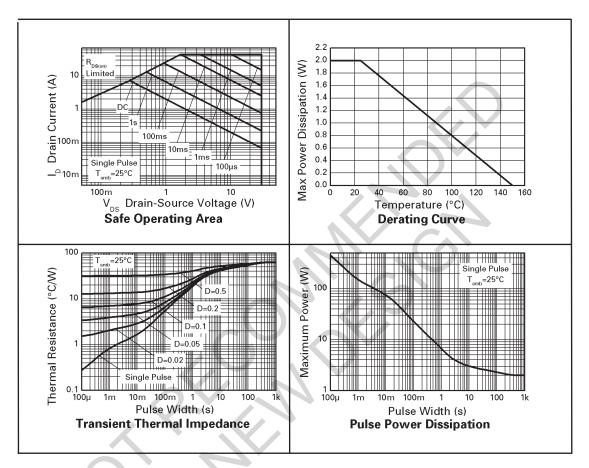
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NOTES

(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions (b) For a device surface mounted on FR4 PCB measured at t≤10 secs.
(c) Repetitive rating 25mm x 25mm FR4 PCB, D = 0.02, pulse width 300µs - pulse width limited by maximum junction temperature.



## ZXMN3A02N8



#### CHARACTERISTICS



## **ZXMN3A02N8**

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.	
STATIC							
Drain-Source Breakdown Voltage	V <sub>(BR)</sub> DSS	30			V	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	
Zero Gate Voltage Drain Current	IDSS			1	μA	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	
Gate-Body Leakage	IGSS			100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	1.0			V	I <sub>D</sub> =250μA, V <sub>DS</sub> = V <sub>GS</sub>	
Static Drain-Source On-State Resistance (1)	R <sub>DS(on)</sub>			0.025 0.035	$\Omega \Omega$	V <sub>GS</sub> =10V, I <sub>D</sub> =12A V <sub>GS</sub> =4.5V, I <sub>D</sub> =10.2A	
Forward Transconductance (1)(3)	9fs		22		s	V <sub>DS</sub> =10V,I <sub>D</sub> =12A	
DYNAMIC (3)							
Input Capacitance	C <sub>iss</sub>		1400		рF		
Output Capacitance	Coss		209		рF	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		120		pF		
SWITCHING(2) (3)							
Turn-On Delay Time	<sup>t</sup> d(on)		3.9		ns		
Rise Time	tr		5.5		ns	V <sub>DD</sub> =10V, I <sub>D</sub> =1A R <sub>G</sub> ≅6.0Ω, V <sub>GS</sub> =4.5V	
Turn-Off Delay Time	<sup>t</sup> d(off)		35.0		ns	(refer to test circuit)	
Fall Time	tf		7.6		ns		
Gate Charge	Qg		14.5		nC	V <sub>DS</sub> =15V,V <sub>GS</sub> =5V, I <sub>D</sub> =5.5A (refer to test circuit)	
Total Gate Charge	Qg		26.8		nC	V <sub>DS</sub> =15V,V <sub>GS</sub> =10V,	
Gate-Source Charge	Qgs	-	4.7		nC	ID=5.5A	
Gate-Drain Charge	Qgd		4.7		nC	(refer to test circuit)	
SOURCE-DRAIN DIODE							
Diode Forward Voltage (1)	V <sub>SD</sub>		0.85	0.95	V	TJ=25°C, I <sub>S</sub> =9A, V <sub>GS</sub> =0V	
Reverse Recovery Time (3)	t <sub>rr</sub>		17		ns	Tj=25°C, IF=5.5A,	
Reverse Recovery Charge (3)	Q <sub>rr</sub>		8.3		nC	di/dt= 100Å/µs	

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**ELECTRICAL CHARACTERISTICS** (at  $T_{amb} = 25^{\circ}C$  unless otherwise stated).

NOTES

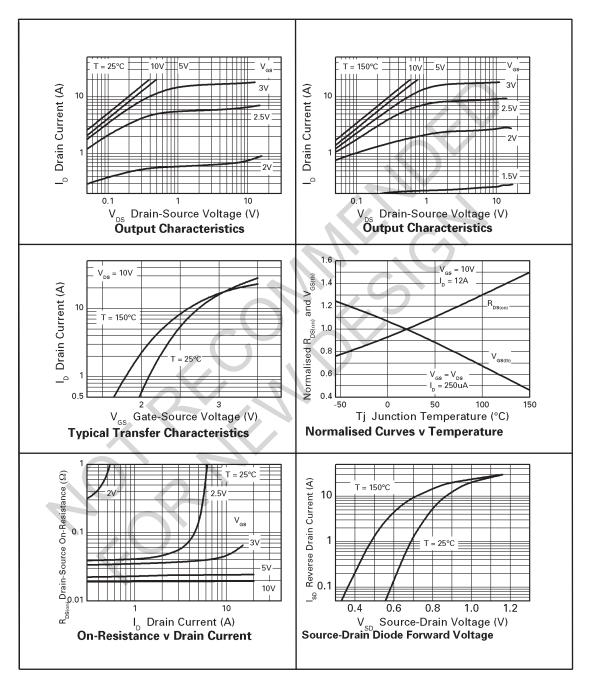
(1) Measured under pulsed conditions. Width  ${\leq}300\mu s.$  Duty cycle  ${\leq}\,2\%$  .

(2) Switching characteristics are independent of operating junction temperature.

(3) For design aid only, not subject to production testing.



## ZXMN3A02N8



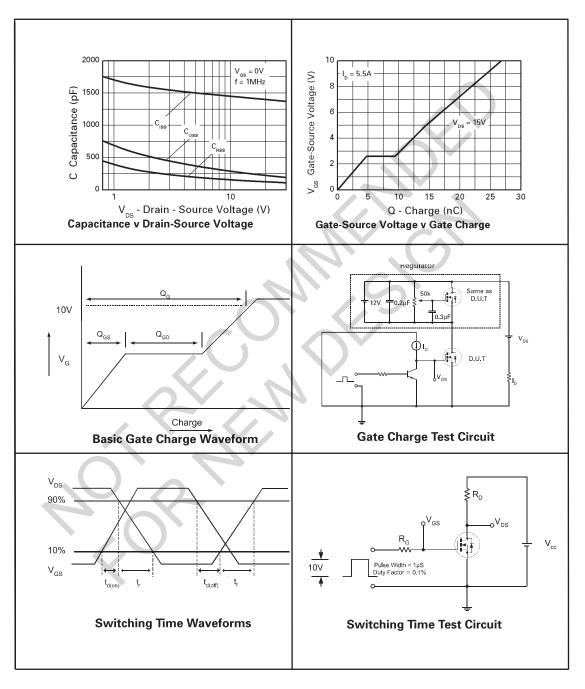
#### CHARACTERISTICS

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## **ZXMN3A02N8**

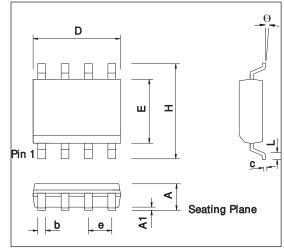


#### CHARACTERISTICS



## **ZXMN3A02N8**

#### PACKAGE OUTLINE



CONTROLLING DIMENSIONS ARE IN INCHES APPROX IN MILLIMETRES

	DIM		MILLIMETRES		
	MIN	МАХ	MIN	МАХ	
А	0.053	0.069	1.35	1.75	
A1	0.004	0.010	0.10	0.25	
D	0.189	0.197	4.80	5.00	
н	0.228	0.244	5.80	6.20	
E	0.150	0.157	3.80	4.00	
L	0.016	0.050	0.40	1.27	
е	0.050	0.050 BSC		BSC	
b	0.013	0.020	0.33	0.51	
с	0.008	0.010	0.19	0.25	
θ	0°	8°	0°	8°	
h	0.010	0.020	0.25	0.50	

PACKAGE DIMENSIONS

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