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SuperSOT4™ 40V PNP SILICON LOW SATURATION SWITCHING TRANSISTOR

SUMMARY

 V_{ceo} =-40V; R_{sat} = 26m Ω ; I_c = -4A

DESCRIPTION

This new 4th generation ultra low saturation transistor utilises the Zetex matrix structure combined with advanced assembly techniques to give extremely low on state losses. This makes it ideal for high efficiency, low voltage switching applications.

MSOP8

FEATURES

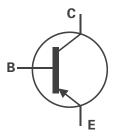
- Extremely Low Equivalent On Resistance
- Extremely Low Saturation Voltage
- h_{FF} characterised up to 8A
- I_C=4A Continuous Collector Current
- MSOP8 package

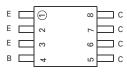
APPLICATIONS

- DC DC Converters
- Power Management Functions
- · Power switches
- Motor control

ORDERING INFORMATION

DEVICE	REEL SIZE (inches)	TAPE WIDTH (mm)	QUANTITY PER REEL
ZXT14P40DXTA	7	12mm embossed	1000 units
ZXT14P40DXTC	13	12mm embossed	4000 units





Top View

DEVICE MARKING

T14P40DX

ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT
Collector-Base Voltage	V _{CBO}	-50	V
Collector-Emitter Voltage	V _{CEO}	-40	V
Emitter-Base Voltage	V _{EBO}	-7.5	V
Peak Pulse Current (c)	I _{CM}	-12	А
Continuous Collector Current	I _C	-4	А
Base Current	IB	-500	mA
Power Dissipation at TA=25°C (a) Linear Derating Factor	P _D	1.1 8.8	W mW/°C
Power Dissipation at TA=25°C (b) Linear Derating Factor	P _D	1.8 14.4	W mW/°C
Operating and Storage Temperature Range	T _j :T _{stg}	-55 to +150	°C

THERMAL RESISTANCE

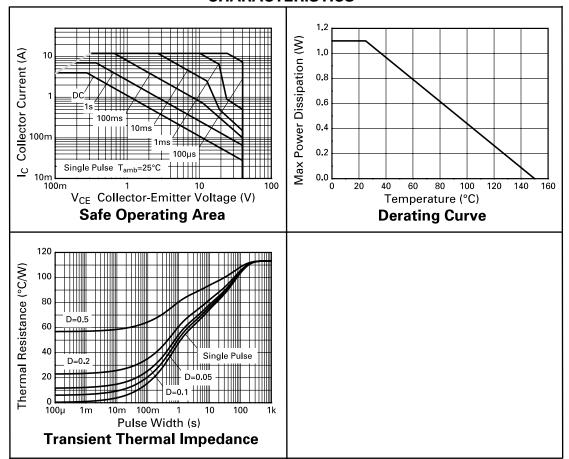
PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)	$R_{\theta JA}$	113	°C/W
Junction to Ambient (b)	$R_{\theta JA}$	70	°C/W

NOTES

- (a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
- (b) For a device surface mounted on FR4 PCB measured at t≤10 secs.
- (c) Repetitive rating pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.



CHARACTERISTICS





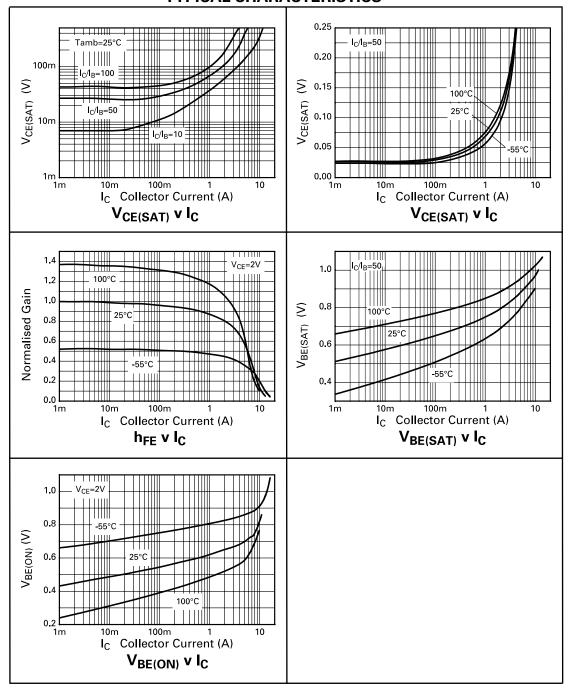
ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25$ °C unless otherwise stated).

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PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.	
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-50	-110		V	I _C =-100μA	
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-40	-90		V	I _C =-10mA*	
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-7.5	-8.5		V	I _E =-100μA	
Collector Cut-Off Current	I _{CBO}			-100	nA	V _{CB} =-40V	
Emitter Cut-Off Current	I _{EBO}			-100	nA	V _{EB} =-6V	
Collector Emitter Cut-Off Current	I _{CES}			-100	nA	V _{CES} =-40V	
Collector-Emitter Saturation Voltage	V _{CE(sat)}		-10 -100 -45 -105 -130	-15 -125 -55 -130 -160	mV mV mV mV	I _C =-0.1A, I _B =-10mA* I _C =-1A, I _B =-20mA* I _C =-1A, I _B =-50mA* I _C =-4A, I _B =-400mA* I _C =-4A, I _B =-200mA*	
Base-Emitter Saturation Voltage	V _{BE(sat)}			-1.0	V	I _C =-4A, I _B =-200mA*	
Base-Emitter Turn-On Voltage	V _{BE(on)}			-0.85	V	I _C =-4A, V _{CE} =-2V*	
Static Forward Current Transfer Ratio	h _{FE}	300 300 200 50	500 450 380 100	900		I _C =-10mA, V _{CE} =-2V* I _C =-1A, V _{CE} =-2V* I _C =-4A, V _{CE} =-2V* I _C =-8A, V _{CE} =-2V*	
Transition Frequency	f _T		185		MHz	I _C =-300mA, V _{CE} =-10V f=-30MHz	
Output Capacitance	C _{obo}		95		pF	V _{CB} =-10V, f=1MHz	
Turn-On Time	t _(on)		130		ns	V _{CC} =-10V, I _C =-4A I _{B1} =I _{B2} =-100mA	
Turn-Off Time	t _(off)		435		ns		

^{*}Measured under pulsed conditions. Pulse width=300 μ s. Duty cycle $\leq 2\%$

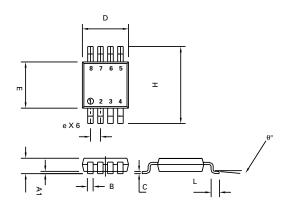


TYPICAL CHARACTERISTICS





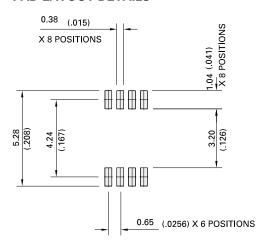
PACKAGE DIMENSIONS



Conforms to JEDEC MO-187 Iss A

DIM	Millimetres		Inches	
	MIN	MAX	MIN	MAX
А		1.10		0.043
A1	0.05	0.15	0.002	0.006
В	0.25	0.40	0.010	0.016
С	0.13	0.23	0.005	0.009
D	2.90	3.10	0.114	0.122
е	0.65	BSC	0.0256	BSC
E	2.90	3.10	0.114	0.122
Н	4.90	BSC	0.193	BSC
L	0.40	0.70	0.016	0.028
q°	0°	6°	0°	6°

PAD LAYOUT DETAILS





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