# imall

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Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





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**ZXTC2061E6** 

#### **12V COMPLEMENTARY MEDIUM POWER TRANSISTOR IN SOT26**

#### **Features**

- NPN + PNP Combination
- BV<sub>CEO</sub> > 12 (-12)V
- BV<sub>EBO</sub> > 7 (-7)V
- Continuous Collector Current I<sub>C</sub> = 5 (-3.5)A
- V<sub>CE(sat)</sub> < 32 (-70)mV @ 1A
- R<sub>CE(sat)</sub> = 25 (45)mΩ
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### Description

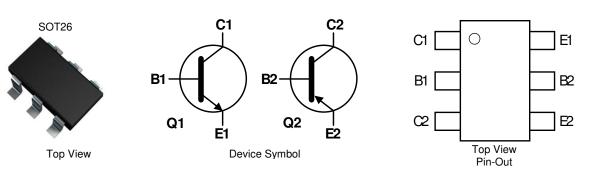
Advanced process capability has been used to achieve this high performance device. Combining NPN and PNP transistors in the SOT26 package provides a compact solution for the intended applications.

#### **Mechanical Data**

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 <sup>(3)</sup>
- Weight: 0.015 grams (Approximate)

#### **Applications**

- MOSFET and IGBT Gate Driving
- Motor Drive



#### Ordering Information (Note 4)

Product	Complianace	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTC2061E6TA	AEC-Q101	2061	7	8	3,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

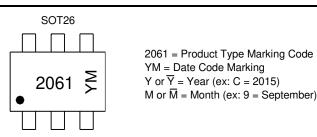
2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**

Notes:



Date Code	Key												
Year	201	5	2016	2017	2018	2019	2020	202	1 20	22	2023	2024	2025
Code	С		D	E	F	G	Н		,	J	K	L	М
Month	1 I	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	)	1	2	3	4	5	6	7	8	9	0	N	D



#### Absolute Maximum Ratings – Q1 (NPN Transistor) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	20	V
Collector-Emitter Voltage	V <sub>CEO</sub>	12	V
Emitter-Base Voltage	V <sub>EBO</sub>	7	V
Continuous Collector Current	lc	5	А
Peak Pulsed Collector Current	I <sub>CM</sub>	12	A
Base Current	IB	1	А

#### Absolute Maximum Ratings – Q2 (PNP Transistor) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-12	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-12	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7	V
Continuous Collector Current	lc	-3.5	A
Peak Pulsed Collector Current	I <sub>CM</sub>	-10	A
Base Current	Ι <sub>Β</sub>	-1	A

#### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
	(Notes 5 & 9)		0.7 5.6	
	(Notes 6 & 9)		0.9 7.2	
Power Dissipation Linear Derating Factor	(Notes 6 & 10)	PD	1.1 8.8	W mW/°C
	(Notes 7 & 9)		1.1 8.8	
	(Notes 8 & 9)		1.7 13.6	
	(Notes 5 & 9)		179	
	(Notes 6 & 9)	1	139	
Thermal Resistance, Junction to Ambient	(Notes 6 & 10)	R <sub>0JA</sub>	113	°C 111
	(Notes 7 & 9)		113	°C/W
	(Notes 8 & 9)		73	
Thermal Resistance, Junction to Lead	(Note 11)	R <sub>0JL</sub>	87.58	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

#### ESD Ratings (Note 12)

	<b>.</b>			
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	ЗA
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 5. For a device surface mounted on 15mm x 15mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured For a device surface mounted on 15mm x 15mm FH4 PCB with high coverage of single when operating in a steady-state condition.
Same as Note 5, except the device is surface mounted on 25mm x 25mm 1oz copper.
Same as Note 5, except the device is surface mounted on 50mm x 50mm 2oz copper.
Same as Note 7, except the device is measured at t < 5 seconds.</li>

9. For device with one active die, both collectors attached to a common heatsink.

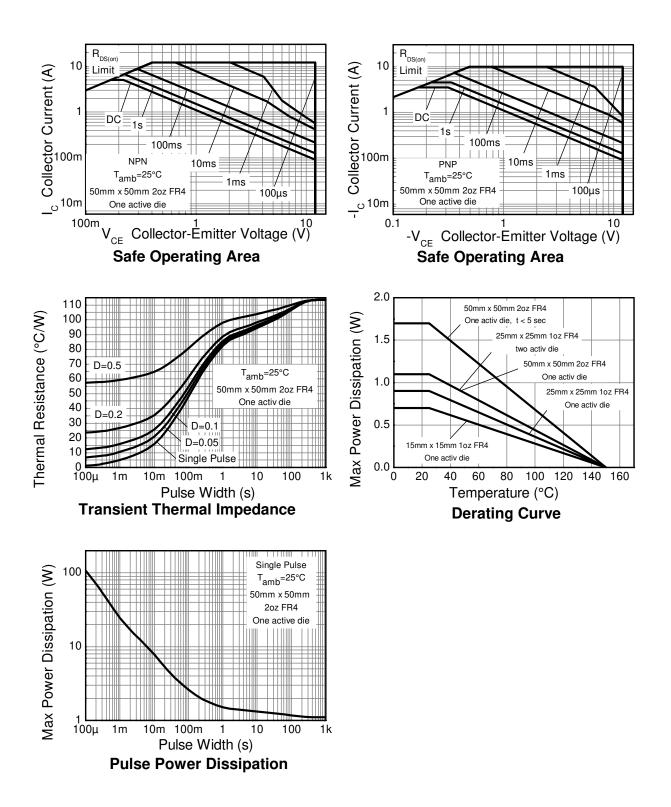
10. For device with two active dice running at equal power, split heatsink 50% to each collector.

11. Thermal resistance from junction to solder-point (at the end of the collector lead).

12. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



### **Thermal Characteristics and Derating Information**





#### Electrical Characteristics – Q1 (NPN Transistor) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BVCBO	20	40		V	$I_{C} = 100 \mu A, I_{E} = 0$
Collector-Emitter Breakdown Voltage (Note 13)	BV <sub>CEO</sub>	12	17	—	V	$I_{C} = 10mA, I_{B} = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	7	8.4	—	V	$I_{E} = 100 \mu A, I_{C} = 0$
Collector Cut-Off Current	I <sub>CBO</sub>	—	<1	50 0.5	nA μA	V <sub>CB</sub> = 20V V <sub>CB</sub> = 20V, T <sub>A</sub> = +100°C
Collector Cut-Off Current	I <sub>EBO</sub>	_	<1	50	nA	V <sub>EB</sub> = 5.6V
ON CHARACTERISTICS (Note 13)						
DC Current Gain	h <sub>FE</sub>	500 480 260	800 750 390	1,500	_	$\begin{split} & I_{C} = 10 \text{mA},  V_{CE} = 2 \text{V} \\ & I_{C} = 1.0 \text{A},  V_{CE} = 2 \text{V} \\ & I_{C} = 5 \text{A},  V_{CE} = 2 \text{V} \end{split}$
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	_	32 50 65 145	40 60 80 180	mV	$\begin{split} I_{C} &= 1.0A, \ I_{B} = 100 \text{mA} \\ I_{C} &= 1.0A, \ I_{B} = 10 \text{mA} \\ I_{C} &= 2.0A, \ I_{B} = 40 \text{mA} \\ I_{C} &= 5A, \ I_{B} = 100 \text{mA} \end{split}$
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	_	920	1,000	mV	I <sub>C</sub> = 5A, I <sub>B</sub> = 100mA
Base-Emitter Turn-On Voltage	V <sub>BE(on)</sub>	_	810	900	mV	$I_C = 5A, V_{CE} = 2V$
SMALL SIGNAL CHARACTERISTICS						•
Output Capacitance	Cobo		26	35	pF	V <sub>CB</sub> = 10V, f = 1.0MHz
Current Gain-Bandwidth Product	f⊤	_	260		MHz	V <sub>CE</sub> = 10V, I <sub>C</sub> = 50mA, f = 100MHz
Delay Time	t <sub>d</sub>		71		ns	
Rise Time	tr		70	_	ns	
Storage Time	ts	_	233		ns	$V_{CC} = 10V, I_C = 1A, I_{B1} = -I_{B2} = 10mA$
Fall Time	t <sub>f</sub>	—	72	—	ns	

# Electrical Characteristics – Q2 (PNP Transistor) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

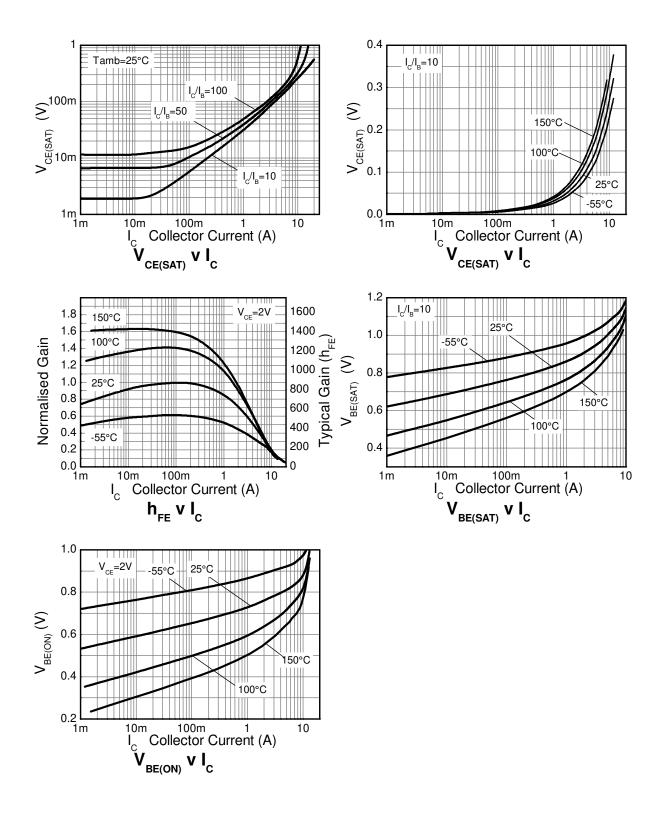
	1		I	1		
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	<b>BV</b> CBO	-12	-35	—	V	$I_{C} = -100 \mu A, I_{E} = 0$
Collector-Emitter Breakdown Voltage (Note 13)	BV <sub>CEO</sub>	-12	-25	_	V	$I_{C} = -10 \text{mA}, I_{B} = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-7	-8.4	_	V	$I_E = -100 \mu A, I_C = 0$
Collector Cut-Off Current	I <sub>CBO</sub>	—	< -1	-50	nA	V <sub>CB</sub> = -12V
	ICBO	_		-0.5	μA	$V_{CB} = -12V, T_A = +100^{\circ}C$
Collector Cut-Off Current	I <sub>EBO</sub>		< -1	-50	nA	V <sub>EB</sub> = -5.6V
ON CHARACTERISTICS (Note 13)						
		500	800	1500		$I_{C} = -10mA, V_{CE} = -2V$
DC Current Gain	h <sub>FE</sub>	290	450	—	—	$I_{C} = -1.0A, V_{CE} = -2V$
		75	100	—		$I_{C} = -3.5A, V_{CE} = -2V$
		—	-55	-70		$I_{\rm C} = -1.0$ A, $I_{\rm B} = -100$ mA
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	—	-170	-265	mV	I <sub>C</sub> = -1.0A, I <sub>B</sub> = -10mA
Conceter Emilier Caldration Voltage	V CE(sat)	—	-220	-360		$I_{\rm C} = -2.0$ A, $I_{\rm B} = -40$ mA
		—	-150	-200		I <sub>C</sub> = -3.5A, I <sub>B</sub> = -350mA
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	—	-955	-1,050	mV	$I_{C} = -3.5A, I_{B} = -350mA$
Base-Emitter Turn-On Voltage	V <sub>BE(on)</sub>	—	-830	-900	mV	$I_{C} = -3.5A, V_{CE} = -2V$
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	Cobo	_	17	25	рF	V <sub>CB</sub> = -10V, f = 1.0MHz
Current Gain-Bandwidth Product	fT	_	310	_	MHz	V <sub>CE</sub> = -10V, I <sub>C</sub> = -50mA, f = 100MHz
Delay Time	t <sub>d</sub>	_	41	—	ns	
Rise Time	t <sub>r</sub>	_	62	_	ns	$V_{CC} = -10V, I_C = -1A,$
Storage Time	ts	_	179	_	ns	$I_{B1} = -I_{B2} = -10mA$
Fall Time	t <sub>f</sub>	_	65	_	ns	

Note: 13. Measured under pulsed conditions. Pulse width  $\leq$  300µs. Duty cycle  $\leq$  2%.



### ZXTC2061E6

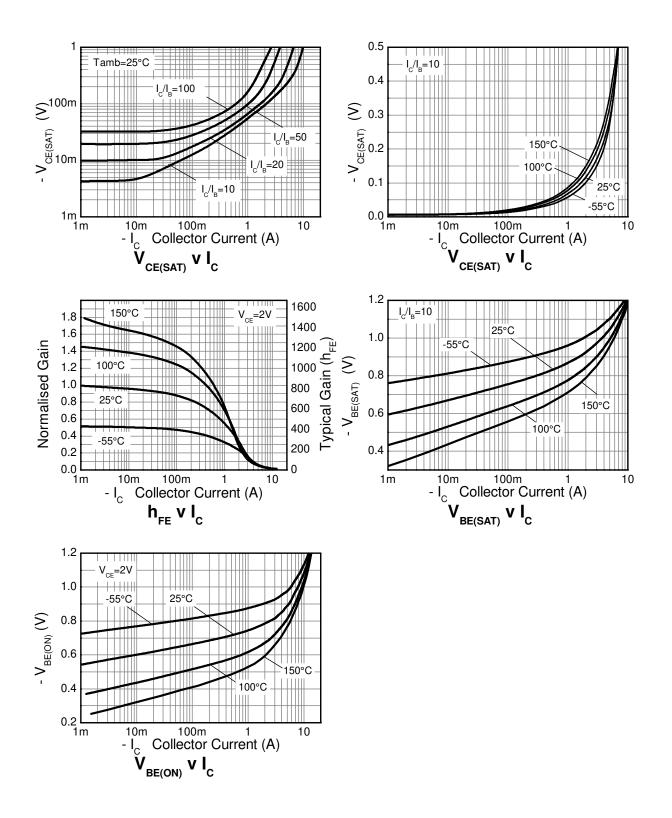
#### Typical Electrical Characteristics – Q1 (NPN Transistor) (@T<sub>A</sub> = +25°C, unless otherwise specified.)





### ZXTC2061E6

# Typical Electrical Characteristics – Q2 (PNP Transistor) (@T<sub>A</sub> = +25°C, unless otherwise specified.)





Тур

0.05

1.10

0.75

0.38

0.15

3.00

0.95

1.90

2.80

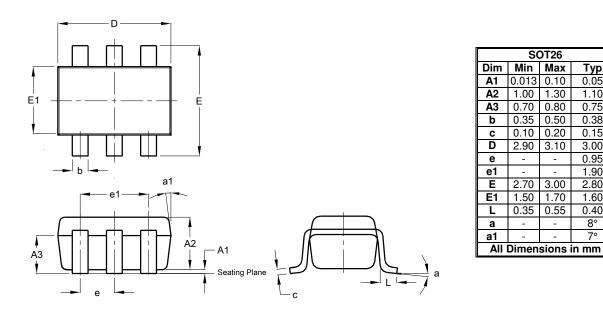
1.60

0.40 8°

7°

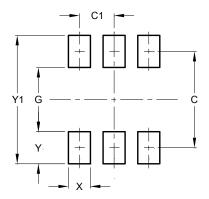
# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



#### **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20



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