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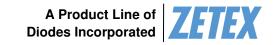












### 20V COMPLEMENTARY MEDIUM POWER TRANSISTORS IN SOT26

### **Features**

- NPN + PNP Combination
- BV<sub>CEO</sub> > 20 (-20)V
- BV<sub>EBO</sub> > 7 (-7)V
- Continuous Collector Current I<sub>C</sub> = 4 (-3.5)A
- V<sub>CE(sat)</sub> < 50 (-65)mV @ 1A</li>
- $R_{CE(sat)} = 35 (54) m\Omega$
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

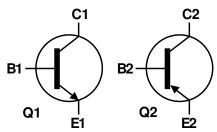
- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 <a>®</a>
- Weight: 0.015 grams (Approximate)

### **Applications**

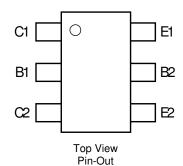
- MOSFET and IGBT Gate Driving
- Motor Drive







Device Symbol



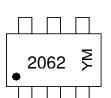
### Ordering Information (Note 4)

h				
Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTC2062E6TA	2062	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/ for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



SOT26

2062 = Product Type Marking Code YM = Date Code Marking

Y or  $\overline{Y}$  = Year (ex: C = 2015) M or  $\overline{M}$  = Month (ex: 9 = September)

Date Code Key

Year	2015	2	016	2017	2018	2019	2020	2021	20	22 2	2023	2024	2025
Code	С		D	Е	F	G	Н	I	·	J	K	L	М
Month	h	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	)	1	2	3	4	5	6	7	8	9	0	N	D





# Absolute Maximum Ratings - Q1 (NPN Transistor) (@T<sub>A</sub> = +25 ℃, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	100	V
Collector-Emitter Voltage	$V_{\sf CEO}$	20	V
Emitter-Collector Voltage (reverse blocking)	V <sub>ECO</sub>	5	V
Emitter-Base Voltage	$V_{EBO}$	7	V
Continuous Collector Current	Ic	4	Α
Peak Pulsed Collector Current	Ісм	10	Α
Base Current	lΒ	1	Α

## Absolute Maximum Ratings - Q2 (PNP Transistor) (@TA = +25 ℃, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-25	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-20	V
Emitter-Collector Voltage (reverse blocking)	V <sub>ECO</sub>	-4	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7	V
Continuous Collector Current	Ic	-3.5	Α
Peak Pulsed Collector Current	I <sub>CM</sub>	-10	Α
Base Current	I <sub>B</sub>	-1	Α

## Thermal Characteristics (@TA = +25 ℃, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
	(Notes 5 & 9)		0.7 5.6		
	(Notes 6 & 9)		0.9 7.2		
Power Dissipation Linear Derating Factor	(Notes 6 & 10)	$P_{D}$	1.1 8.8	W mW/℃	
-	(Notes 7 & 9)		1.1 8.8		
	(Notes 8 & 9)		1.7 13.6		
	(Notes 5 & 9)		179		
	(Notes 6 & 9)		139		
Thermal Resistance, Junction to Ambient	(Notes 6 & 10)	$R_{ hetaJA}$	113	22.444	
	(Notes 7 & 9)	**	113	°C/W	
	(Notes 8 & 9)		73		
Thermal Resistance, Junction to Lead	(Note 11)	$R_{ heta JL}$	87.5		
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C		

## ESD Ratings (Note 12)

Notes:

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

5. For a device surface mounted on 15mm x 15mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

<sup>6.</sup> Same as Note (5), except the device is surface mounted on 25mm x 25mm 1oz copper. 7. Same as Note (5), except the device is surface mounted on 50mm x 50mm 2oz copper.

<sup>8.</sup> Same as Note (7), except the device is measured at t < 5 seconds.

<sup>9.</sup> For device with one active die, both collectors attached to a common heatsink.

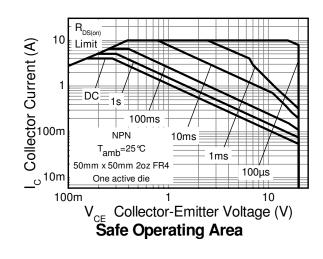
<sup>10.</sup> For device with two active dice running at equal power, split heatsink 50% to each collector.

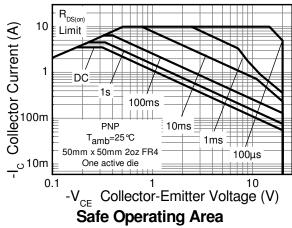
<sup>11.</sup> Thermal resistance from junction to solder-point (at the end of the collector lead).

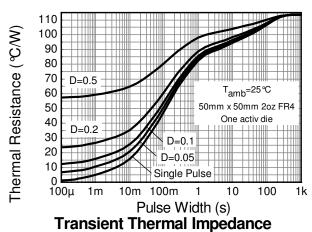
<sup>12.</sup> Refer to JEDEC specification JESD22-A114 and JESD22-A115.

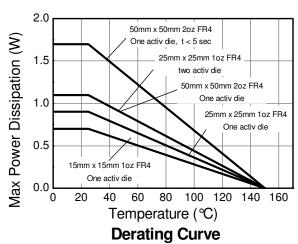


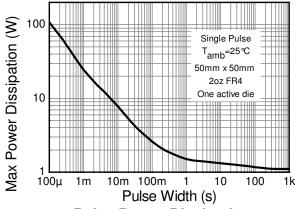
## **Thermal Characteristics and Derating Information**















# Electrical Characteristics - Q1 (NPN Transistor) (@T<sub>A</sub> = +25 ℃, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	100	140		V	$I_C = 100\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 13)	BV <sub>CEO</sub>	20	35	_	V	$I_C = 10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	7	8.3	_	V	$I_E = 100 \mu A, I_C = 0$
Emitter-Collector breakdown voltage (base open)	BV <sub>ECO</sub>	5	6		V	$I_E = 100\mu A$
Collector Cutoff Current	I <sub>CBO</sub>		<1	50 0.5	nΑ μΑ	V <sub>CB</sub> = 100V V <sub>CB</sub> = 100V, T <sub>A</sub> = +100 °C
Collector Cutoff Current	I <sub>EBO</sub>	_	<1	50	nA	V <sub>EB</sub> = 5.6V
ON CHARACTERISTICS (Note 13)						
DC Current Gain	h <sub>FE</sub>	300 280 140 —	450 420 210 15	900 — — —	_	I <sub>C</sub> = 10mA, V <sub>CE</sub> = 2V I <sub>C</sub> = 1A, V <sub>CE</sub> = 2V I <sub>C</sub> = 4A, V <sub>CE</sub> = 2V I <sub>C</sub> = 15A, V <sub>CE</sub> = 2V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	_	40 60 95 140	50 75 115 190	mV	$I_C = 1.0A$ , $I_B = 100mA$ $I_C = 1.0A$ , $I_B = 20mA$ $I_C = 2.0A$ , $I_B = 40mA$ $I_C = 4A$ , $I_B = 200mA$
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	_	940	1,050	mV	$I_C = 4A$ , $I_B = 200mA$
Base-Emitter Turn-On Voltage	V <sub>BE(on)</sub>	_	810	900	mV	$I_C = 4A$ , $V_{CE} = 2V$
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C <sub>obo</sub>	_	17	25	pF	V <sub>CB</sub> = 10V, f = 1.0MHz
Current Gain-Bandwidth Product	f <sub>T</sub>	_	215		MHz	$V_{CE} = 10V, I_{C} = 50mA, f = 100MHz$
Delay Time	t <sub>d</sub>	_	68	_	ns	
Rise Time	tr		72		ns	$V_{CC} = 10V$ , $I_{C} = 1A$ , $I_{B1} = -I_{B2} = 10mA$
Storage Time	ts		361	_	ns	$V_{CC} = 10V$ , $I_{C} = 1A$ , $I_{B1} = -I_{B2} = 10IIIA$
Fall Time	t <sub>f</sub>	_	64		ns	

Notes: 13. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.





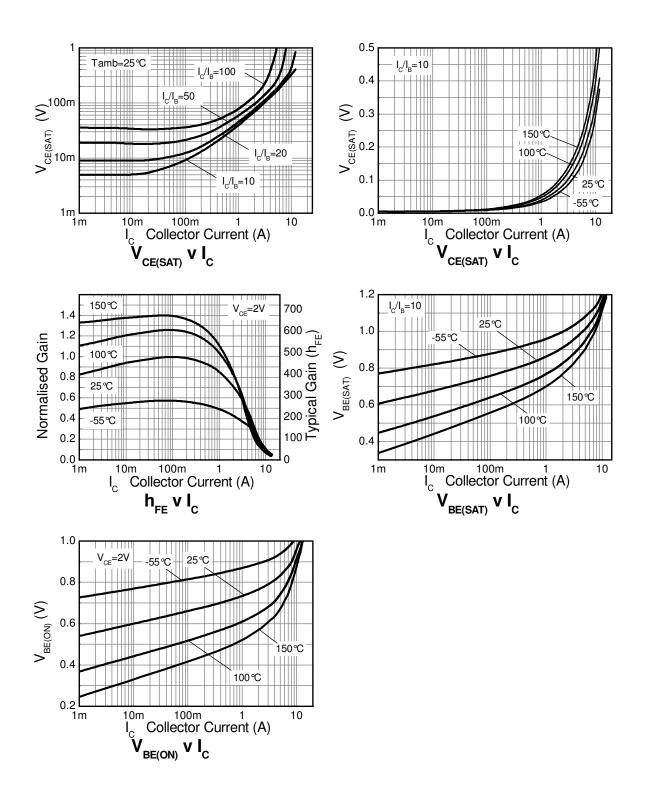
# Electrical Characteristics - Q2 (PNP Transistor) (@TA = +25℃, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	$BV_{CBO}$	-25	-55		V	$I_C = -100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 13)	$BV_CEO$	-20	-45		V	$I_C = -10mA$ , $I_B = 0$
Emitter-Base Breakdown Voltage	$BV_{EBO}$	-7	-8.3	_	V	$I_E = -100 \mu A, I_C = 0$
Collector Cutoff Current	I <sub>CBO</sub>	_	< -1	-50 -0.5	nA μA	V <sub>CB</sub> = -25V V <sub>CB</sub> = -25V, T <sub>A</sub> = +100°C
Collector Cutoff Current	I <sub>EBO</sub>	_	< -1	-50	nA	V <sub>EB</sub> = -5.6V
ON CHARACTERISTICS (Note 13)						
DC Current Gain	h <sub>FE</sub>	300 170 65 —	450 300 100 15	900 — — —	_	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -2V I <sub>C</sub> = -1.0A, V <sub>CE</sub> = -2V I <sub>C</sub> = -3.5A, V <sub>CE</sub> = -2V I <sub>C</sub> = -10A, V <sub>CE</sub> = -2V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	_ _ _	-55 -100 -185 -190	-65 -135 -280 -250	mV	I <sub>C</sub> = -1.0A, I <sub>B</sub> = -100mA I <sub>C</sub> = -1.0A, I <sub>B</sub> = -20mA I <sub>C</sub> = -2.0A, I <sub>B</sub> = -40mA I <sub>C</sub> = -3.5A, I <sub>B</sub> = -175mA
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	_	-925	-1,000	mV	$I_C = -3.5A$ , $I_B = -175mA$
Base-Emitter Turn-On Voltage	$V_{BE(on)}$	_	-835	-900	mV	$I_C = -3.5A$ , $V_{CE} = -2V$
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	$C_{ m obo}$	_	21	30	рF	V <sub>CB</sub> = -10V, f = 1.0MHz
Current Gain-Bandwidth Product	$f_T$	_	290		MHz	$V_{CE} = -10V$ , $I_{C} = -50mA$ , $f = 100MHz$
Delay Time	t <sub>d</sub>	_	56		ns	
Rise Time	t <sub>r</sub>	_	68		ns	$V_{CC} = -10V, I_C = -1A,$
Storage Time	ts	_	158		ns	$I_{B1} = -I_{B2} = -10 \text{mA}$
Fall Time	t <sub>f</sub>		59		ns	

Note: 13. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.

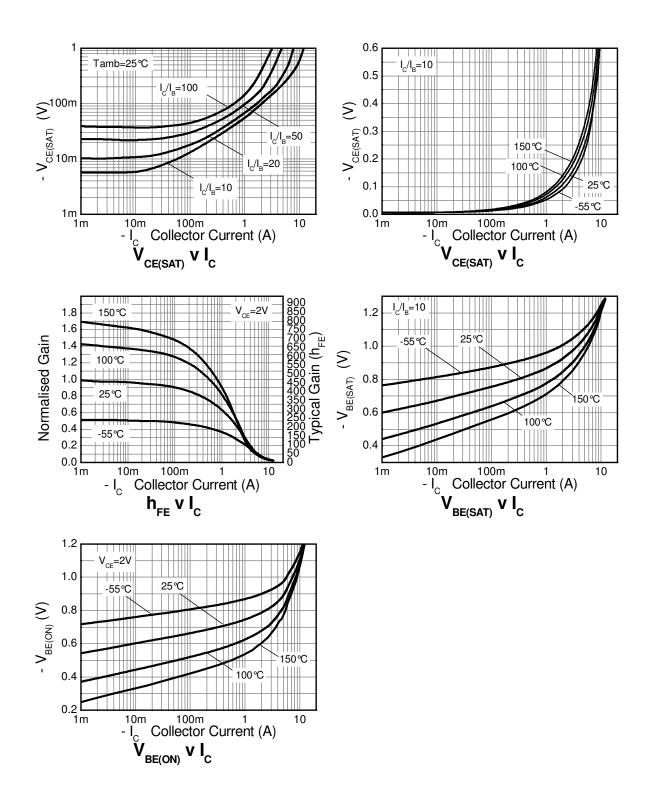


# Typical Electrical Characteristics - Q1 (NPN Transistor) (@T<sub>A</sub> = +25 ℃, unless otherwise specified.)





# Typical Electrical Characteristics - Q2 (PNP Transistor) (@T<sub>A</sub> = +25 ℃, unless otherwise specified.)

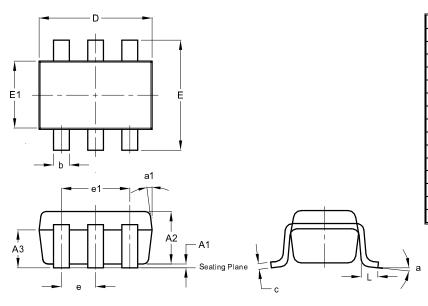




# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

### SOT26

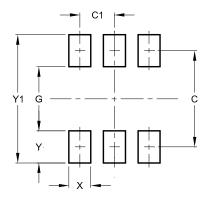


	SOT26						
Dim	Min	Max	Тур				
<b>A</b> 1	0.013	0.10	0.05				
A2	1.00	1.30	1.10				
А3	0.70	0.80	0.75				
b	0.35	0.50	0.38				
С	0.10	0.20	0.15				
D	2.90	3.10	3.00				
е	-	-	0.95				
e1	-	-	1.90				
Е	2.70	3.00	2.80				
E1	1.50	1.70	1.60				
L	0.35	0.55	0.40				
а	-	-	8°				
a1	-	-	7°				
All Dimensions in mm							

# **Suggested Pad Layout**

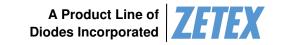
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

## SOT26



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Υ	0.80
V1	3.20





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